Dell Inc. PowerEdge XR12 (Intel Xeon Gold 6338T, 2.10 GHz)

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Apr-2021</th>
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<td>Jul-2021</td>
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**Threads**

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.5</th>
<th>SPECspeed®2017_int_peak = 11.8</th>
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<tr>
<td>Threads</td>
<td>24</td>
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<tr>
<td>perlbench_s</td>
<td>24</td>
</tr>
<tr>
<td>gcc_s</td>
<td>24</td>
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<tr>
<td>mcf_s</td>
<td>24</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>24</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>24</td>
</tr>
<tr>
<td>x264_s</td>
<td>24</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>24</td>
</tr>
<tr>
<td>leela_s</td>
<td>24</td>
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<tr>
<td>exchange2_s</td>
<td>24</td>
</tr>
<tr>
<td>xz_s</td>
<td>24</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 6338T
- **Max MHz:** 3400
- **Nominal:** 2100
- **Enabled:** 24 cores, 1 chip
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 36 MB I+D on chip per chip
- **Other:** None
- **Memory:** 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)
- **Storage:** 225 GB on tmpfs
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.15.1.el8_3.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 0.6.2 released Apr-2021
- **System State:** Run level 5 (graphical multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
# SPEC CPU® 2017 Integer Speed Result

## Dell Inc.

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## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>600.perlbench_s</td>
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<td>251</td>
<td>7.06</td>
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<tr>
<td>602.gcc_s</td>
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<td>605.mcf_s</td>
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<td>623.xalanchmk_s</td>
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### SPECspeed®2017_int_peak = 11.8

## Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.5-ic2021.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

## General Notes

- Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with: `sync; echo 3 > /proc/sys/vm/drop_caches`

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

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**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

**Platform Notes**

**BIOS Settings:**  
- Logical Processor : Disabled  
- Virtualization Technology : Disabled

- System Profile : Custom  
- CPU Power Management : Maximum Performance  
- C1E : Disabled  
- C States : Autonomous  
- Memory Patrol Scrub : Disabled  
- Energy Efficiency Policy : Performance  
- CPU Interconnect Bus Link  
- Power Management : Enabled

**Sysinfo program** /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo  
**Rev:** r6538 of 2020-09-24 e8664e6d2d7080afeaa89d4b38e2f1c  
running on localhost.localdomain Wed Apr 28 09:00:17 2021

**SUT (System Under Test) info as seen by some common utilities.**  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

**From /proc/cpuinfo**

- model name : Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz  
- 1 "physical id"s (chips)  
- 24 "processors"

- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  
  - cpu cores : 24  
  - siblings : 24  
  - physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23

**From lscpu:**

- Architecture: x86_64  
- CPU op-mode(s): 32-bit, 64-bit  
- Byte Order: Little Endian  
- CPU(s): 24

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge XR12 (Intel Xeon Gold 6338T, 2.10 GHz)

SPECspeed®2017_int_base = 11.5
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

On-line CPU(s) list: 0-23
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2529.223
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-23

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpesgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pae ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cmqm rdt_a avx512f avx512dq rdseed adx smap avx512sfma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaves cmqm_llc cmqm_occup_llc cmqm_mbb_total cmqm_mbb_local split_lock_detect wbnoinvd dtc flush_stl spl iat bni dpl sie mic pmct dtr ecall fpxr eaxr adx fprreg cpl pm zpt tm2 aarch64_a32 aarch64_a128 ts msr aarch64_c0 aarch64_c1 aarch64_c2 aarch64_c3 aarch64_c4 aarch64_c5 aarch64_c6 aarch64_c7

/proc/cpuinfo cache data

cache size : 36864 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 491444 MB
node 0 free: 498432 MB
node distances:
node 0
0: 10

From /proc/meminfo

MemTotal: 527817236 KB

(Continued on next page)
Dell Inc.

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SPEC CPU®2017 Integer Speed Result

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Apr-2021
Tested by: Dell Inc.
Hardware Availability: Jul-2021
Software Availability: Feb-2021

SPECspeed®2017_int_base = 11.5
SPECspeed®2017_int_peak = 11.8

Platform Notes (Continued)

HugePages_Total:       0
Hugepagesize:       2048 kB
/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
   NAME="Red Hat Enterprise Linux"
   VERSION="8.3 (Ootpa)"
   ID="rhel"
   ID_LIKE="fedora"
   VERSION_ID="8.3"
   PLATFORM_ID="platform:el8"
   PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
   ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
   Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Apr 28 08:55

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs   tmpfs 225G  7.0G  219G   4% /mnt/ramdisk

(Continued on next page)
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**Platform Notes (Continued)**

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge XR12
Product Family: PowerEdge
Serial: 0990104

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
5x 002C0632002C36ASF8G72PZ-3G2B2 64 GB 2 rank 3200
3x 00CE063200CE M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 0.6.2
BIOS Date: 04/12/2021
BIOS Revision: 0.6

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
-----------------------------------------------
C       | 600.perlbench_s(peak)
-----------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------
```

```
-----------------------------------------------
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
        | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----------------------------------------------
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------
```

```
-----------------------------------------------
C       | 600.perlbench_s(peak)
-----------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
```

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Compiler Version Notes (Continued)

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

Base Compiler Invocation

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
SPEC CPU®2017 Integer Speed Result

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Base Portability Flags (Continued)

602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort
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### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

**C benchmarks:**

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs  
-xCORE-AVX512 -flto -O3 -ffast-math  
-qopt-mem-layout-trans=4 -fno-alias  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

**C++ benchmarks:**

620.omnetpp_s: basepeak = yes

623.xalancbnk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

**Fortran benchmarks:**

648.exchange2_s: basepeak = yes

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Standard Performance Evaluation Corporation (info@spec.org)  
https://www.spec.org/
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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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Tested with SPEC CPU®2017 v1.1.5 on 2021-04-28 10:00:16-0400.
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