## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**
PowerEdge XR12 (Intel Xeon Gold 6312U, 2.40 GHz)

<table>
<thead>
<tr>
<th>Software</th>
<th>SPECspeed®2017_int_base = 12.1</th>
<th>SPECspeed®2017_int_peak = 12.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>55</td>
<td>Test Date: May-2021</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
<td>Hardware Availability: Jul-2021</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
<td>Software Availability: Feb-2021</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Gold 6312U</td>
</tr>
<tr>
<td>Max MHz:</td>
<td>3600</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled:</td>
<td>24 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>36 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>225 GB on tmpfs</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Software Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS:</td>
<td>Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.15.1.el8_3.x86_64</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 0.6.2 released Apr-2021</td>
</tr>
<tr>
<td>File System:</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 5 (graphical multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

### Performance Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>8.56</td>
<td>12.4</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>11.2</td>
<td>12.1</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>24</td>
<td>11.7</td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>11.8</td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>24</td>
<td>14.0</td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>24</td>
<td>17.6</td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>24</td>
<td>18.4</td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>24</td>
<td>19.9</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>24</td>
<td>21.6</td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>24</td>
<td>SPECspeed®2017_int_base (12.1)</td>
<td>SPECspeed®2017_int_peak (12.4)</td>
</tr>
</tbody>
</table>
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge XR12 (Intel Xeon Gold 6312U, 2.40 GHz)

SPECspeed®2017_int_base = 12.1
SPECspeed®2017_int_peak = 12.4

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>240</td>
<td>7.40</td>
<td>240</td>
<td>7.40</td>
<td>239</td>
<td>7.41</td>
<td>24</td>
<td>207</td>
<td>8.56</td>
<td>207</td>
<td>8.57</td>
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<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>355</td>
<td>11.2</td>
<td>355</td>
<td>11.2</td>
<td>355</td>
<td>11.2</td>
<td>24</td>
<td>342</td>
<td>11.6</td>
<td>340</td>
<td>11.7</td>
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<tr>
<td>605.mcf_s</td>
<td>24</td>
<td>226</td>
<td>20.8</td>
<td>225</td>
<td>20.9</td>
<td>227</td>
<td>20.8</td>
<td>24</td>
<td>226</td>
<td>20.8</td>
<td>225</td>
<td>20.9</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>138</td>
<td>11.8</td>
<td>139</td>
<td>11.8</td>
<td>138</td>
<td>11.8</td>
<td>24</td>
<td>138</td>
<td>11.8</td>
<td>139</td>
<td>11.8</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>24</td>
<td>102</td>
<td>14.0</td>
<td>101</td>
<td>14.0</td>
<td>102</td>
<td>14.0</td>
<td>24</td>
<td>102</td>
<td>14.0</td>
<td>101</td>
<td>14.0</td>
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<tr>
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<td>100</td>
<td>17.6</td>
<td>100</td>
<td>17.6</td>
<td>100</td>
<td>17.6</td>
<td>24</td>
<td>95.7</td>
<td>18.4</td>
<td>95.6</td>
<td>18.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc.

PowerEdge XR12 (Intel Xeon Gold 6312U, 2.40 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>12.1</td>
<td>12.4</td>
</tr>
</tbody>
</table>

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled

- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link
  - Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Tue May 4 05:39:41 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz
- 1 "physical id"s (chips)
- 24 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 24
  - siblings: 24
  - physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 24

(Continued on next page)
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SPECspeed®2017_int_base = 12.1
SPECspeed®2017_int_peak = 12.4

On-line CPU(s) list: 0-23
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz
Stepping: 6
CPU MHz: 3152.096
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-23

Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invhpc_single intel_pstate ssbd mba ibrs ibp ibrs_halted ibrs_enabled fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invvpid f16c rdrand lane11 fma9 macro_fission stdx rdtscp cpuid_fault

/cache data
size : 36864 KB

From numactl --hardware WARNING: a numa node might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 490610 MB
node 0 free: 498418 MB
node distances:
node 0:
0: 10

From /proc/meminfo
MemTotal: 527817236 kB
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_int_base = 12.1
SPECspeed®2017_int_peak = 12.4

Test Date:
Hardware Availability:
Software Availability:

Platform Notes (Continued)

HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
    Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 May 4 05:35

SPEC is set to: /mnt/ramdisk/cpu2017-int-1.1.5-ic2021.1

Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 7.0G 219G 4% /mnt/ramdisk

(Continued on next page)
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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Platform Notes (Continued)

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge XR12
Product Family: PowerEdge
Serial: 0990104

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
5x 002C0632002C 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200
3x 00CE063200CE M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 0.6.2
BIOS Date: 04/12/2021
BIOS Revision: 0.6

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(peak)
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)
(Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C | 600.perlbench_s(peak)
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
(Continued on next page)
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SPECspeed®2017_int_base = 12.1
SPECspeed®2017_int_peak = 12.4

Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---------------------------------------------------------------------
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
       | 625.x264_s(base, peak) 657.xz_s(base, peak)  
---------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
---------------------------------------------------------------------
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
       | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)  
---------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
---------------------------------------------------------------------
Fortran | 648.exchange2_s(base, peak)  
---------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
---------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)
## Base Portability Flags (Continued)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>602.gcc_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>-DSPEC_LP64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

## Base Optimization Flags

**C benchmarks:**

-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-03 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**

-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -03 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

**Fortran benchmarks:**

-03 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

## Peak Compiler Invocation

**C benchmarks (except as noted below):**

icx

600.perlbench_s: icc

**C++ benchmarks:**

icpx

**Fortran benchmarks:**

ifort
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SPECspeed®2017_int_peak = 12.4

CPU2017 License: 55
Test Date: May-2021
Test Sponsor: Dell Inc.
Hardware Availability: Jul-2021
Tested by: Dell Inc.
Software Availability: Feb-2021

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdatallopassex -xCORE-AVX512 -flto
-Otest(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

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**Software Availability:** Feb-2021

The flags files that were used to format this result can be browsed at:

- [Intel-ic2021-official-linux64_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)
- [Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.1.html](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.1.html)

You can also download the XML flags sources by saving the following links:

- [Intel-ic2021-official-linux64_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)
- [Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.1.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.1.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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