### Dell Inc.

**PowerEdge XR11** (Intel Xeon Silver 4310T, 2.30 GHz)

**SPECspeed®2017 int_base = 10.4**

**SPECspeed®2017 int_peak = 10.6**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Red Hat Enterprise Linux 8.3 (Ootpa)</td>
<td>CPU Name: Intel Xeon Silver 4310T</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
<td>Max MHz: 3400</td>
</tr>
<tr>
<td>Firmware: Version 0.9.0 released May-2021</td>
<td>Nominal: 2300</td>
</tr>
<tr>
<td>File System: tmpfs</td>
<td>Enabled: 10 cores, 1 chip</td>
</tr>
<tr>
<td>System State: Run level 5 (graphical multi-user)</td>
<td>Orderable: 1 chip</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
<td>L3: 15 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** May-2021

**Hardware Availability:** Jul-2021

**Software Availability:** Feb-2021

---

#### SPEC CPU®2017 Integer Speed Result

<table>
<thead>
<tr>
<th>Test</th>
<th>SPECspeed®2017 int_base</th>
<th>SPECspeed®2017 int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>6.83</td>
<td>7.82</td>
</tr>
<tr>
<td>gcc_s</td>
<td>9.92</td>
<td>10.3</td>
</tr>
<tr>
<td>mcf_s</td>
<td>7.94</td>
<td>10.3</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>13.2</td>
<td>16.4</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>17.1</td>
<td>17.1</td>
</tr>
<tr>
<td>x264_s</td>
<td>18.8</td>
<td>18.8</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>5.77</td>
<td>13.1</td>
</tr>
<tr>
<td>leela_s</td>
<td>4.70</td>
<td>13.1</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>19.6</td>
<td>19.6</td>
</tr>
<tr>
<td>xz_s</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

| Threads | 0 | 1.00 | 2.00 | 3.00 | 4.00 | 5.00 | 6.00 | 7.00 | 8.00 | 9.00 | 10.0 | 11.0 | 12.0 | 13.0 | 14.0 | 15.0 | 16.0 | 17.0 | 18.0 | 19.0 | 20.0 |
|---------|----|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| perlbench_s | 6.83 | | | | | | | | | | | | | | | | | | | | | |
| gcc_s | 7.82 | | | | | | | | | | | | | | | | | | | | | |
| mcf_s | | | | | | | | | | | | | | | | | | | | | | |
| omnetpp_s | | | | | | | | | | | | | | | | | | | | | | |
| xalancbmk_s | | | | | | | | | | | | | | | | | | | | | | |
| x264_s | | | | | | | | | | | | | | | | | | | | | | |
| deepsjeng_s | | | | | | | | | | | | | | | | | | | | | | |
| leela_s | | | | | | | | | | | | | | | | | | | | | | |
| exchange2_s | | | | | | | | | | | | | | | | | | | | | | |
| xz_s | | | | | | | | | | | | | | | | | | | | | | |

---

**Test Date:** May-2021

**Hardware Availability:** Jul-2021

**Software Availability:** Feb-2021

---

**CPU Name:** Intel Xeon Silver 4310T

**Max MHz:** 3400

**Nominal:** 2300

**Enabled:** 10 cores, 1 chip

**Orderable:** 1 chip

**Cache L1:** 32 KB I + 48 KB D on chip per core

**L2:** 1.25 MB I+D on chip per core

**L3:** 15 MB I+D on chip per chip

**Other:** None

**Memory:** 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)

**Storage:** 225 GB on tmpfs

**Other:** None
Dell Inc.

PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)

SPECspeed®2017_int_base = 10.4

SPECspeed®2017_int_peak = 10.6

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>10</td>
<td>260</td>
<td>6.84</td>
<td>261</td>
<td>6.80</td>
<td>260</td>
<td>6.83</td>
<td>10</td>
<td>227</td>
<td>7.83</td>
<td>227</td>
<td>7.82</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>10</td>
<td>401</td>
<td>9.92</td>
<td>401</td>
<td>9.92</td>
<td>401</td>
<td>9.92</td>
<td>10</td>
<td>388</td>
<td>10.3</td>
<td>386</td>
<td>10.3</td>
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<tr>
<td>605.mcf_s</td>
<td>10</td>
<td>241</td>
<td>19.6</td>
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<td>19.6</td>
<td>241</td>
<td>19.6</td>
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<td>19.6</td>
<td>240</td>
<td>19.6</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>10</td>
<td>205</td>
<td>7.94</td>
<td>201</td>
<td>8.11</td>
<td>207</td>
<td>7.90</td>
<td>10</td>
<td>205</td>
<td>7.94</td>
<td>201</td>
<td>8.11</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>10</td>
<td>107</td>
<td>16.4</td>
<td>108</td>
<td>16.4</td>
<td>108</td>
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<td>103</td>
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<td>17.1</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
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<td>5.77</td>
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<td>5.77</td>
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<td>5.76</td>
<td>10</td>
<td>248</td>
<td>5.77</td>
<td>248</td>
<td>5.77</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>10</td>
<td>363</td>
<td>4.70</td>
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<td>4.72</td>
<td>363</td>
<td>4.70</td>
<td>10</td>
<td>363</td>
<td>4.70</td>
<td>362</td>
<td>4.72</td>
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<tr>
<td>642.exchange2_s</td>
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<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>10</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
</tr>
</tbody>
</table>

SPECspeed®2017_int_base = 10.4
SPECspeed®2017_int_peak = 10.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = ":/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files System page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)

SPECspeed®2017_int_base = 10.4
SPECspeed®2017_int_peak = 10.6

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2021
Tested by: Dell Inc.
Hardware Availability: Jul-2021
Software Availability: Feb-2021

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri May 14 03:09:54 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
  1 "physical id"s (chips)
  10 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 5 6 7 8 9

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 10

(Continued on next page)
Dell Inc.

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CPU2017 License: 55
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Platform Notes (Continued)

On-line CPU(s) list: 0–9
Thread(s) per core: 1
Core(s) per socket: 10
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
Stepping: 6
CPU MHz: 1664.924
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 15360K
NUMA node0 CPU(s): 0–9
Flags: fpu vme de pse tsc msr pae mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_pppin ssbd mba ibrs ibpb stibp ibrsenhanced fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaves cmq_llc cmq_occup_llc
cmq_mbb_total cmq_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vmbi umip pkup ospke avx512_vmbi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq 1a57 rdpid md_clear pconfig flush_l1d
arch_capabilities

/proc/cpuinfo cache data
   cache size : 15363 KB

From numactl --hardware WARNING: a numaclt 'node' might or might not correspond to a
physical chip.
   available: 1 nodes (0)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9
   node 0 size: 508149 MB
   node 0 free: 498908 MB
   node distances:
     node 0
     0: 10

From /proc/meminfo
   MemTotal: 527820280 KB

(Continued on next page)
Dell Inc.

PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)

SPEC CPU®2017 Integer Speed Result

SPECspeed®2017_int_base = 10.4
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2021
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Hardware Availability: Jul-2021
Software Availability: Feb-2021

Platform Notes (Continued)

HugePages_Total:       0
Hugepagesize:       2048 kB
/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.3 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.3"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
  Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 14:03:06

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1

Filesystem  Type  Size  Used Avail Use% Mounted on
tmpfs     tmpfs   225G   6.9G   219G   4% /mnt/ramdisk

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)

| SPECspeed®2017_int_base = 10.4 |
| SPECspeed®2017_int_peak = 10.6 |

| CPU2017 License: | 55 |
| Test Date: | May-2021 |
| Test Sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |
| Hardware Availability: | Jul-2021 |
| Software Availability: | Feb-2021 |

Platform Notes (Continued)

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge XR11
Product Family: PowerEdge
Serial: 09A000N

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
2x 002C0632002C 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200, configured at 2666
2x 002C069D002C 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200, configured at 2666
1x 00AD063200AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200, configured at 2666
3x 00CE063200CE M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 0.9.0
BIOS Date: 05/10/2021
BIOS Revision: 0.9

Compiler Version Notes

C | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 600.perlbench_s(peak)

(Continued on next page)
Dell Inc.  
PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)  

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CPU2017 License:  55  
Test Sponsor:  Dell Inc.  
Tested by:  Dell Inc.  

Test Date:  May-2021  
Hardware Availability:  Jul-2021  
Software Availability:  Feb-2021

Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  

C  | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)  

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  

C++  | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)  

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  

Fortran  | 648.exchange2_s(base, peak)  

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:  
icx  

C++ benchmarks:  
icpx  

Fortran benchmarks:  
ifort
**Dell Inc.**

**PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)**

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<tr>
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**Test Sponsor:** Dell Inc.  
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### Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
625.x264_s: -DSPEC_LP64  
631.deepsjeng_s: -DSPEC_LP64  
641.leela_s: -DSPEC_LP64  
648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512  
- -O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
- -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**
- -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
- -mbranches-within-32B-boundaries  
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/  
- -lqkmalloc

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
- -nostandard-realloc-lhs -align array32byte -auto  
- -mbranches-within-32B-boundaries

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

icx

600.perlbench_s: icc

**C++ benchmarks:**

icpx
Dell Inc.

PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)

SPECspeak®2017_int_base = 10.4
SPECspeak®2017_int_peak = 10.6

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Jul-2021
Software Availability: Feb-2021

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

(Continued on next page)
Dell Inc.

PowerEdge XR11 (Intel Xeon Silver 4310T, 2.30 GHz)  

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CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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