Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL110 Gen10 Plus
(2.40 GHz, Intel Xeon Gold 6312U)

SPECspeed®2017_int_base = 12.0
SPECspeed®2017_int_peak = 12.3

HPE

CPU2017 License: 3
Test Sponsor: HPE
Test Date: Jun-2021
Software Availability: Dec-2020
Hardware Availability: Jun-2021

Tested by: HPE
Software Availability: Dec-2020
Hardware Availability: Jun-2021

Threads

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base (12.0)</th>
<th>SPECspeed®2017_int_peak (12.3)</th>
</tr>
</thead>
</table>

**CPU Name:** Intel Xeon Gold 6312U

**Max MHz:** 3600
**Nominal:** 2400
**Enabled:** 24 cores, 1 chip
**Orderable:** 1 chip
**Cache L1:** 32 KB I + 48 KB D on chip per core
**L2:** 1.25 MB I+D on chip per core
**L3:** 36 MB I+D on chip per chip
**Other:** None

**Memory:** 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)
**Storage:** 1 x 480 GB NVMe SSD, RAID 0
**Other:** None

**OS:** Red Hat Enterprise Linux 8.3 (Ootpa)

**Compiler:**
- C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
- Fortran: Version 2021.1 of Intel Fortran Compiler
- C/C++: Version 2021.1 of Intel C/C++ Compiler

**Parallel:** Yes
**Firmware:** HPE BIOS Version U56 v1.50 05/13/2021 released May-2021
**File System:** xfs
**System State:** Run level 3 (multi-user)
**Base Pointers:** 64-bit
**Peak Pointers:** 64-bit
**Other:** jemalloc memory allocator V5.0.1
**Power Management:** BIOS set to prefer performance at the cost of additional power usage
## SPEC CPU®2017 Integer Speed Result

Hewlett Packard Enterprise  
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ProLiant DL110 Gen10 Plus  
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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>240</td>
<td>7.39</td>
<td>240</td>
<td>7.39</td>
<td>241</td>
<td>7.36</td>
<td>24</td>
<td>210</td>
<td>8.44</td>
<td>210</td>
<td>8.45</td>
<td>210</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>360</td>
<td>11.1</td>
<td>363</td>
<td>11.0</td>
<td>363</td>
<td>11.0</td>
<td>24</td>
<td>350</td>
<td>11.4</td>
<td>348</td>
<td>11.5</td>
<td>350</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>24</td>
<td>228</td>
<td>20.7</td>
<td>232</td>
<td>20.3</td>
<td>228</td>
<td>20.7</td>
<td>24</td>
<td>228</td>
<td>20.7</td>
<td>232</td>
<td>20.3</td>
<td>228</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>135</td>
<td>12.1</td>
<td>136</td>
<td>12.0</td>
<td>135</td>
<td>12.1</td>
<td>24</td>
<td>135</td>
<td>12.1</td>
<td>136</td>
<td>12.0</td>
<td>135</td>
</tr>
<tr>
<td>632.xalanchmk_s</td>
<td>24</td>
<td>102</td>
<td>13.8</td>
<td>103</td>
<td>13.8</td>
<td>103</td>
<td>13.8</td>
<td>24</td>
<td>102</td>
<td>13.8</td>
<td>103</td>
<td>13.8</td>
<td>103</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>24</td>
<td>101</td>
<td>17.5</td>
<td>101</td>
<td>17.5</td>
<td>101</td>
<td>17.5</td>
<td>24</td>
<td>96.6</td>
<td>18.3</td>
<td>96.7</td>
<td>18.2</td>
<td>96.5</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>24</td>
<td>235</td>
<td>6.11</td>
<td>234</td>
<td>6.11</td>
<td>234</td>
<td>6.11</td>
<td>24</td>
<td>235</td>
<td>6.11</td>
<td>234</td>
<td>6.11</td>
<td>234</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>24</td>
<td>289</td>
<td>21.4</td>
<td>289</td>
<td>21.4</td>
<td>289</td>
<td>21.4</td>
<td>24</td>
<td>289</td>
<td>21.4</td>
<td>289</td>
<td>21.4</td>
<td>289</td>
</tr>
</tbody>
</table>

**Specspeed®2017_int_base = 12.0**  
**Specspeed®2017_int_peak = 12.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:  
  
  `sync; echo 3 > /proc/sys/vm/drop_caches`

### Environment Variables Notes

- Environment variables set by runcpu before the start of the run:  
  - KMP_AFFINITY = "granularity=fine,scatter"
  - LD_LIBRARY_PATH = "/cpu2017/lib/intel64:/cpu2017/je5.0.1-64"
  - MALLOC_CONF = "retain:true"
  - OMP_STACKSIZE = "192M"

### General Notes

- Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
- NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
- jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>12.0</td>
<td>12.3</td>
</tr>
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</table>

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jun-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Dec-2020

---

**General Notes (Continued)**


Submitted by: "Bhatnagar, Prateek" <prateek.bhatnagar@hpe.com>

Submitted: Mon Jul 5 08:00:46 EDT 2021  
Submission: cpu2017-20210705-27733.sub

---

**Platform Notes**

The system ROM used for this result contains Intel microcode version 0xd0002a0 for the Intel Xeon Gold 6312U processor.

**BIOS Configuration:**
- Workload Profile set to General Peak Frequency Compute
- Intel Hyper-Threading set to Disabled
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- Advanced Memory Protection set to Advanced ECC
- Last Level Cache (LLC) Prefetch set to Enabled
- Last Level Cache (LLC) Dead Line Allocation set to Disabled
- Enhanced Processor Performance set to Enabled
- Workload Profile set to Custom
  - Energy/Performance Bias set to Balanced Power
  - DCU Stream Prefetcher set to Disabled
  - Adjacent Sector Prefetch set to Disabled
  - Minimum Processor Idle Power Package C-State set to No Package State
  - Numa Group Size Optimization set to Flat

Sysinfo program /cpu2017/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Wed Jun 23 01:30:52 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

<table>
<thead>
<tr>
<th>model name</th>
<th>Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>&quot;physical id&quot;s (chips)</td>
</tr>
<tr>
<td>24</td>
<td>&quot;processors&quot;</td>
</tr>
<tr>
<td>cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)</td>
<td></td>
</tr>
<tr>
<td>cpu cores</td>
<td>24</td>
</tr>
<tr>
<td>siblings</td>
<td>24</td>
</tr>
<tr>
<td>physical 0</td>
<td>cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23</td>
</tr>
</tbody>
</table>

From lscpu from util-linux 2.32.1:

| Architecture | x86_64 |

(Continued on next page)
SPEC CPU® 2017 Integer Speed Result

Hewlett Packard Enterprise
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SPECspeed® 2017_int_base = 12.0
SPECspeed® 2017_int_peak = 12.3

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Dec-2020

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 24
On-line CPU(s) list: 0-23
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz
Stepping: 6
CPU MHz: 3419.821
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node 0 CPU(s): 0-23

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcd cda sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512ifne smap cqmுavx512sfma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaves xsaveopt xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local split_lock_detect wbnoiwvd dtherm ida arat pni pts avx512vbm1 umip pku ospke avx512_vbmi2 gfni vaes vpcm1ldqavx512_avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size: 36864 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 489905 MB
node 0 free: 514078 MB
node distances:
  node 0
  0: 10

(Continued on next page)
## Platform Notes (Continued)

From `/proc/meminfo`
- MemTotal: 528053716 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/sbin/tuned-adm active
- Current active profile: throughput-performance

From `/etc/*release*` /`/etc/*version*`
- os-release:
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.3 (Ootpa)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="8.3"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  - ANSI_COLOR="0;31"
- redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
- system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
- Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
- x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

<table>
<thead>
<tr>
<th>CVE-2018-12207 (iTLB Multihit)</th>
<th>Not affected</th>
</tr>
</thead>
<tbody>
<tr>
<td>CVE-2018-3620 (L1 Terminal Fault)</td>
<td>Not affected</td>
</tr>
<tr>
<td>Microarchitectural Data Sampling</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2017-5754 (Meltdown)</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2018-3639 (Speculative Store Bypass)</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td>CVE-2017-5753 (Spectre variant 1)</td>
<td>Mitigation: usercopy/swaps barriers and __user pointer sanitization</td>
</tr>
<tr>
<td>CVE-2017-5715 (Spectre variant 2)</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
<tr>
<td>CVE-2020-0543 (Special Register Buffer Data Sampling)</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2019-11135 (TSX Asynchronous Abort)</td>
<td>Not affected</td>
</tr>
</tbody>
</table>

run-level 3 Jun 23 01:25

SPEC is set to: /cpu2017

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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<table>
<thead>
<tr>
<th>CPU2017 License: 3</th>
<th>Test Date:</th>
<th>Jun-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>HPE</td>
<td></td>
</tr>
<tr>
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<td>HPE</td>
<td></td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/nvme1n1p4</td>
<td>xfs</td>
<td>442G</td>
<td>138G</td>
<td>305G</td>
<td>32%</td>
<td>/</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id
Vendor:         HPE
Product:        ProLiant DL110 Gen10 Plus
Product Family: ProLiant
Serial:         T912P0032

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200

BIOS:
BIOS Vendor:     HPE
BIOS Version:    U56
BIOS Date:       05/13/2021
BIOS Revision:   1.50
Firmware Revision: 2.40

(End of data from sysinfo program)

**Compiler Version Notes**

```
C    | 600.perlbench_s(peak)
```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C    | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C    | 600.perlbench_s(peak)
```

(Continued on next page)
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Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort
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<td>Dec-2020</td>
</tr>
</tbody>
</table>

#### Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

#### Base Optimization Flags

**C benchmarks:**
- -DSPEC_OPENMP -std=c11 -m64 -fioopenmp -Wl,-z,muldefs -xCORE-AVX512
- -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**
- -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
- -lqkmalloc

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -mbranches-within-32B-boundaries

#### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icx

- 600.perlibench_s: icc

**C++ benchmarks:**
- icpx

(Continued on next page)
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CPU2017 License: 3
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Test Date: Jun-2021
Tested by: HPE
Hardware Availability: Jun-2021
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:
  ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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(Test Sponsor: HPE)
ProLiant DL110 Gen10 Plus
(2.40 GHz, Intel Xeon Gold 6312U)

SPECspeed®2017_int_base = 12.0
SPECspeed®2017_int_peak = 12.3

CPU2017 License: 3
Test Sponsor: HPE
Test Date: Jun-2021
Tested by: HPE
Hardware Availability: Jun-2021
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revE.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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