# SPEC CPU®2017 Integer Speed Result

## New H3C Technologies Co., Ltd.

H3C UniServer B5700 G5 (Intel Xeon Silver 4310)

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>New H3C Technologies Co., Ltd.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>CPU2017 License:</td>
<td>9066</td>
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<tr>
<td>Test Date:</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

### Software

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)
  - 4.18.0-193.el8.x86_64
- **Compiler:**
  - C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
    - Compiler Build 20201113 for Linux
  - Fortran: Version 2021.1 of Intel Fortran Compiler
    - Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 5.23 released Apr-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

### Hardware

- **CPU Name:** Intel Xeon Silver 4310
- **Max MHz:** 3300
- **Nominal:** 2100
- **Enabled:** 24 cores, 2 chips
- **Orderable:** 1.2 Chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 18 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)
- **Storage:** 6.4 TB SSD NVME
- **Other:** None

### Threads

<table>
<thead>
<tr>
<th>Name</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<tbody>
<tr>
<td>perlbench_s</td>
<td>24</td>
<td>6.74</td>
<td>7.74</td>
</tr>
<tr>
<td>gcc_s</td>
<td>24</td>
<td>9.96</td>
<td>10.3</td>
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<tr>
<td>mcf_s</td>
<td>24</td>
<td>8.04</td>
<td>10.3</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>24</td>
<td>8.04</td>
<td>10.3</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>24</td>
<td>12.7</td>
<td>15.8</td>
</tr>
<tr>
<td>x264_s</td>
<td>24</td>
<td>5.61</td>
<td>15.8</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>24</td>
<td>4.58</td>
<td>16.5</td>
</tr>
<tr>
<td>leela_s</td>
<td>24</td>
<td>18.2</td>
<td>16.5</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>24</td>
<td></td>
<td>19.8</td>
</tr>
<tr>
<td>xz_s</td>
<td>24</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

600.perlbench_s: 24 Threads, SPECspeed®2017_int_base = 10.7, SPECspeed®2017_int_peak = 10.9
New H3C Technologies Co., Ltd.  
H3C UniServer B5700 G5 (Intel Xeon Silver 4310)  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>24</td>
<td>265</td>
<td>6.70</td>
<td>263</td>
<td>6.74</td>
<td>262</td>
<td>6.77</td>
<td>24</td>
<td>229</td>
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<tr>
<td>602.gcc_s</td>
<td>24</td>
<td>400</td>
<td>9.95</td>
<td>399</td>
<td>9.97</td>
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<td>9.96</td>
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<td>250</td>
<td>18.9</td>
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<tr>
<td>620.omnetpp_s</td>
<td>24</td>
<td>203</td>
<td>12.8</td>
<td>111</td>
<td>12.7</td>
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<td>203</td>
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<td>623.xalanchmk_s</td>
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<td>625.x264_s</td>
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<td>5.61</td>
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<td>641.leela_s</td>
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<td>4.58</td>
<td>24</td>
<td>373</td>
<td>4.58</td>
<td>373</td>
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<tr>
<td>648.exchange2_s</td>
<td>24</td>
<td>162</td>
<td>18.2</td>
<td>161</td>
<td>18.2</td>
<td>161</td>
<td>18.2</td>
<td>24</td>
<td>162</td>
<td>18.2</td>
<td>161</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>24</td>
<td>312</td>
<td>19.8</td>
<td>312</td>
<td>19.8</td>
<td>312</td>
<td>19.8</td>
<td>24</td>
<td>312</td>
<td>19.8</td>
<td>312</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited".

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
- jemalloc, a general purpose malloc implementation
  - built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

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New H3C Technologies Co., Ltd. H3C UniServer B5700 G5 (Intel Xeon Silver 4310)

SPECspeed®2017_int_base = 10.7
SPECspeed®2017_int_peak = 10.9

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Jul-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Apr-2021
Software Availability: Dec-2020

General Notes (Continued)


Platform Notes

BIOS Settings:
Set Hyper-Threading to disabled
Set Patrol Scrub to disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6c64d
running on localhost.localdomain Sun Jul  4 22:33:48 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4310 CPU @ 2.10GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 12
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11

From lscpu from util-linux 2.32.1:
Architecture:        x86_64
CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              24
On-line CPU(s) list: 0-23
Thread(s) per core:  1
Core(s) per socket:  12
Socket(s):           2
NUMA node(s):        2
Vendor ID:           GenuineIntel
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Silver 4310 CPU @ 2.10GHz
Stepping:            6
CPU MHz:             2878.887
CPU max MHz:         3300.0000
CPU min MHz:         800.0000
BogoMIPS:            4200.00

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New H3C Technologies Co., Ltd.

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SPEC CPU®2017 Integer Speed Result

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Test Date: Jul-2021
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SPECspeed®2017_int_base = 10.7
SPECspeed®2017_int_peak = 10.9

Platform Notes (Continued)

Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 18432K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
Flags: fpu vme de pse tsc msr pae mce cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrn pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmx flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsaveopt xsavec xsetbv1 xsavec cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req avx512vbm umip pku ospke avx512_vbmi2 gfn vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d
arch_capabilities

/proc/cpuinfo cache data
  cache size : 18432 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
    node 0 size: 515427 MB
    node 0 free: 514410 MB
    node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
    node 1 size: 516062 MB
    node 1 free: 515121 MB
    node distances:
      node 0 1
        0: 10 20
        1: 20 10

From /proc/meminfo
  MemTotal: 1056246172 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

/sbin/tuned-adm active
  Current active profile: throughput-performance

(Continued on next page)
Platform Notes (Continued)

/vendor/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

os-release:
   NAME="Red Hat Enterprise Linux"
   VERSION="8.2 (Ootpa)"
   ID=rhel
   ID_LIKE="fedora"
   VERSION_ID="8.2"
   PLATFORM_ID="platform:el8"
   PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
   ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
   Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
run-level 3 Jul 4 22:31 last=5

SPEC is set to: /home/spec/cpu
   Filesystem Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs  5.8T  131G  5.7T  3%  /home

From /sys/devices/virtual/dmi/id
   Vendor: New H3C Technologies Co., Ltd.
   Product: B5700 G5

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CPU2017 License: 9066  
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Tested by: New H3C Technologies Co., Ltd.

Test Date: Jul-2021  
Hardware Availability: Apr-2021  
Software Availability: Dec-2020

Platform Notes (Continued)

Product Family: Rack  
Serial: 210235A3W9H212000017

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Hynix HMAA8GR7CJR4N-XN 64 GB 2 rank 3200, configured at 2666
16x NO DIMM NO DIMM

BIOS:
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 5.23
BIOS Date: 04/23/2021
BIOS Revision: 5.21

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 600.perlbench_s(peak)
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SPECspeed®2017_int_base = 10.7
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CPU2017 License: 9066
Test Date: July 2021
Test Sponsor: New H3C Technologies Co., Ltd.
Hardware Availability: April 2021
Tested by: New H3C Technologies Co., Ltd.
Software Availability: December 2020

Compiler Version Notes (Continued)

==============================================================================
<p>| C     | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)       |</p>
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<tr>
<th></th>
<th>625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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------------------------------------------------------------------------------

==============================================================================
<p>| C++   | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)                    |</p>
<table>
<thead>
<tr>
<th></th>
<th>631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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<table>
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<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed®2017_int_base = 10.7
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### Base Portability Flags (Continued)

- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
- -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**
- -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
- -lqkmalloc

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -mbranches-within-32B-boundaries

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icx
- 600.perlbench_s: icc

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort
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<td>Jul-2021</td>
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**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Jul-2021

**Hardware Availability:** Apr-2021

**Software Availability:** Dec-2020

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

#### C benchmarks:

- `600.perlbench_s`: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)`
- `-xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -fno-strict-overflow`
- `-mbranches-within-32B-boundaries`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

- `602.gcc_s`: `-m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)`
- `-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto`
- `-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

- `605.mcf_s`: `basepeak = yes`

- `625.x264_s`: `-DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs`
- `-xCORE-AVX512 -flto -O3 -ffast-math`
- `-qopt-mem-layout-trans=4 -fno-alias`
- `-mbranches-within-32B-boundaries`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

- `657.xz_s`: `basepeak = yes`

#### C++ benchmarks:

- `620.omnetpp_s`: `basepeak = yes`

- `623.xalancbmk_s`: `basepeak = yes`

- `631.deepsjeng_s`: `basepeak = yes`

- `641.leela_s`: `basepeak = yes`

#### Fortran benchmarks:

- `648.exchange2_s`: `basepeak = yes`
## SPEC CPU®2017 Integer Speed Result

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<table>
<thead>
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<td>10.9</td>
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**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Jul-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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