# SPEC CPU®2017 Floating Point Rate Result

**Lenovo Global Technology**  
ThinkSystem SD630 V2  
(3.00 GHz, Intel Xeon Gold 5317)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base = 224</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>48</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>48</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>48</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>48</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>48</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>48</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>48</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>48</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>48</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>48</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>48</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>48</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>48</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 5317  
- **Max MHz:** 3600  
- **Nominal:** 3000  
- **Enabled:** 24 cores, 2 chips, 2 threads/core  
- **Orderable:** 2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 18 MB I+D on chip per chip  
- **Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)  
- **Storage:** 1 x 480 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
- **Kernel:** 4.18.0-240.el8.x86_64  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
- **Parallel:** No  
- **Firmware:** Lenovo BIOS Version USE111A 1.02 released May-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage

**Test Date:** Jun-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020
# SPEC CPU®2017 Floating Point Rate Result

**Lenovo Global Technology**  
ThinkSystem SD630 V2  
(3.00 GHz, Intel Xeon Gold 5317)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Test Sponsor:</th>
<th>Tested by:</th>
</tr>
</thead>
<tbody>
<tr>
<td>9017</td>
<td>Lenovo Global Technology</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

**Test Date:** Jun-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>48</td>
<td>882</td>
<td>546</td>
<td>881</td>
<td>546</td>
<td>881</td>
<td>546</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>48</td>
<td>214</td>
<td>284</td>
<td>215</td>
<td>283</td>
<td>215</td>
<td>283</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>48</td>
<td>304</td>
<td>150</td>
<td>304</td>
<td>150</td>
<td>303</td>
<td>150</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>48</td>
<td>1008</td>
<td>125</td>
<td>1006</td>
<td>125</td>
<td>1010</td>
<td>124</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>48</td>
<td>497</td>
<td>226</td>
<td>494</td>
<td>227</td>
<td>492</td>
<td>228</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>48</td>
<td>285</td>
<td>177</td>
<td>287</td>
<td>176</td>
<td>287</td>
<td>176</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>48</td>
<td>513</td>
<td>210</td>
<td>506</td>
<td>212</td>
<td>487</td>
<td>221</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>48</td>
<td>357</td>
<td>205</td>
<td>356</td>
<td>205</td>
<td>357</td>
<td>205</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>48</td>
<td>406</td>
<td>207</td>
<td>406</td>
<td>207</td>
<td>401</td>
<td>209</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>48</td>
<td>222</td>
<td>539</td>
<td>221</td>
<td>539</td>
<td>221</td>
<td>540</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>48</td>
<td>233</td>
<td>347</td>
<td>234</td>
<td>346</td>
<td>233</td>
<td>347</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>48</td>
<td>1057</td>
<td>177</td>
<td>1058</td>
<td>177</td>
<td>1057</td>
<td>177</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>48</td>
<td>741</td>
<td>103</td>
<td>740</td>
<td>103</td>
<td>741</td>
<td>103</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 224**  
**SPECrate®2017_fp_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.8-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.8-ic202
1.1-revB/j65.0.1-64"
MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Red Hat Enterprise Linux 8.1  
Transparent Huge Pages enabled by default  

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrated®2017_fp_base = 224
SPECrated®2017_fp_peak = Not Run

Test Date: Jun-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disabled
SNC set to Enabled
Adjacent Cache Prefetch set to Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d
running on localhost.localdomain Wed Jun 23 11:04:24 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5317 CPU @ 3.00GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

SPECrate®2017_fp_base = 224
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2021
Tested by: Lenovo Global Technology
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5317 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3400.000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 18432K
NUMA node0 CPU(s): 0-5,24-29
NUMA node1 CPU(s): 6-11,30-35
NUMA node2 CPU(s): 12-17,36-41
NUMA node3 CPU(s): 18-23,42-47
Flags: fpu vm de pse tsc msr pae mce cx8 apic sep mthr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdxrd ldm abhi lahl msbs 3monwprefetch cpuid_fault epb cat_13 invpcid_single intel_pinn
ssbd mba ibrs ibpp stib stbps enhanced ipc ipcpd vnmi flexpriority ept vpd ept_ad
fsgsbase tsc_deadline myl hle avx2 smep bmi2 msre invpcid cmq rdt_a avx512f avx512dq
rdseed adm smap axx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
axx512vl xsaveopt xsaves xsaveopt xsaves cmq_llc cmq_occmap LLVM LLVM mm_total
cmq_mmb_ho_mmap split_lock_detect wboinvd dtherm ida arat pln pts avx512vbmi umip pku
ospke avx512_vmbi2 gfn vaes vpcimulqdq avx512_vnni avx512_bitalg tme
axx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 18432 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 24 25 26 27 28 29
node 0 size: 127114 MB
node 0 free: 128146 MB
node 1 cpus: 6 7 8 9 10 11 30 31 32 33 34 35
node 1 size: 127552 MB

(Continued on next page)
Platform Notes (Continued)

node 1 free: 128281 MB
node 2 cpus: 12 13 14 15 16 17 36 37 38 39 40 41
node 2 size: 127444 MB
node 2 free: 128697 MB
node 3 cpus: 18 19 20 21 22 23 42 43 44 45 46 47
node 3 size: 127610 MB
node 3 free: 128749 MB
node distances:
  node   0   1   2   3
  0:  10  11  20  20
  1:  11  10  20  20
  2:  20  20  10  11
  3:  20  20  11  10

From /proc/meminfo
  MemTotal:   528010044 kB
  HugePages_Total:       0
  Hugepagesize:     2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
o-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.3 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.3"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

SPECrade®2017_fp_base = 224
SPECrade®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

CPE-2017-5753 (Spectre variant 1):
Bypass disabled via prctl and seccomp
Mitigation: usercopy/swaps barriers and __user pointer sanitization

CPE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CPE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CPE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 23 11:00
SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 372G 73G 300G 20% /home

From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SD630 V2
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: Lenovo
BIOS Version: U8E111A-1.02
BIOS Date: 05/07/2021
BIOS Revision: 1.2
Firmware Revision: 1.40

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>C</th>
<th>519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

SPECrater®2017_fp_base = 224
SPECrater®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Jun-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)

------------------------------------------------------------------------------------------
C++                  | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------
C++, C               | 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------
C++, C, Fortran      | 507.cactuBSSN_r(base)
------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------
Fortran               | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------
Fortran, C            | 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
(Continued on next page)
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

SPECrate®2017_fp_base = 224  
Test Date: Jun-2021  
Hardware Availability: Jul-2021

SPECrate®2017_fp_peak = Not Run  
Software Availability: Dec-2020

Compiler Version Notes (Continued)

Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
Lenovo Global Technology
ThinkSystem SD630 V2
(3.00 GHz, Intel Xeon Gold 5317)

SPECraten2017.fp_base = 224
SPECraten2017.fp_peak = Not Run

Base Optimization Flags

C benchmarks:
- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries -ljemalloc
- /usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
- -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries -ljemalloc
- /usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
- -qopt-prefetch -ffinite-math-only
- -qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -mbranches-within-32B-boundaries -ljemalloc
- /usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
- -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
- -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-multiple-gather-scatter-by-shuffles
- -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
- -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries -ljemalloc
- /usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
- -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
- -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-multiple-gather-scatter-by-shuffles
- -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.html
**SPEC CPU®2017 Floating Point Rate Result**

---

### Lenovo Global Technology

**ThinkSystem SD630 V2**  
(3.00 GHz, Intel Xeon Gold 5317)  

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>224</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

---

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Jun-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

---

You can also download the XML flags sources by saving the following links:


---

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-06-23 11:04:24-0400.  
Originally published on 2021-07-20.