### Lenovo Global Technology

**ThinkSystem SR630 V2**  
(2.80 GHz, Intel Xeon Platinum 8362)

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<tbody>
<tr>
<td>SPECrate®2017_fp_peak = Not Run</td>
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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jun-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020

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<td>526.blender_r 64</td>
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<td>527.cam4_r 64</td>
<td>538.imagick_r 64</td>
</tr>
<tr>
<td>544.nab_r 64</td>
<td>549.fotonik3d_r 64</td>
</tr>
<tr>
<td>554.roms_r 64</td>
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</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Platinum 8362  
- **Max MHz:** 3600  
- **Nominal:** 2800  
- **Enabled:** 64 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 48 MB I+D on chip per chip  
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

#### Software

- **OS:** SUSE Linux Enterprise Server 15 SP2 (x86_64)  
  Kernel 5.3.18-22-default  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
- **Parallel:** No  
- **Firmware:** Lenovo BIOS Version AFE111A 1.02 released May-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
Results Table

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</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
MALLOCONF = "retain: true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM memory using openSUSE Leap 15.2
Transparent Huge Pages enabled by default

(Continued on next page)
General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
`sync; echo 3 > /proc/sys/vm/drop_caches`
runcpu command invoked through numactl i.e.:
`numactl --interleave=all runcpu <etc>`
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
MONITOR/MWAIT set to Enabled
Hyper-Threading set to Disabled
Intel Virtualization Technology set to Disabled
SNC set to Enabled
XPT Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revA-update1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acac64d
running on localhost Tue Jun 22 03:57:45 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
  siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
```
Lenovo Global Technology
ThinkSystem SR630 V2
(2.80 GHz, Intel Xeon Platinum 8362)

SPECrate®2017_fp_base =  441
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2021
Tested by: Lenovo Global Technology
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

From lscpu from util-linux 2.33.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 57 bits virtual
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
Stepping: 6
CPU MHz: 3485.477
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
NUMA node2 CPU(s): 32-47
NUMA node3 CPU(s): 48-63
Flags: fpu vme de pse tsc msr pae mce cmov pipis mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrm pdcm pdc daca se41_sse4_2_x2apic movbe popcnt tsc_deadline_timer aes xsave
avxf6c rrdand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd
ma ibs ibpb stibp ibrs_enhanced tpr_shadow vmmi flexpriority ept vpid ept_ad
fsbgbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
avx512dq rseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni
avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsavechat cqm_mbb avx512_mbb
xlc cqm_mbb_local wbinvd dtherm ida arat pln pt avx512vmbi umip pkp ospke
avx512_vbmi gfn vaer vpclmulqdq avx512_vnni avx512_vitalg tme avx512_vpopcntdq
la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)

(Continued on next page)
Lenovo Global Technology
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SPECrater®2017_fp_base = 441
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CPU2017 License: 9017
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Platform Notes (Continued)

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 257634 MB
dnode 0 free: 257043 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 258010 MB
node 1 free: 257444 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 2 size: 258043 MB
node 2 free: 257560 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 258040 MB
node 3 free: 257531 MB
node distances:
node 0: 10 11 20 20
node 1: 11 10 20 20
node 2: 20 20 10 11
node 3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056491172 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 15 SP2

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15-SP2"
VERSION_ID="15.2"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected

(Continued on next page)
### Lenovo Global Technology

ThinkSystem SR630 V2  
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#### Platform Notes (Continued)

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<tbody>
<tr>
<td>CVE-2018-3639 (Speculative Store Bypass):</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td>CVE-2017-5753 (Spectre variant 1):</td>
<td>Mitigation: usercopy/swapgs barriers and __user pointer sanitization</td>
</tr>
<tr>
<td>CVE-2017-5715 (Spectre variant 2):</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
<tr>
<td>CVE-2020-0543 (Special Register Buffer Data Sampling):</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2019-11135 (TSX Asynchronous Abort):</td>
<td>Not affected</td>
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</table>

run-level 3 Jun 21 22:09

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revA-update1

From /sys/devices/virtual/dmi/id

Vendor: Lenovo  
Product: ThinkSystem SR630 V2 MB  
Product Family: ThinkSystem  
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
- 32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:  
- BIOS Vendor: Lenovo  
- BIOS Version: AFE111A-1.02  
- BIOS Date: 05/07/2021  
- BIOS Revision: 1.2  
- Firmware Revision: 1.10

(End of data from sysinfo program)

#### Compiler Version Notes

| C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base) |
|-----------------------------------------------|

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  

(Continued on next page)
Lenovo Global Technology
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SPECrater®2017_fp_base = 441
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CPU2017 License: 9017
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Hardware Availability: Jul-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)

Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++ | 508.namd_r(base) 510.parest_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C++, C | 511.povray_r(base) 526.blender_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

Lenovo Global Technology
ThinkSystem SR630 V2
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SPECrate®2017_fp_base = 441
SPECrate®2017_fp_peak = Not Run

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**  
Copyright 2017-2021 Standard Performance Evaluation Corporation

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**Base Portability Flags (Continued)**

554.roms_r: -DSPEC_LP64

---

**Base Optimization Flags**

**C benchmarks:**
- `-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`  
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries -ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

**C++ benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries -ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Fortran benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`  
- `-qopt-prefetch -ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs -align array32byte -auto`  
- `-mbranches-within-32B-boundaries -ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both Fortran and C:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`  
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo`  
- `-no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs`  
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both C and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`  
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries -ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using Fortran, C, and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`  
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3`  
- `-no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs`  
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`

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# Lenovo Global Technology

ThinkSystem SR630 V2  
(2.80 GHz, Intel Xeon Platinum 8362)

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Test Date:** Jun-2021  
**Tested by:** Lenovo Global Technology  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2020  
**Test Sponsor:** Lenovo Global Technology

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

- `align array32byte`  
- `auto`  
- `ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at:

- [Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.html)  
- [Intel-ic2021-official-linux64_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

- [Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.xml)  
- [Intel-ic2021-official-linux64_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

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Tested with SPEC CPU®2017 v1.1.8 on 2021-06-21 15:57:45-0400.  
Report generated on 2021-07-21 15:50:02 by CPU2017 PDF formatter v6442.  
Originally published on 2021-07-20.