SPEC CPU®2017 Floating Point Rate Result

Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 5318N)

SPECr®2017_fp_base = 321
SPECr®2017_fp_peak = 335

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation
Test Date: Jul-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECr®2017_fp_base</th>
<th>SPECr®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>96</td>
<td>503.bwaves_r</td>
<td>623</td>
</tr>
<tr>
<td>48</td>
<td>507.cactuBSSN_r</td>
<td>623</td>
</tr>
<tr>
<td>96</td>
<td>508.namd_r</td>
<td>464</td>
</tr>
<tr>
<td>168</td>
<td>510.parest_r</td>
<td>364</td>
</tr>
<tr>
<td>214</td>
<td>511.povray_r</td>
<td>419</td>
</tr>
<tr>
<td>96</td>
<td>519.lbm_r</td>
<td>228</td>
</tr>
<tr>
<td>288</td>
<td>521.wrf_r</td>
<td>283</td>
</tr>
<tr>
<td>96</td>
<td>526.blender_r</td>
<td>339</td>
</tr>
<tr>
<td>96</td>
<td>527.cam4_r</td>
<td>346</td>
</tr>
<tr>
<td>96</td>
<td>538.imagick_r</td>
<td>833</td>
</tr>
<tr>
<td>96</td>
<td>544.nab_r</td>
<td>562</td>
</tr>
<tr>
<td>96</td>
<td>549.fotonik3d_r</td>
<td>585</td>
</tr>
<tr>
<td>96</td>
<td>554.roms_r</td>
<td>155</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Gold 5318N
Max MHz: 3400
Nominal: 2100
Enabled: 48 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 36 MB I+D on chip per core
Other: None
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R, running at 2666)
Storage: 1 x 4 TB NVME SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
           C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux;
           Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux
Parallel: No
Firmware: Version 05.00.00 released Apr-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.


**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
<td>1545</td>
<td>623</td>
<td>1546</td>
<td>623</td>
<td>1547</td>
<td>622</td>
<td>48</td>
<td>772</td>
<td>623</td>
<td>772</td>
<td>624</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>96</td>
<td>262</td>
<td>464</td>
<td>263</td>
<td>463</td>
<td>262</td>
<td>464</td>
<td>96</td>
<td>262</td>
<td>464</td>
<td>263</td>
<td>463</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>96</td>
<td>377</td>
<td>242</td>
<td>377</td>
<td>242</td>
<td>377</td>
<td>242</td>
<td>96</td>
<td>377</td>
<td>242</td>
<td>377</td>
<td>242</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>96</td>
<td>1491</td>
<td>168</td>
<td>1491</td>
<td>168</td>
<td>1494</td>
<td>168</td>
<td>48</td>
<td>586</td>
<td>214</td>
<td>585</td>
<td>214</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>96</td>
<td>615</td>
<td>364</td>
<td>615</td>
<td>364</td>
<td>615</td>
<td>364</td>
<td>96</td>
<td>532</td>
<td>421</td>
<td>535</td>
<td>419</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>443</td>
<td>228</td>
<td>443</td>
<td>228</td>
<td>445</td>
<td>227</td>
<td>96</td>
<td>443</td>
<td>228</td>
<td>443</td>
<td>228</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>96</td>
<td>751</td>
<td>286</td>
<td>747</td>
<td>288</td>
<td>744</td>
<td>289</td>
<td>48</td>
<td>380</td>
<td>283</td>
<td>380</td>
<td>283</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>96</td>
<td>431</td>
<td>339</td>
<td>431</td>
<td>339</td>
<td>432</td>
<td>339</td>
<td>96</td>
<td>431</td>
<td>339</td>
<td>431</td>
<td>339</td>
</tr>
<tr>
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<td>96</td>
<td>485</td>
<td>346</td>
<td>486</td>
<td>346</td>
<td>483</td>
<td>347</td>
<td>96</td>
<td>485</td>
<td>346</td>
<td>486</td>
<td>346</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>96</td>
<td>286</td>
<td>332</td>
<td>287</td>
<td>333</td>
<td>288</td>
<td>332</td>
<td>96</td>
<td>286</td>
<td>332</td>
<td>287</td>
<td>332</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>96</td>
<td>287</td>
<td>562</td>
<td>287</td>
<td>562</td>
<td>290</td>
<td>557</td>
<td>96</td>
<td>285</td>
<td>566</td>
<td>287</td>
<td>563</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
<td>1930</td>
<td>194</td>
<td>1932</td>
<td>194</td>
<td>1933</td>
<td>194</td>
<td>96</td>
<td>1930</td>
<td>194</td>
<td>1932</td>
<td>194</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>96</td>
<td>1177</td>
<td>130</td>
<td>1174</td>
<td>130</td>
<td>1171</td>
<td>130</td>
<td>48</td>
<td>493</td>
<td>155</td>
<td>492</td>
<td>155</td>
</tr>
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**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GOVERNOR set to Performance

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOCONF = "retain:true"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
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General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.


Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable
Sub NUMA Cluster (SNC) set to Enable
Intel Hyper Threading Technology set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d running on localhost.localdomain Tue Jul 20 19:06:28 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5318N CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
Platform Notes (Continued)

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5318N CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2699.849
CPU max MHz: 3400.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdseed avx2 smep bmi2 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibs
ibpb ibrs enhanced tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust
bm1 hle avx2 smep bmi2 erms invpcid rtm cmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt
xsaves xgetbv1 xsavec cmq_llc cmq_occup llc cmq_mbm_total cmq_mbm_local wbnoinvd
dtc dtherm ida arat pln pts avx512vbm1 umip pkup ospeke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d
arch_capabilities

/proc/cpuinfo cache data
cache size: 36864 KB

From numactl --hardware

(Continued on next page)
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Platform Notes (Continued)

WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 18 22 23 30 31 32 33 34 35 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 257637 MB
node 0 free: 245326 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 67 68 69 70 71
node 1 size: 258015 MB
node 1 free: 248071 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
node 2 size: 258042 MB
node 2 free: 248159 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 258040 MB
node 3 free: 248135 MB
node distances:
node 0 1 2 3
0: 11 11 20 20
1: 11 11 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056496696 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*

os-release:
NAME=\"Red Hat Enterprise Linux\"
VERSION=\"8.2 (Ootpa)\"
ID=\"rhel\"
ID_LIKE=\"fedora\"
VERSION_ID=\"8.2\"
PLATFORM_ID=\"platform:el8\"
PRETTY_NAME=\"Red Hat Enterprise Linux 8.2 (Ootpa)\"
ANSI_COLOR=\"0;31\"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:

(Continued on next page)
Platform Notes (Continued)

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jul 20 08:44

SPEC is set to: /home/CPU2017
From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5180M6
Product Family: Family
Serial: 380827124

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Micron 36ASF4G72PZ-3G2R1 32 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 05.00.00
BIOS Date: 04/25/2021
BIOS Revision: 5.22

(End of data from sysinfo program)
## SPEC CPU®2017 Floating Point Rate Result

### Inspur Corporation

**Inspur NF5180M6 (Intel Xeon Gold 5318N)**

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_peak</th>
<th>SPECrate®2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>335</td>
<td>321</td>
</tr>
</tbody>
</table>

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### Compiler Version Notes

<table>
<thead>
<tr>
<th>C</th>
<th>519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)</th>
</tr>
</thead>
</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
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<p>| | |
| | |</p>
<table>
<thead>
<tr>
<th>C++</th>
<th>508.namd_r(base, peak) 510.parest_r(base, peak)</th>
</tr>
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</table>
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<p>| | |
| | |</p>
<table>
<thead>
<tr>
<th>C++, C</th>
<th>511.povray_r(peak)</th>
</tr>
</thead>
</table>
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<table>
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<th>511.povray_r(base) 526.blender_r(base, peak)</th>
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</tbody>
</table>

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<th>507.cactuBSSN_r(base, peak)</th>
</tr>
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CPU2017 License: 3358  
Test Sponsor: Inspur Corporation  
Tested by: Inspur Corporation

Compiler Version Notes (Continued)

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Base Compiler Invocation

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

**Inspur Corporation**

**Inspur NF5180M6 (Intel Xeon Gold 5318N)**

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<tr>
<th>SPECrate®2017_fp_base</th>
<th>321</th>
</tr>
</thead>
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</tr>
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**Base Compiler Invocation (Continued)**

Benchmarks using both Fortran and C:

ifort icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifort

**Base Portability Flags**

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-fltio -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -fltio
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only

(Continued on next page)
Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc
-/-usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-/-usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
521.wrf_r: ifort icx
Inspur Corporation

Inspur NF5180M6 (Intel Xeon Gold 5318N)

SPECrate®2017_fp_base = 321
SPECrate®2017_fp_peak = 335

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Jul-2021
Tested by: Inspur Corporation
Hardware Availability: May-2021
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

527.cam4_r: ifort icx

Benchmarks using both C and C++:
511.povray_r: icpc icc
526.blender_r: icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes

C++ benchmarks:
508.namd_r: basepeak = yes

Fortran benchmarks:
503.bwaves_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-prefetch -ffinite-math-only

(Continued on next page)
Peak Optimization Flags (Continued)

503.bwaves_r (continued):
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-ALIGN array32byte -auto -mbranches-within-32B-boundsaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

549.fotonik3d_r: basepeak = yes

554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundsaries
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundsaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.0.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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