**SPEC CPU®2017 Integer Speed Result**

xFusion

xFusion 2288H V6 (Intel Xeon Silver 4310T)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.0</th>
</tr>
</thead>
</table>

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion  
**Test Date:** Apr-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2021

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_peak = Not Run</th>
</tr>
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</table>

<table>
<thead>
<tr>
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<tr>
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<td>623.xalancbmk_s 20</td>
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<td>625.x264_s 20</td>
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<td>641.leela_s 20</td>
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<tr>
<td>648.exchange2_s 20</td>
<td>5.84</td>
</tr>
<tr>
<td>657.xz_s 20</td>
<td>4.72</td>
</tr>
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</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4310T  
- **Max MHz:** 3400  
- **Nominal:** 2300  
- **Enabled:** 20 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **Cache L2:** 1.25 MB I+D on chip per core  
- **Cache L3:** 15 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2666)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux release 8.4 (Ootpa)  
  4.18.0-305.el8.x86_64  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler  
  Classic Build 20201112 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 0.95 Released Dec-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
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Test Date: Apr-2022  
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Results Table

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</table>

SPECspeed®2017_int_base = 11.0  
SPECspeed®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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Test Sponsor: xFusion
Tested by: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

General Notes (Continued)

Platform Notes

BIOS configuration:
Performance Profile Set to Load Balance
Hyper-Threading Set to Disabled

Sysinfo program /spec2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aaca64d
running on localhost.localdomain Fri Apr 15 09:52:55 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
  2 "physical id"s (chips)
  20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 5 6 7 8 9
physical 1: cores 0 1 2 3 4 5 6 7 8 9

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 20
On-line CPU(s) list: 0-19
Thread(s) per core: 1
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
BIOS Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
Stepping: 6
CPU MHz: 1695.273
CPU max MHz: 2301.0000

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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Specspeed®2017_int_base = 11.0
Specspeed®2017_int_peak = Not Run

Platform Notes (Continued)

CPU min MHz: 800.0000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 15360K
NUMA node0 CPU(s): 0-9
NUMA node1 CPU(s): 10-19

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes x挽救 fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgrbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cgq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves xsaveopt xgetbv1 xsaves cgq_llc cgq_occup_llc cgq_mbb_total cgq_mbb_local split_lock_detect wbenoinvd dtherm idi arat pin pts avx512v bmi umip pku ospke avx512_vbmi2 gfnv vaes vpcmldqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid fsrm md_clear pconfig flush_lld arch_capabilities

/proc/cpuinfo cache data
  cache size : 15360 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9
  node 0 size: 257180 MB
  node 0 free: 256620 MB
  node 1 cpus: 10 11 12 13 14 15 16 17 18 19
  node 1 size: 258005 MB
  node 1 free: 257420 MB

/proc/meminfo
  MemTotal: 527550796 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

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Platform Notes (Continued)

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.4 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.4"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
  Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (ITLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 15 09:51

SPEC is set to: /spec2017

From /sys/devices/virtual/dmi/id
  Vendor: XFUSION

(Continued on next page)
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Platform Notes (Continued)

Product: 2288H V6
Product Family: Whitley
Serial: 123456

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2666

BIOS:
  BIOS Vendor: INSYDE Corp.
  BIOS Version: 0.95
  BIOS Date: 12/22/2021
  BIOS Revision: 0.95

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C     | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
     | 625.x264_s(base) 657.xz_s(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++   | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
     | 641.leela_s(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 648.exchange2_s(base)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
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### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort

### Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
- 602.gcc_s: -DSPEC_LP64  
- 605.mcf_s: -DSPEC_LP64  
- 620.omnetpp_s: -DSPEC_LP64  
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
- 625.x264_s: -DSPEC_LP64  
- 631.deepsjeng_s: -DSPEC_LP64  
- 641.leela_s: -DSPEC_LP64  
- 648.exchange2_s: -DSPEC_LP64  
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**

**Fortran benchmarks:**
- -m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4 -nostandard-malloc-lhs -align array32byte -auto -mbranches-within-32B-boundaries
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The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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