### SPEC CPU®2017 Floating Point Rate Result

**xFusion**

**xFusion 5288 V6 (Intel Xeon Gold 5315Y)**

**SPECrater®2017_fp_base = 175**

**SPECrater®2017_fp_peak = Not Run**

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base (175)</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 5315Y  
  - **Max MHz:** 3600  
  - **Nominal:** 3200  
  - **Enabled:** 16 cores, 2 chips, 2 threads/core  
  - **Orderable:** 1,2 chips  
  - **Cache L1:** 32 KB I + 48 KB D on chip per core  
  - **L2:** 1.25 MB I+D on chip per core  
  - **L3:** 12 MB I+D on chip per chip  
  - **Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)  
  - **Storage:** 1 x 960 GB SATA SSD  
  - **Other:** None

#### Software

- **OS:** Red Hat Enterprise Linux release 8.4 (Ootpa)  
  - **4.18.0-305.el8.x86_64**  
- **Compiler:**  
  - **C/C++:** Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
  - **Fortran:** Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.95 Released Dec-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion 5288 V6 (Intel Xeon Gold 5315Y)

SPECrate®2017_fp_base = 175

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion
Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>705</td>
<td>455</td>
<td>705</td>
<td>455</td>
<td>706</td>
<td>454</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>214</td>
<td>190</td>
<td>214</td>
<td>190</td>
<td>214</td>
<td>190</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>113</td>
<td>268</td>
<td>113</td>
<td>268</td>
<td>113</td>
<td>269</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>99.3</td>
<td>843</td>
<td>99.3</td>
<td>844</td>
<td>99.2</td>
<td>844</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>170</td>
<td>440</td>
<td>170</td>
<td>440</td>
<td>170</td>
<td>440</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>146</td>
<td>230</td>
<td>146</td>
<td>229</td>
<td>147</td>
<td>229</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>169</td>
<td>423</td>
<td>169</td>
<td>431</td>
<td>166</td>
<td>431</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>148</td>
<td>330</td>
<td>148</td>
<td>331</td>
<td>147</td>
<td>331</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>159</td>
<td>352</td>
<td>159</td>
<td>351</td>
<td>158</td>
<td>355</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td>423</td>
<td>188</td>
<td>423</td>
<td>189</td>
<td>421</td>
<td>190</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>259</td>
<td>208</td>
<td>259</td>
<td>209</td>
<td>258</td>
<td>208</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>157</td>
<td>796</td>
<td>157</td>
<td>794</td>
<td>157</td>
<td>794</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>80.9</td>
<td>628</td>
<td>80.9</td>
<td>628</td>
<td>81.0</td>
<td>627</td>
</tr>
</tbody>
</table>

SPECrate®2017_fp_base = 175
SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)
General Notes (Continued)

sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numacl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Performance Profile Set to Performance
SNC Set to Enabled SNC2 (2-clusters)

Sysinfo program /spec2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca640d
running on localhost.localdomain Sat Apr 16 18:12:23 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
  2  "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
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Test Sponsor: xFusion
Tested by: xFusion
Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
BIOS Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
Stepping: 6
CPU MHz: 3482.758
BogoMIPS: 6400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 12288K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf perfctr pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebpx cat_l3 invpcid_single ssbd mba ibrs ibpb ibp_enhanced ibs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsbgbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdts_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsave xsetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbml_total cqm_mbml_local split_lock detect wbinvd dtherm ida arat pln pts hwp epp avx512v bmi umip pku ospke avx512_vbmi2 gfn vaes vpcmldq vavx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig flush_lld arch_capabilities

/proc/cpuinfo cache data
cache size: 12288 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 257142 MB
node 0 free: 250861 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 258041 MB
node 1 free: 253658 MB
node distances:
node 0 1
0: 10 20

(Continued on next page)
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Platform Notes (Continued)

1:  20  10

From /proc/meminfo
MemTotal:       527548352 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling):
CVE-2019-11135 (TSX Asynchronous Abort):

run-level 3 Apr 16 15:12

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion 5288 V6 (Intel Xeon Gold 5315Y)

<table>
<thead>
<tr>
<th>SPEC is set to: /spec2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Filesystem</td>
</tr>
<tr>
<td>------------</td>
</tr>
<tr>
<td>/dev/sda3</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id
Vendor: XFUSION
Product: 5288 V6
Product Family: Whitley
Serial: 123456

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: INSYDE Corp.
BIOS Version: 0.95
BIOS Date: 12/22/2021
BIOS Revision: 0.95

(End of data from sysinfo program)

Compiler Version Notes

C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++ | 508.namd_r(base) 510.parest_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base) 526.blender_r(base)

(Continued on next page)
Base Compiler Invocation

C benchmarks:
- icx
xFusion
xFusion 5288 V6 (Intel Xeon Gold 5315Y)

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

(Continued on next page)
## Base Optimization Flags (Continued)

**C++ benchmarks (continued):**
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-Wl/usr/local/jemalloc64-5.0.1/lib`

**Fortran benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`
- `-qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte -auto`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-Wl/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both Fortran and C:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -Wl/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both C and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-Wl/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using Fortran, C, and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -Wl/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

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Test Date: Apr-2022
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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