



# SPEC CPU®2017 Floating Point Speed Result

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## Lenovo Global Technology

ThinkSystem SR635 V3  
(3.25 GHz, AMD EPYC 9354)

**SPECSpeed®2017\_fp\_base = 235**

**SPECSpeed®2017\_fp\_peak = 250**

CPU2017 License: 9017

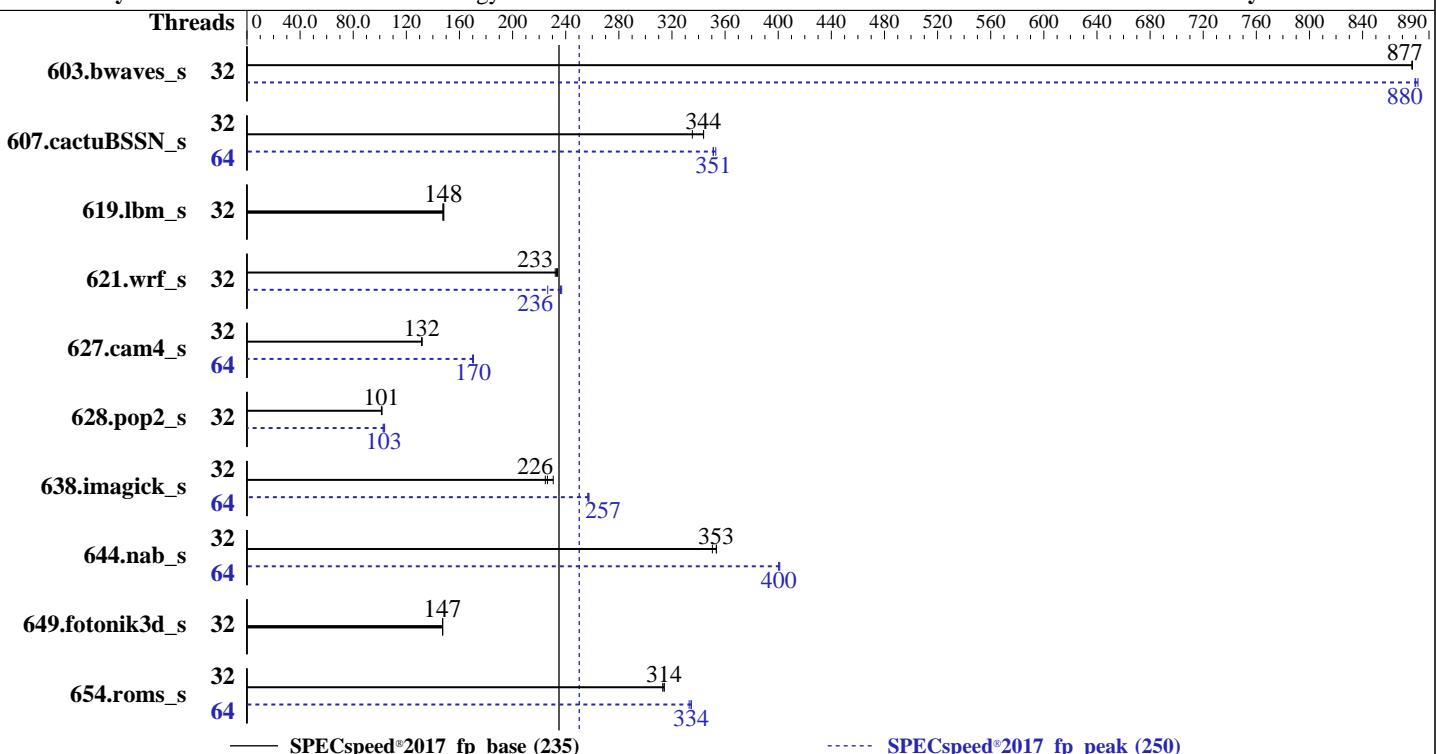
Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Apr-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022



## Hardware

CPU Name: AMD EPYC 9354  
Max MHz: 3800  
Nominal: 3250  
Enabled: 32 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 256 MB I+D on chip per chip,  
32 MB shared / 4 cores  
Other: None  
Memory: 384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)  
Storage: 1 x 480 GB SATA SSD  
Other: None

## Software

OS: SUSE Linux Enterprise Server 15 SP4  
Compiler: Kernel 5.14.21-150400.22-default  
Parallel: C/C++/Fortran: Version 4.0.0 of AOCC  
Firmware: Yes  
File System: Lenovo BIOS Version KAE109F 1.40 released Feb-2023  
System State: xfs  
Base Pointers: Run level 3 (multi-user)  
Peak Pointers: 64-bit  
Other: 64-bit  
Power Management: None  
BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Threads
603.bwaves_s	32	67.3	877	<b>67.2</b>	<b>877</b>	67.2	878		32	67.1	879	66.9	882	<b>67.0</b>	<b>880</b>	
607.cactubSSN_s	32	49.7	335	48.5	344	<b>48.5</b>	<b>344</b>		64	47.5	351	<b>47.5</b>	<b>351</b>	47.2	353	
619.lbm_s	32	<b>35.4</b>	<b>148</b>	35.5	148	35.3	148		32	<b>35.4</b>	<b>148</b>	35.5	148	35.3	148	
621.wrf_s	32	57.0	232	56.6	234	<b>56.8</b>	<b>233</b>		32	58.4	226	55.8	237	<b>56.0</b>	<b>236</b>	
627.cam4_s	32	<b>67.3</b>	<b>132</b>	67.3	132	67.3	132		64	<b>52.0</b>	<b>170</b>	52.0	171	52.2	170	
628.pop2_s	32	117	101	<b>117</b>	<b>101</b>	117	102		32	115	104	<b>115</b>	<b>103</b>	116	103	
638.imagick_s	32	<b>63.8</b>	<b>226</b>	62.6	231	64.2	225		64	<b>56.1</b>	<b>257</b>	56.2	257	56.0	257	
644.nab_s	32	<b>49.4</b>	<b>353</b>	49.4	354	49.9	350		64	<b>43.6</b>	<b>400</b>	43.6	401	43.6	400	
649.fotonik3d_s	32	61.9	147	<b>61.9</b>	<b>147</b>	61.8	147		32	61.9	147	<b>61.9</b>	<b>147</b>	61.8	147	
654.roms_s	32	<b>50.1</b>	<b>314</b>	50.3	313	50.1	314		64	<b>47.1</b>	<b>334</b>	47.1	335	47.3	333	
<b>SPECspeed®2017_fp_base = 235</b>																
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

(Continued on next page)



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## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.  
To always enable THP for peak runs of:  
603.bwaves\_s, 607.cactubSSN\_s, 619.lbm\_s, 627.cam4\_s, 628.pop2\_s, 638.imagick\_s, 644.nab\_s, 649.fotonik3d\_s:  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled; echo always > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.  
To disable THP for peak runs of 621.wrf\_s:  
'echo never > /sys/kernel/mm/transparent\_hugepage/enabled; echo always > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.  
To enable THP only on request for peak runs of 654.roms\_s:  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled; echo madvise > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-63"  
LD\_LIBRARY\_PATH =  
  "/home/cpu2017-1.1.9-amd-aocc400-genoa-Ble/amd\_speed\_aocc400\_genoa\_B\_lib  
  /lib:"  
LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"  
MALLOC\_CONF = "oversize\_threshold:0,retain:true"  
OMP\_DYNAMIC = "false"  
OMP\_SCHEDULE = "static"  
OMP\_STACKSIZE = "128M"  
OMP\_THREAD\_LIMIT = "64"

Environment variables set by runcpu during the 603.bwaves\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 607.cactubSSN\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

Environment variables set by runcpu during the 628.pop2\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 638.imagick\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

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## Environment Variables Notes (Continued)

Environment variables set by runcpu during the 644.nab\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0 32 1 33 2 34 3 35 4 36 5 37 6 38 7 39 8 40 9 41 10 42  
11 43 12 44 13 45 14 46 15 47 16 48 17 49 18 50 19 51 20 52 21 53 22 54  
23 55 24 56 25 57 26 58 27 59 28 60 29 61 30 62 31 63"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance

Sysinfo program /home/cpu2017-1.1.9-amd-aocc400-genoa-Ble/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Mar 14 17:28:06 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

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## Platform Notes (Continued)

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  12. Services, from systemctl list-unit-files
  13. Linux kernel boot-time arguments, from /proc/cmdline
  14. cpupower frequency-info
  15. sysctl
  16. /sys/kernel/mm/transparent\_hugepage
  17. /sys/kernel/mm/transparent\_hugepage/khugepaged
  18. OS release
  19. Disk information
  20. /sys/devices/virtual/dmi/id
  21. dmidecode
  22. BIOS
- 

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

---

```
2. w
17:28:06 up 1:03, 1 user, load average: 0.05, 0.01, 0.00
USER      TTY      FROM             LOGIN@     IDLE    JCPU   PCPU WHAT
root      tty1      -           17:27    36.00s  1.13s  0.05s /bin/bash ./amd_speed_aocc400_genoa_B1.sh
```

---

```
3. Username
From environment variable $USER: root
```

---

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
cheduling priority       (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 1545969
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority        (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 1545969
virtual memory             (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

---

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## Platform Notes (Continued)

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
/bin/bash ./Run036-compliant-amd-speedfp.sh
python3 ./run_amd_speed_aocc400_genoa_B1.py
/bin/bash ./amd_speed_aocc400_genoa_B1.sh
runcpu --config amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.078/templogs/preenv.fpspeed.078.0.log --lognum 078.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc400-genoa-B1e
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9354 32-Core Processor
vendor_id        : AuthenticAMD
cpu family       : 25
model           : 17
stepping         : 1
microcode        : 0xa101111
bugs             : sysret_ss_atrs spectre_v1 spectre_v2 spec_store_bypass
TLB size         : 3584 4K pages
cpu cores        : 32
siblings          : 64
1 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

```
From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                64
On-line CPU(s) list:   0-63
Vendor ID:             AuthenticAMD
```

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## Platform Notes (Continued)

Model name:	AMD EPYC 9354 32-Core Processor
CPU family:	25
Model:	17
Thread(s) per core:	2
Core(s) per socket:	32
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU max MHz:	3799.0720
CPU min MHz:	1500.0000
BogoMIPS:	6490.19
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
Virtualization:	AMD-V
L1d cache:	1 MiB (32 instances)
L1i cache:	1 MiB (32 instances)
L2 cache:	32 MiB (32 instances)
L3 cache:	256 MiB (8 instances)
NUMA node(s):	1
NUMA node0 CPU(s):	0-63
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected
From lscpu --cache:	
NAME ONE-SIZE ALL-SIZE WAYS TYPE	LEVEL SETS PHY-LINE COHERENCY-SIZE

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### Platform Notes (Continued)

L1d	32K	1M	8 Data	1	64	1	64
L1i	32K	1M	8 Instruction	1	64	1	64
L2	1M	32M	8 Unified	2	2048	1	64
L3	32M	256M	16 Unified	3	32768	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0-63  
node 0 size: 386516 MB  
node 0 free: 385384 MB  
node distances:  
node 0  
0: 10

-----  
9. /proc/meminfo

MemTotal: 395792584 kB

-----  
10. who -r

run-level 3 Mar 14 16:24

-----  
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievfd iscsi-init iscsid iscsiui0 issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
indirect	wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline

(Continued on next page)



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## Platform Notes (Continued)

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=07e3aa6d-6e00-4b8c-ab1a-89473b71b5fa
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 1.50 GHz and 3.25 GHz.  
                The governor "performance" may decide which speed to use  
                within this range.  
    boost state support:  
        Supported: yes  
        Active: yes
```

```
-----  
15. sysctl  
kernel.numa_balancing          0  
kernel.randomize_va_space       0  
vm.compaction_proactiveness    20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                 0  
vm.nr_hugepages_mempolicy       0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                   1  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor       10  
vm.zone_reclaim_mode            1
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage  
defrag           [always] defer defer+madvise madvise never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force
```

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## Platform Notes (Continued)

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs    60000
    defrag                  1
    max_ptes_none           511
    max_ptes_shared          256
    max_ptes_swap            64
    pages_to_scan            4096
    scan_sleep_millisecs     10000
```

```
-----  
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----  
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc400-genoa-B1e
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   446G   82G  364G  19%  /
```

```
-----  
20. /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem SR635V3
Product Family: ThinkSystem
Serial:          1234567890
```

```
-----  
21. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  10x Samsung M321R4GA3BB0-CQKDG 32 GB 2 rank 4800
  2x Samsung M321R4GA3BB0-CQKMG 32 GB 2 rank 4800
```

```
-----  
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Lenovo
BIOS Version:     KAE109F-1.40
BIOS Date:        02/27/2023
BIOS Revision:    1.40
Firmware Revision: 1.40
```



# SPEC CPU®2017 Floating Point Speed Result

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## Lenovo Global Technology

ThinkSystem SR635 V3  
(3.25 GHz, AMD EPYC 9354)

SPECSpeed®2017\_fp\_base = 235

SPECSpeed®2017\_fp\_peak = 250

CPU2017 License: 9017

Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Apr-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

## Compiler Version Notes

=====

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak)  
| 644.nab\_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on

(Continued on next page)



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## Compiler Version Notes (Continued)

LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

---

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64



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Test Sponsor: Lenovo Global Technology

Hardware Availability: Apr-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```



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CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Mar-2023

Hardware Availability: Apr-2023

Software Availability: Nov-2022

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-fsto -fstruct-layout=9 -mllvm -unroll-threshold=50

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Tested by: Lenovo Global Technology

Software Availability: Nov-2022

## Peak Optimization Flags (Continued)

638.imagick\_s (continued):

```
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-region-vectorize -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3
-fvector-transform -fscalar-transform -fopenmp=libomp
-lomp -lamdlibm -lamdaloc -lflang
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: Same as 603.bwaves\_s

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

```
627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
```

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## Peak Optimization Flags (Continued)

627.cam4\_s (continued):

```
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

628.pop2\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIB -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-Mrecursive -fvector-transform -fscalar-transform
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4
-fveclib=AMDLIB -ffast-math -fopenmp -flto -fstruct-layout=9
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -finline-aggressive -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-R.html>  
<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-R.xml>  
<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

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