



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

H3C UniServer R4300 G6 (Intel Xeon Gold 6530)

SPECrate®2017\_int\_base = 515

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9066

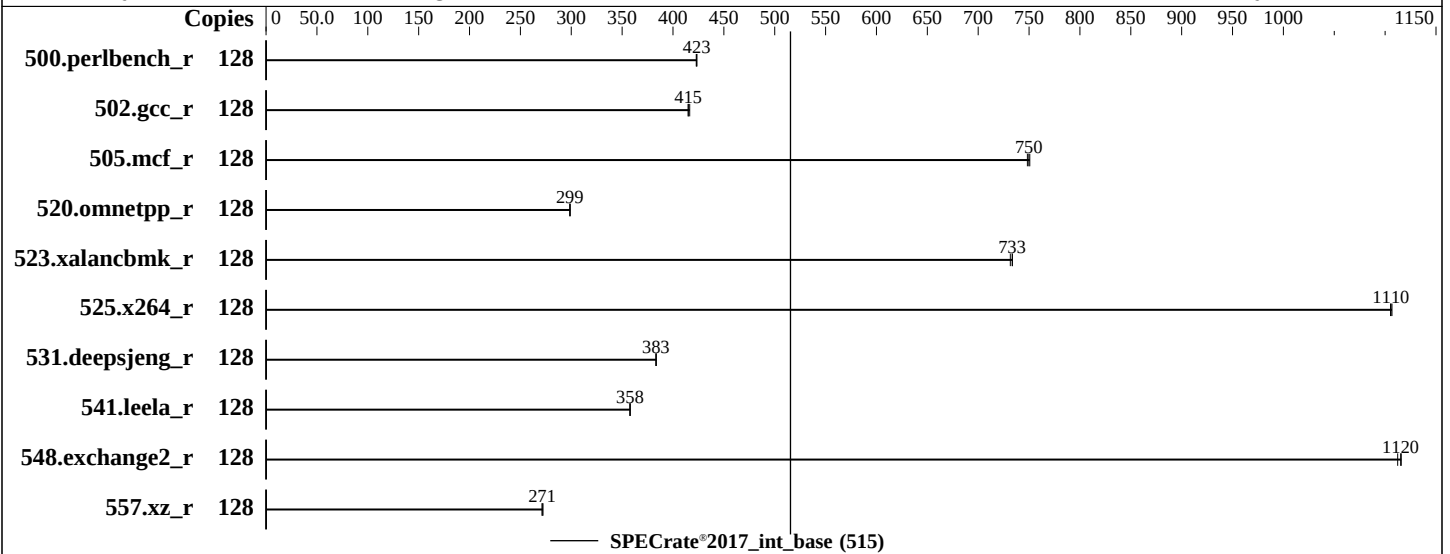
Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2025

Hardware Availability: Oct-2023

Software Availability: Mar-2024



## Hardware

CPU Name: Intel Xeon Gold 6530  
Max MHz: 4000  
Nominal: 2100  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 160 MB I+D on chip per chip  
Other: None  
Memory: 512 GB (8 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
Storage: 1 x 960 GB SATA SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP5  
5.14.21-150500.53-default  
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Version 6.10.50 released Feb-2025 BIOS  
File System: btrfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base   |            |             |            |            |            |             | Peak   |         |       |         |       |         |       |
|-----------------|--------|------------|-------------|------------|------------|------------|-------------|--------|---------|-------|---------|-------|---------|-------|
|                 | Copies | Seconds    | Ratio       | Seconds    | Ratio      | Seconds    | Ratio       | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 128    | <b>481</b> | <b>423</b>  | 481        | 423        | 482        | 423         |        |         |       |         |       |         |       |
| 502.gcc_r       | 128    | 435        | 416         | <b>437</b> | <b>415</b> | 437        | 415         |        |         |       |         |       |         |       |
| 505.mcf_r       | 128    | 276        | 748         | <b>276</b> | <b>750</b> | 276        | 751         |        |         |       |         |       |         |       |
| 520.omnetpp_r   | 128    | <b>562</b> | <b>299</b>  | 562        | 299        | 562        | 299         |        |         |       |         |       |         |       |
| 523.xalancbmk_r | 128    | 184        | 734         | 185        | 732        | <b>184</b> | <b>733</b>  |        |         |       |         |       |         |       |
| 525.x264_r      | 128    | 203        | 1110        | 203        | 1110       | <b>203</b> | <b>1110</b> |        |         |       |         |       |         |       |
| 531.deepsjeng_r | 128    | 383        | 383         | <b>383</b> | <b>383</b> | 382        | 384         |        |         |       |         |       |         |       |
| 541.leela_r     | 128    | <b>592</b> | <b>358</b>  | 592        | 358        | 593        | 358         |        |         |       |         |       |         |       |
| 548.exchange2_r | 128    | <b>301</b> | <b>1120</b> | 301        | 1110       | 301        | 1120        |        |         |       |         |       |         |       |
| 557.xz_r        | 128    | 508        | 272         | 509        | 271        | <b>509</b> | <b>271</b>  |        |         |       |         |       |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/lib/ia32:/home/speccpu/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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## Platform Notes

### BIOS Settings:

SNC = Enable SNC2 (2-clusters)  
Package C State = C6(Retention) state

### BMC Settings:

Fan mode = powerful mode

Sysinfo program /home/speccpu/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Mar 19 17:23:21 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
17:23:21 up 13 min, 1 user, load average: 0.13, 0.08, 0.04  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 17:16 12.00s 2.62s 0.14s sh rate.sh

3. Username  
From environment variable \$USER: root

4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0

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## Platform Notes (Continued)

file size (blocks, -f) unlimited  
pending signals (-i) 2062401  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 2062401  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
sh rate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.003/temlogs/preenv.intrate.003.0.log --lognum 003.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/speccpu
```

```
-----
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 6530
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping        : 2
microcode       : 0x21000291
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 32
siblings        : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

### 7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             128
```

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## Platform Notes (Continued)

On-line CPU(s) list: 0-127  
Vendor ID: GenuineIntel  
Model name: INTEL(R) XEON(R) GOLD 6530  
CPU family: 6  
Model: 207  
Thread(s) per core: 2  
Core(s) per socket: 32  
Socket(s): 2  
Stepping: 2  
CPU max MHz: 4000.0000  
CPU min MHz: 800.0000  
BogoMIPS: 4200.00  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid aperfperf tsc\_known\_freq pni pclmulqdq dtes64 monitor ds\_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb cat\_l3 cat\_l2 cdp\_l3 invpcid\_single cdp\_l2 ssbd mba ibrs ibpb stibp ibrs\_enhanced fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local avx\_vnni avx512\_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp\_act\_window hwp\_epp hwp\_pkg\_req avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize tsxldtrk pconfig arch\_lbr avx512\_fp16 amx\_tile flush\_l1d arch\_capabilities  
L1d cache: 3 MiB (64 instances)  
L1i cache: 2 MiB (64 instances)  
L2 cache: 128 MiB (64 instances)  
L3 cache: 320 MiB (2 instances)  
NUMA node(s): 4  
NUMA node0 CPU(s): 0-15,64-79  
NUMA node1 CPU(s): 16-31,80-95  
NUMA node2 CPU(s): 32-47,96-111  
NUMA node3 CPU(s): 48-63,112-127  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBSRB-eIBRS SW sequence  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS   | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|--------|----------|----------------|
| L1d  | 48K      | 3M       | 12   | Data        | 1     | 64     | 1        | 64             |
| L1i  | 32K      | 2M       | 8    | Instruction | 1     | 64     | 1        | 64             |
| L2   | 2M       | 128M     | 16   | Unified     | 2     | 2048   | 1        | 64             |
| L3   | 160M     | 320M     | 20   | Unified     | 3     | 131072 | 1        | 64             |

8. numactl --hardware

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## Platform Notes (Continued)

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-15,64-79
node 0 size: 128648 MB
node 0 free: 128208 MB
node 1 cpus: 16-31,80-95
node 1 size: 129015 MB
node 1 free: 128498 MB
node 2 cpus: 32-47,96-111
node 2 size: 129015 MB
node 2 free: 128554 MB
node 3 cpus: 48-63,112-127
node 3 size: 128942 MB
node 3 free: 128575 MB
node distances:
node  0  1  2  3
  0:  10  12  21  21
  1:  12  10  21  21
  2:  21  21  10  12
  3:  21  21  12  10
```

```
9. /proc/meminfo
MemTotal:          527997904 kB
```

```
10. who -r
run-level 3 Mar 19 17:10
```

```
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target    Status
multi-user        running
```

```
12. Services, from systemctl list-unit-files
STATE      UNIT FILES
enabled    apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings postfix
           purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
           wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled    boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables
           grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask
           rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator
           systemd-sysextd systemd-time-wait-sync systemd-timesyncd tuned
indirect    wickedd
```

```
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=5b1bbac0-75ae-47d9-95db-594b94b8ab43
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 800 MHz and 4.00 GHz.
```

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## Platform Notes (Continued)

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

-----  
15. tuned-adm active

Current active profile: throughput-performance

-----  
16. sysctl

|                              |       |
|------------------------------|-------|
| kernel.numa_balancing        | 1     |
| kernel.randomize_va_space    | 2     |
| vm.compaction_proactiveness  | 20    |
| vm.dirty_background_bytes    | 0     |
| vm.dirty_background_ratio    | 10    |
| vm.dirty_bytes               | 0     |
| vm.dirty_expire_centisecs    | 3000  |
| vm.dirty_ratio               | 20    |
| vm.dirty_writeback_centisecs | 500   |
| vm.dirtytime_expire_seconds  | 43200 |
| vm.extfrag_threshold         | 500   |
| vm.min_unmapped_ratio        | 1     |
| vm.nr_hugepages              | 0     |
| vm.nr_hugepages_mempolicy    | 0     |
| vm.nr_overcommit_hugepages   | 0     |
| vm.swappiness                | 10    |
| vm.watermark_boost_factor    | 15000 |
| vm.watermark_scale_factor    | 10    |
| vm.zone_reclaim_mode         | 0     |

-----  
17. /sys/kernel/mm/transparent\_hugepage

|                |                                              |
|----------------|----------------------------------------------|
| defrag         | always defer defer+madvice [madvice] never   |
| enabled        | [always] madvice never                       |
| hpage_pmd_size | 2097152                                      |
| shmem_enabled  | always within_size advise [never] deny force |

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

|                       |       |
|-----------------------|-------|
| alloc_sleep_millisecs | 60000 |
| defrag                | 1     |
| max_ptes_none         | 511   |
| max_ptes_shared       | 256   |
| max_ptes_swap         | 64    |
| pages_to_scan         | 4096  |
| scan_sleep_millisecs  | 10000 |

-----  
19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP5

-----  
20. Disk information

SPEC is set to: /home/speccpu

| Filesystem | Type  | Size | Used | Avail | Use% | Mounted on |
|------------|-------|------|------|-------|------|------------|
| /dev/sda2  | btrfs | 892G | 31G  | 860G  | 4%   | /home      |

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## Platform Notes (Continued)

### 21. /sys/devices/virtual/dmi/id

Vendor: New H3C Technologies Co., Ltd.  
Product: H3C UniServer R4300 G6  
Product Family: Rack  
Serial: 210235A4S5H244000015

### 22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

8x Hynix HMC94AGBRA181N 64 GB 2 rank 5600, configured at 4800

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 6.10.50  
BIOS Date: 02/28/2025  
BIOS Revision: 5.32

## Compiler Version Notes

C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557.xz\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base) 541.leela\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)





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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-intel-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-intel-RevB.html)



# SPEC CPU®2017 Integer Rate Result

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New H3C Technologies Co., Ltd.

H3C UniServer R4300 G6 (Intel Xeon Gold 6530)

SPECrate®2017\_int\_base = 515

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Mar-2025

**Hardware Availability:** Oct-2023

**Software Availability:** Mar-2024

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-intel-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-intel-RevB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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