



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340 Gen12  
(3.50 GHz, Intel Xeon 6507P)

**SPECrate®2017\_int\_base = 106**

**SPECrate®2017\_int\_peak = 109**

CPU2017 License: 3

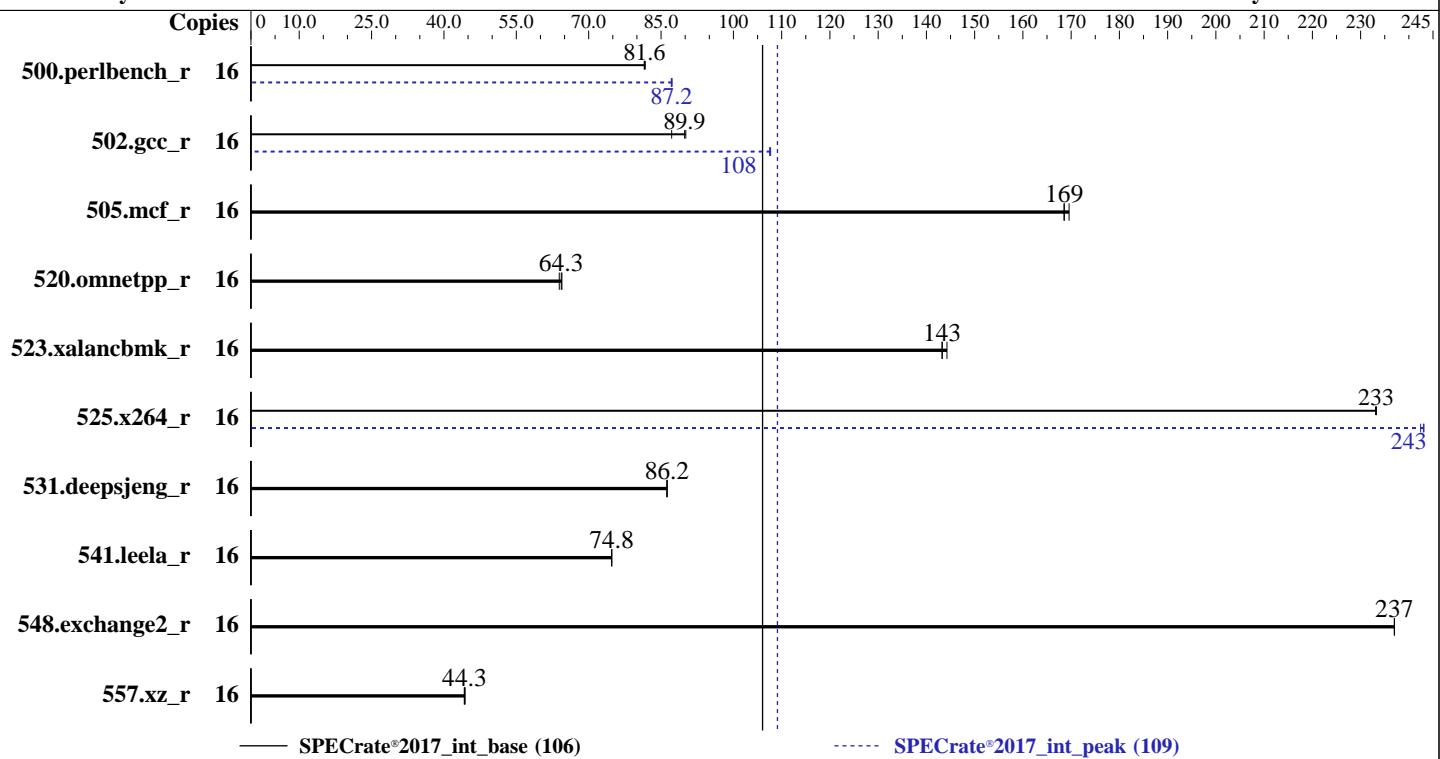
**Test Date:** Apr-2025

**Test Sponsor:** HPE

**Hardware Availability:** Mar-2025

**Tested by:** HPE

**Software Availability:** Jun-2024



## Hardware

CPU Name: Intel Xeon 6507P  
Max MHz: 4300  
Nominal: 3500  
Enabled: 8 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 64 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 48 MB I+D on chip per chip  
Other: None  
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)  
Storage: 1 x 1.2 TB NVMe SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP6  
Compiler: Kernel 6.4.0-150600.21-default  
C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: HPE BIOS Version v1.20 02/14/2025 released Feb-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	16	<b>312</b>	<b>81.6</b>	312	81.5	312	81.7	16	<b>292</b>	87.1	292	87.3	<b>292</b>	<b>87.2</b>
502.gcc_r	16	251	90.1	260	87.2	<b>252</b>	<b>89.9</b>	16	210	108	211	108	<b>211</b>	<b>108</b>
505.mcf_r	16	<b>153</b>	<b>169</b>	152	170	153	169	16	<b>153</b>	<b>169</b>	152	170	<b>153</b>	<b>169</b>
520.omnetpp_r	16	326	64.5	<b>326</b>	<b>64.3</b>	328	63.9	16	326	64.5	<b>326</b>	<b>64.3</b>	328	63.9
523.xalancbmk_r	16	117	144	118	143	<b>118</b>	<b>143</b>	16	117	144	118	143	<b>118</b>	<b>143</b>
525.x264_r	16	<b>120</b>	<b>233</b>	120	233	120	233	16	<b>115</b>	<b>243</b>	116	242	<b>115</b>	243
531.deepsjeng_r	16	213	86.3	213	86.2	<b>213</b>	<b>86.2</b>	16	213	86.3	213	86.2	<b>213</b>	<b>86.2</b>
541.leela_r	16	354	74.8	355	74.7	<b>354</b>	<b>74.8</b>	16	354	74.8	355	74.7	<b>354</b>	<b>74.8</b>
548.exchange2_r	16	177	237	<b>177</b>	<b>237</b>	177	237	16	177	237	<b>177</b>	<b>237</b>	177	237
557.xz_r	16	389	44.4	<b>390</b>	<b>44.3</b>	391	44.2	16	389	44.4	<b>390</b>	<b>44.3</b>	391	44.2

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation

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## General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute  
Enhanced Processor Performance Profile set to Aggressive  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
Last Level Cache (LLC) Prefetch set to Enabled  
XPT Prefetch set to Disabled  
Workload Profile set to Custom  
DCU Stream Prefetcher set to Disabled  
Adjacent Sector Prefetch set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Thu Apr 24 06:38:26 2025

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. sysctl  
16. /sys/kernel/mm/transparent\_hugepage  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS  
-----

-----  
1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

2. w  
06:38:26 up 6:46, 2 users, load average: 0.00, 0.00, 0.00  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root pts/0 172.17.1.13 23:55 10.00s 0.71s 0.00s -bash

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## Platform Notes (Continued)

```
3. Username
From environment variable $USER: root

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 1030040
max locked memory        (kbytes, -l) 8192
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 1030040
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 -c
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=8 --define physicalfirst
  --define no-numa --tune base,peak -o all --define drop_caches intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 --configfile
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=8 --define physicalfirst
  --define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
  base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.intrate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6507P
vendor_id       : GenuineIntel
cpu family      : 6
model          : 173
stepping        : 1
microcode       : 0x1000380
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 8
siblings         : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 0: apicids 0-15
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

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## Platform Notes (Continued)

7. lscpu

```
From lscpu from util-linux 2.39.3:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6507P
BIOS Model name: Intel(R) Xeon(R) 6507P CPU @ 3.5GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 1
Stepping: 1
BogoMIPS: 7000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nop1 xtTopology nonstop_tsc cpuid aperfimperf tsc_known_freq pn1
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 intel_ppin cdp_12
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid
rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57
rdpid bus_lock_detect cldemote movdir64b enqcmd fsrm md_clear
serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
amx_int8 flush_llc arch_capabilities
Virtualization: VT-x
L1d cache: 384 KiB (8 instances)
L1i cache: 512 KiB (8 instances)
L2 cache: 16 MiB (8 instances)
L3 cache: 48 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-15
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
```

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## Platform Notes (Continued)

Vulnerability Spectre v2:

Mitigation: Enhanced / Automatic IBRS; IBPB conditional; RSB filling;  
PBRSB-eIBRS Not affected; BHI BHI\_DIS\_S

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	384K	12	Data	1	64	1	64
L1i	64K	512K	16	Instruction	1	64	1	64
L2	2M	16M	16	Unified	2	2048	1	64
L3	48M	48M	16	Unified	3	49152	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0-15  
node 0 size: 257534 MB  
node 0 free: 250011 MB  
node distances:  
node 0  
0: 10

-----  
9. /proc/meminfo

MemTotal: 263715728 kB

-----  
10. who -r  
run-level 3 Apr 23 23:52

-----  
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)  
Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor audtfd cron getty@ irqbalance issue-generator kbdsettings nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
indirect	systemd-userdbd wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=eab0ed4d-682e-4710-8733-c28bac484a35  
splash=silent  
resume=/dev/disk/by-uuid/aeed0eb8-1155-47a5-869c-f7c223bb00af  
mitigations=auto  
quiet  
security=apparmor

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## Platform Notes (Continued)

```
14. cpupower frequency-info
analyzing CPU 11:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

```
15. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           0
```

```
16. /sys/kernel/mm/transparent_hugepage
defrag           always defer defer+madvise [madvise] never
enabled          [always] madvise never
hugepage_pmd_size 2097152
shmem_enabled    always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag               1
max_ptes_none        511
max_ptes_shared      256
max_ptes_swap        64
pages_to_scan        4096
scan_sleep_millisecs 10000
```

```
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used  Avail Use% Mounted on
/dev/nvme1n1p3  xfs  1.2T  73G  1.1T  7%  /home
```

```
20. /sys/devices/virtual/dmi/id
Vendor:      HPE
```

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Product: HPE ProLiant Compute DL340 Gen12  
Product Family: ProLiant  
Serial: C2X2NWW69B

-----  
21. dmidecode

Additional information from dmidecode 3.4 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Micron MTC20F2085S1RC64BD2 QSFF 32 GB 2 rank 6400

-----  
22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 1.20  
BIOS Date: 02/14/2025  
BIOS Revision: 1.20  
Firmware Revision: 1.11

## Compiler Version Notes

=====

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 502.gcc\_r(peak)

=====

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=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
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=====

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

(Continued on next page)



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Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
Fortran | 548.exchange2\_r(base, peak)

=====  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-fipa -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL340 Gen12  
(3.50 GHz, Intel Xeon 6507P)

SPECrate®2017\_int\_base = 106

SPECrate®2017\_int\_peak = 109

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Apr-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)
```

(Continued on next page)



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## Peak Optimization Flags (Continued)

500.perlbench\_r (continued):

```
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

502.gcc\_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89

```
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

525.x264\_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast

```
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.xml>



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Test Sponsor: HPE

Tested by: HPE

**Test Date:** Apr-2025

**Hardware Availability:** Mar-2025

**Software Availability:** Jun-2024

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Tested with SPEC CPU®2017 v1.1.9 on 2025-04-24 06:38:26-0400.

Report generated on 2025-05-20 16:00:59 by CPU2017 PDF formatter v6716.

Originally published on 2025-05-20.