



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

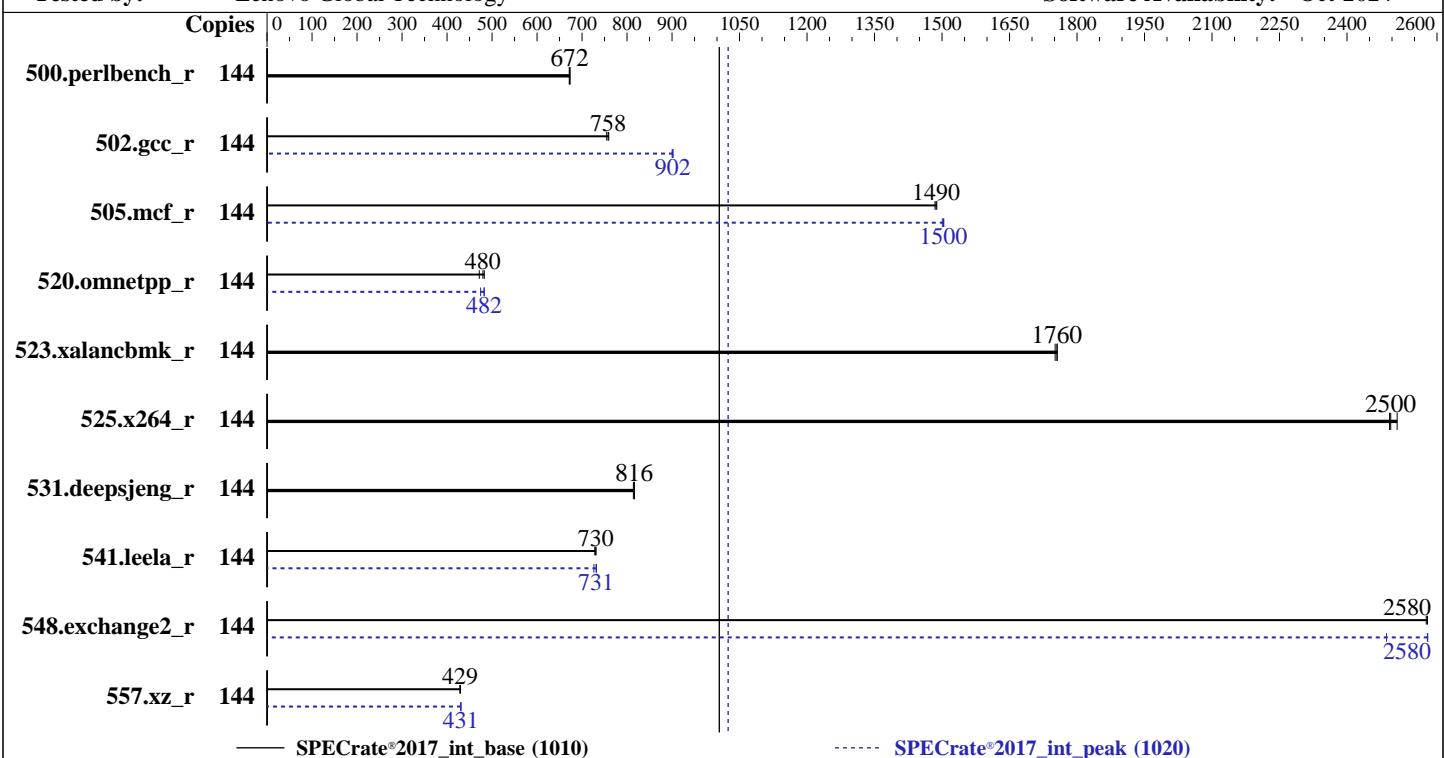
Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



— SPECrate®2017_int_base (1010)

----- SPECrate®2017_int_peak (1020)

Hardware

CPU Name: AMD EPYC 9365
Max MHz: 4300
Nominal: 3400
Enabled: 72 cores, 2 chips, 2 threads/core
Orderable: 2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 192 MB I+D on chip per chip,
32 MB shared / 6 cores
Other: None
Memory: 768 GB (24 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)
Storage: 1 x 3.84 TB NVME SSD
Other: CPU Cooling: DLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
Compiler: Kernel 6.4.0-150600.21-default
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
Firmware: No
File System: Lenovo BIOS Version QGE135D 8.10 released Feb-2025
System State: xfs
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: 32/64-bit
Power Management: None
BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	144	341	672	340	674	341	672	144	341	672	340	674	341	672	341	672
502.gcc_r	144	269	759	270	755	269	758	144	227	900	226	902	226	902	226	902
505.mcf_r	144	156	1490	157	1480	157	1490	144	155	1500	155	1500	155	1500	155	1500
520.omnetpp_r	144	401	472	391	483	394	480	144	391	483	392	482	398	475	392	475
523.xalancbmk_r	144	86.8	1750	86.6	1760	86.6	1760	144	86.8	1750	86.6	1760	86.6	1760	86.6	1760
525.x264_r	144	100	2510	101	2490	101	2500	144	100	2510	101	2490	101	2500	101	2500
531.deepsjeng_r	144	202	816	202	816	203	814	144	202	816	202	816	203	814	202	814
541.leela_r	144	327	728	326	731	327	730	144	326	731	326	732	328	726	326	726
548.exchange2_r	144	146	2580	146	2580	146	2580	144	152	2490	146	2580	146	2580	146	2580
557.xz_r	144	362	430	362	429	363	429	144	362	430	361	431	361	431	361	431

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

cpupower set to performance mode
cpupower frequency-set -r -g performance
To enable Transparent Hugepages (THP) for all allocations:
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-
    -aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

P-State set to Enabled

NUMA Nodes per Socket set to NPS2

ACPI SRAT L3 Cache as NUMA Domain set to Enabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat May 3 08:34:44 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
08:34:44 up 4:09, 1 user, load average: 25.85, 101.54, 127.58
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 3094098
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 3094098
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run025-compliant-amd-rateint.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.249/templogs/preenv.intrate.249.0.log --lognum 249.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9365 36-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 2
stepping        : 1
microcode      : 0xb00211a
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 36
siblings        : 72
2 physical ids (chips)
144 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45
physical id 1: core ids 0-5,8-13,16-21,24-29,32-37,40-45
physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91
physical id 1: apicids 128-139,144-155,160-171,176-187,192-203,208-219
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	144
On-line CPU(s) list:	0-143
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9365 36-Core Processor
BIOS Model name:	AMD EPYC 9365 36-Core Processor
BIOS CPU family:	Unknown CPU @ 3.4GHz
CPU family:	107
Model:	26
Thread(s) per core:	2
Core(s) per socket:	36
Socket(s):	2
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	101%
CPU max MHz:	3400.0000
CPU min MHz:	1500.0000
BogoMIPS:	6800.06
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pnpi pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch oswi ibs skinfit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2 smep bmi2 erms invpcid cqmp rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmp_llc cqmp_occur_llc cqmp_mbm_total cqmp_mbm_local user_shstck avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cpc_arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_lld debug_swap
Virtualization:	AMD-V

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

L1d cache:	3.4 MiB (72 instances)
L1i cache:	2.3 MiB (72 instances)
L2 cache:	72 MiB (72 instances)
L3 cache:	384 MiB (12 instances)
NUMA node(s):	12
NUMA node0 CPU(s):	0-5,72-77
NUMA node1 CPU(s):	6-11,78-83
NUMA node2 CPU(s):	12-17,84-89
NUMA node3 CPU(s):	18-23,90-95
NUMA node4 CPU(s):	24-29,96-101
NUMA node5 CPU(s):	30-35,102-107
NUMA node6 CPU(s):	36-41,108-113
NUMA node7 CPU(s):	42-47,114-119
NUMA node8 CPU(s):	48-53,120-125
NUMA node9 CPU(s):	54-59,126-131
NUMA node10 CPU(s):	60-65,132-137
NUMA node11 CPU(s):	66-71,138-143
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.4M	12	Data	1	64	1	64
L1i	32K	2.3M	8	Instruction	1	64	1	64
L2	1M	72M	16	Unified	2	1024	1	64
L3	32M	384M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)

node 0 cpus: 0-5,72-77

node 0 size: 64088 MB

node 0 free: 63417 MB

node 1 cpus: 6-11,78-83

node 1 size: 64508 MB

node 1 free: 63938 MB

node 2 cpus: 12-17,84-89

node 2 size: 64508 MB

node 2 free: 63962 MB

node 3 cpus: 18-23,90-95

node 3 size: 64508 MB

node 3 free: 63886 MB

node 4 cpus: 24-29,96-101

node 4 size: 64470 MB

node 4 free: 63511 MB

node 5 cpus: 30-35,102-107

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
node 5 size: 64508 MB
node 5 free: 63917 MB
node 6 cpus: 36-41,108-113
node 6 size: 64508 MB
node 6 free: 63991 MB
node 7 cpus: 42-47,114-119
node 7 size: 64508 MB
node 7 free: 63986 MB
node 8 cpus: 48-53,120-125
node 8 size: 64508 MB
node 8 free: 63971 MB
node 9 cpus: 54-59,126-131
node 9 size: 64508 MB
node 9 free: 63986 MB
node 10 cpus: 60-65,132-137
node 10 size: 64508 MB
node 10 free: 63971 MB
node 11 cpus: 66-71,138-143
node 11 size: 64413 MB
node 11 free: 63885 MB
node distances:
node   0   1   2   3   4   5   6   7   8   9   10  11
  0: 10  11  11  12  12  12  32  32  32  32  32  32
  1: 11  10  11  12  12  12  32  32  32  32  32  32
  2: 11  11  10  12  12  12  32  32  32  32  32  32
  3: 12  12  12  10  11  11  32  32  32  32  32  32
  4: 12  12  12  11  10  11  32  32  32  32  32  32
  5: 12  12  12  11  11  10  32  32  32  32  32  32
  6: 32  32  32  32  32  32  10  11  11  12  12  12
  7: 32  32  32  32  32  32  11  10  11  12  12  12
  8: 32  32  32  32  32  32  11  11  10  12  12  12
  9: 32  32  32  32  32  32  12  12  12  10  11  11
 10: 32  32  32  32  32  32  12  12  12  11  10  11
 11: 32  32  32  32  32  32  12  12  12  11  11  10
```

```
9. /proc/meminfo
MemTotal:      792116236 kB
```

```
10. who -r
run-level 3 May 3 04:26
```

```
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target      Status
multi-user          running
```

```
12. Services, from systemctl list-unit-files
STATE           UNIT FILES
enabled         YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
                kbdsettings klog lvm2-monitor nsqd nvmefc-boot-connections nvmf-autoconnect postfix
                purge-kernels rollback rsyslog smartd sshd systemd-pstore wickedd wickedd-auto4
                wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime  systemd-remount-fs
disabled        autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievfd issue-add-ssh-keys
                kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
systemd-confexit systemd-network-generator systemd-sysext systemd-time-wait-sync
systemd-timesyncd
indirect           systemd-userdbd wickedd

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=9444ad63-6600-41de-9db7-026811eaf134
splash=silent
mitigations=auto
quiet
security=apparmor

-----
14. cpupower frequency-info
analyzing CPU 2:
    current policy: frequency should be within 1.50 GHz and 3.40 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
        Supported: yes
        Active: yes

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compression_proactiveness   20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1

-----
16. /sys/kernel/mm/transparent_hugepage
defrag           [always] defer defer+madvise madvice never
enabled          [always] madvice never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag             1
max_ptes_none      511
max_ptes_shared    256
max_ptes_swap      64
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
pages_to_scan      4096
scan_sleep_millisecs 10000
```

```
-----  
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----  
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p3  xfs  3.5T  96G  3.4T   3%  /
```

```
-----  
20. /sys/devices/virtual/dmi/id
Vendor:        Lenovo
Product:       ThinkSystem SD665 V3
Product Family: ThinkSystem
Serial:        1234567890
```

```
-----  
21. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
24x Samsung M321R4GA3PB2-CCPKC 32 GB 2 rank 6400, configured at 6000
```

```
-----  
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:        Lenovo
BIOS Version:       QGE135D-8.10
BIOS Date:          02/10/2025
BIOS Revision:      8.10
Firmware Revision: 12.10
```

Compiler Version Notes

```
=====| 502.gcc_r(peak)
```

```
-----  
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

```
=====| 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)
```

```
-----  
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Compiler Version Notes (Continued)

=====

C | 502.gcc_r(peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran | 548.exchange2_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdaloc-ext -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdaloc-ext
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdaloc -ldl
```



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Oct-2024

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

```
502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdaloc
```

```
505.mcf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdaloc-ext -ldl
```

```
525.x264_r: basepeak = yes
```

```
557.xz_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdaloc-ext -ldl
```

C++ benchmarks:

```
520.omnetpp_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lamdaloc-ext
-ldl
```

```
523.xalancbmk_r: basepeak = yes
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

531.deepsjeng_r: basepeak = yes

```
541.leela_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang
-lamdaloc-ext -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -flto -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdaloc -ldl
```

Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

```
502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v119/aocc5/1316/amd_rate_aocc500_znver5_A_lib/lib32
```

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.html>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD665 V3
(3.40 GHz, AMD EPYC 9365)

SPECrate®2017_int_base = 1010

SPECrate®2017_int_peak = 1020

CPU2017 License: 9017

Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Mar-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECCpu2017-Flags-V1.2-Turin-E.xml>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-05-02 20:34:44-0400.

Report generated on 2025-05-20 16:01:31 by CPU2017 PDF formatter v6716.

Originally published on 2025-05-20.