SPEC CPU®2017 Floating Point Rate Result

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Lenovo Global Technology

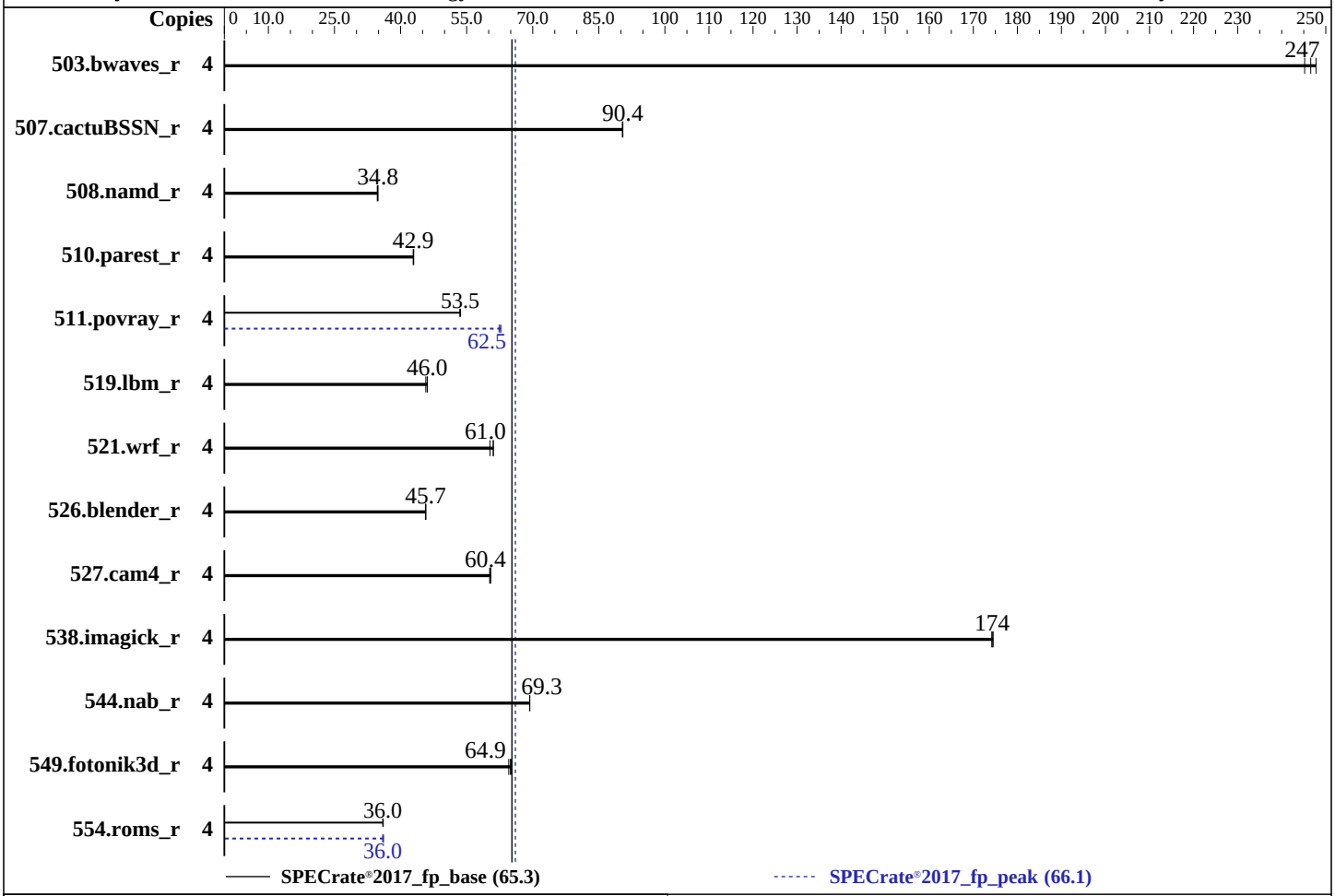
ThinkSystem SR250 V3
(2.80 GHz,Intel Xeon 6315P)

SPECrate®2017_fp_base = 65.3

SPECrate®2017_fp_peak = 66.1

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2025
Hardware Availability: Apr-2025
Software Availability: Nov-2024



Hardware

CPU Name:	Intel Xeon 6315P
Max MHz:	5200
Nominal:	2800
Enabled:	4 cores, 1 chip
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	12 MB I+D on chip per chip
Other:	None
Memory:	64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400)
Storage:	1 x 480 GB SATA SSD
Other:	CPU Cooling: Air

Software

OS:	Red Hat Enterprise Linux 9.5 (Plow) Kernel 5.14.0-503.11.1.el9_5.x86_64
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Lenovo BIOS Version CTE119B 7.10 released Feb-2025
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	4	164	245	**163**	**247**	162	248	4	164	245	**163**	**247**	162	248
507.cactuBSSN_r	4	**56.0**	**90.4**	56.0	90.5	56.1	90.3	4	**56.0**	**90.4**	56.0	90.5	56.1	90.3
508.namd_r	4	**109**	**34.8**	109	34.7	109	34.8	4	**109**	**34.8**	109	34.7	109	34.8
510.parest_r	4	243	43.0	244	42.9	**244**	**42.9**	4	243	43.0	244	42.9	**244**	**42.9**
511.povray_r	4	175	53.5	174	53.6	**175**	**53.5**	4	149	62.8	**149**	**62.5**	150	62.4
519.lbm_r	4	92.2	45.7	91.5	46.1	**91.6**	**46.0**	4	92.2	45.7	91.5	46.1	**91.6**	**46.0**
521.wrf_r	4	**147**	**61.0**	149	60.3	147	61.1	4	**147**	**61.0**	149	60.3	147	61.1
526.blender_r	4	133	45.6	133	45.7	**133**	**45.7**	4	133	45.6	133	45.7	**133**	**45.7**
527.cam4_r	4	116	60.4	**116**	**60.4**	116	60.2	4	116	60.4	**116**	**60.4**	116	60.2
538.imagick_r	4	57.1	174	57.0	175	**57.1**	**174**	4	57.1	174	57.0	175	**57.1**	**174**
544.nab_r	4	**97.1**	**69.3**	97.1	69.3	97.1	69.3	4	**97.1**	**69.3**	97.1	69.3	97.1	69.3
549.fotonik3d_r	4	239	65.1	241	64.6	**240**	**64.9**	4	239	65.1	241	64.6	**240**	**64.9**
554.roms_r	4	**177**	**36.0**	177	36.0	176	36.1	4	176	36.1	177	36.0	**177**	**36.0**

SPECrate®2017_fp_base = 65.3

SPECrate®2017_fp_peak = 66.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Red Hat Enterprise Linux 8.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

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General Notes (Continued)

```
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
Turbo Limit for 95W CPU set to Disabled

 Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Tue May 27 03:02:44 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 252 (252-46.el9_5.2)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux localhost.localdomain 5.14.0-503.11.1.el9_5.x86_64 #1 SMP PREEMPT_DYNAMIC Mon Sep 30 11:54:45 EDT
   2024 x86_64 x86_64 x86_64 GNU/Linux

 ------------------------------------------------------------
 2. w
    03:02:44 up 3 min,  0 users,  load average: 0.07, 0.05, 0.01
   USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT

 ------------------------------------------------------------
 3. Username
   From environment variable $USER:  root

 ------------------------------------------------------------
```

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Platform Notes (Continued)

```
4. ulimit -a
  real-time non-blocking time  (microseconds, -R) unlimited
  core file size              (blocks, -c) unlimited
  data seg size               (kbytes, -d) unlimited
  scheduling priority                 (-e) 0
  file size                   (blocks, -f) unlimited
  pending signals                     (-i) 256706
  max locked memory           (kbytes, -l) 8192
  max memory size             (kbytes, -m) unlimited
  open files                          (-n) 1024
  pipe size                (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority                  (-r) 0
  stack size                  (kbytes, -s) unlimited
  cpu time                   (seconds, -t) unlimited
  max user processes                  (-u) 256706
  virtual memory              (kbytes, -v) unlimited
  file locks                          (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 31
 sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
 sshd: root [priv]
 sshd: root@notty
 /bin/bash ./02.remote_local_SPECcpu_1.01.sh
 sh Run316-compliant-ic2024.1-lin-core-avx2-ratefp-smt-off-20240308.sh
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=4 -c
   ic2024.1-lin-core-avx2-rate-20240308.cfg --define cores=4 --define physicalfirst --define no-numa --tune
   base,peak -o all --define drop_caches fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=4 --configfile
   ic2024.1-lin-core-avx2-rate-20240308.cfg --define cores=4 --define physicalfirst --define no-numa --tune
   base,peak --output_format all --define drop_caches --nopower --runmode rate --tune base:peak --size
   refrate fprate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.883/templogs/preenv.fprate.883.0.log
   --lognum 883.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) 6315P
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x12c
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
    cpu cores       : 4
    siblings        : 4
    1 physical ids (chips)
    4 processors (hardware threads)
    physical id 0: core ids 0-3
    physical id 0: apicids 0,2,4,6
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
```

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Platform Notes (Continued)

```
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   42 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          4
  On-line CPU(s) list:             0-3
  Vendor ID:                       GenuineIntel
  BIOS Vendor ID:                  Intel(R) Corporation
  Model name:                      Intel(R) Xeon(R) 6315P
  BIOS Model name:                 Intel(R) Xeon(R) 6315P
  CPU family:                      6
  Model:                           183
  Thread(s) per core:              1
  Core(s) per socket:              4
  Socket(s):                       1
  Stepping:                        1
  BogoMIPS:                        5606.40
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                   pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                   pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                   nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                   pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                   xtpr pdcm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                                   xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd
                                   ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad
                                   fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap
                                   clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                                   split_lock_detect avx_vnni dtherm ida arat pln pts hfi vnmi umip pku
                                   ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm
                                   md_clear serialize pconfig arch_lbr ibt flush_l1d arch_capabilities
  Virtualization:                  VT-x
  L1d cache:                       192 KiB (4 instances)
  L1i cache:                       128 KiB (4 instances)
  L2 cache:                        8 MiB (4 instances)
  L3 cache:                        12 MiB (1 instance)
  NUMA node(s):                    1
  NUMA node0 CPU(s):               0-3
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Not affected
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                   PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K     192K   12 Data            1    64        1             64
      L1i       32K     128K    8 Instruction     1    64        1             64
      L2         2M       8M   16 Unified         2  2048        1             64
      L3        12M      12M    6 Unified         3 32768        1             64
```

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Platform Notes (Continued)

```
------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-3
  node 0 size: 64217 MB
  node 0 free: 63648 MB
  node distances:
  node     0
     0:   10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       65758332 kB

------------------------------------------------------------
10. who -r
  run-level 3 May 27 02:59

------------------------------------------------------------
11. Systemd service manager version: systemd 252 (252-46.el9_5.2)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                   dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
                   mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark
                   sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
  enabled-runtime  systemd-remount-fs
  disabled         canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot
                   chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade
                   kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm rhsm-facts
                   rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
                   systemd-boot-check-no-failures systemd-pstore systemd-sysext
  indirect         sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                   systemd-sysupdate-reboot

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd4,gpt2)/boot/vmlinuz-5.14.0-503.11.1.el9_5.x86_64
  root=UUID=ac8f2e4e-a15f-4475-ba58-5cea88b71de2
  ro
  resume=UUID=b1768a5a-be88-4ca4-8723-20387600340a

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 2:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
```

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Platform Notes (Continued)

```
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release     Red Hat Enterprise Linux 9.5 (Plow)
  redhat-release Red Hat Enterprise Linux release 9.5 (Plow)
  system-release Red Hat Enterprise Linux release 9.5 (Plow)

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.1
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda4      xfs   361G   16G  345G   5% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Lenovo
    Product:        ThinkSystem SR250 V3
    Product Family: ThinkSystem
    Serial:         1234567890

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
```

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Platform Notes (Continued)

```
  Memory:
    2x SK Hynix HMCG88AGBEA081N 32 GB 2 rank 5600, configured at 4400


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Lenovo
    BIOS Version:      CTE119B-7.10
    BIOS Date:         02/20/2025
    BIOS Revision:     7.10
    Firmware Revision: 6.10
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf_r: `basepeak = yes`

527.cam4_r: `basepeak = yes`

Benchmarks using both C and C++:

511.povray_r: `-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate`(pass 1) `-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2 -flto -Ofast -ffast-math -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

526.blender_r: `basepeak = yes`

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.html`
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.xml`
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml`

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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