SPEC CFP95 Results

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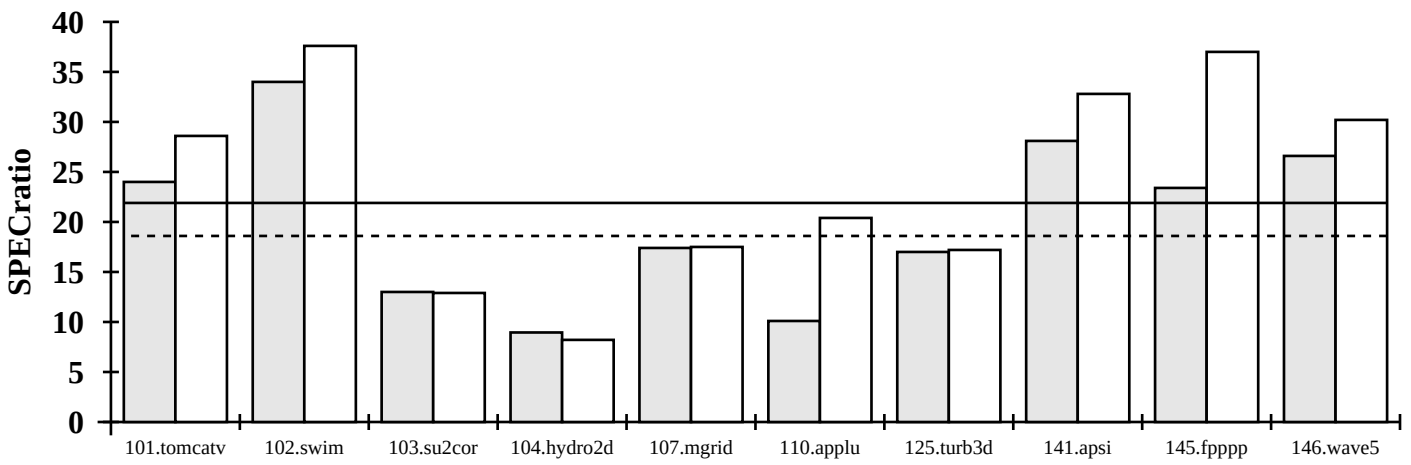
Sun Microsystems
Ultra 5 Model 400

SPECfp95 =	21.9
SPECfp_base95 =	18.6

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Mar-2000	Hardware Avail: Mar-2000	Software Avail: May-2000



Hardware/Software Configuration for:
Ultra 5 Model 400

Hardware

Model Name:	Ultra 5 Model 400
CPU:	400 MHz UltraSPARC-IIi
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 9.1GB 7200-rpm
Other Hardware:	Fast Ethernet System clock frequency: 100 MHz

Software

Operating System:	Solaris 8
Compiler:	Sun WorkShop F77 6.0
File System:	UFS
System State:	Single User
Kernel Extensions:	

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	154	24.0	129	28.6
102.swim	8600	253	34.0	229	37.6
103.su2cor	1400	108	13.0	108	12.9
104.hydro2d	2400	268	8.95	292	8.21
107.mgrid	2500	143	17.4	143	17.5
110.applu	2200	219	10.1	108	20.4
125.turb3d	4100	241	17.0	238	17.2
141.apsi	2100	74.8	28.1	64	32.8
145.fpppp	9600	410	23.4	259	37.0
146.wave5	3000	113	26.6	99	30.2
SPECfp_base95 (G. Mean)			18.6		
		SPECfp95 (G. Mean)			21.9

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2=
‘pwd‘/../feedback; FOPTION= -fast -xarch=v8plusa -s
BCOPY = -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp;
Baseline: -fast  -xarch=v8plusa -xprefetch
Baseline:  PASS1= ${P1}; PASS2= ${P2}
101.tomcatv: -fast -xchip=ultra2 -xarch=v8plusa -xpad=local:23 ${PF}
102.swim:    -fast -xchip=ultra2 -xarch=v8plusa -xpad=common:15 ${PF}
103.su2cor:  -fast -xchip=ultra2 -xarch=v8plusa -Qoption cg
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= ${P2}; EXTRA_LIBS= -lmvec
104.hydro2d: ${FOPTION} -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid: -fast -xchip=ultra2 -xarch=v8plusa ${PF}  -fsimple=2
110.applu: -fast -xchip=ultra2 -xarch=v8plusa -Qoption iropt
125.turb3d:  -fast -xchip=ultra2 -xarch=v8plusa ${PF} -stackvar -xpad
141.apsi:    -fast -xchip=ultra2 -xarch=v8plusa -stackvar -fsimple=2
141.apsi (cont):    -O4+invccexp+algassoc+bmerge+ipa,-Si36
145.fpppp:    -fast -xchip=ultra2 -xarch=v8plusa -xO5 -xsafe=mem
145.fpppp (cont):   -Qoption iropt -reroll=1  -Qoption cg -Qms_pipe-off
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   ${FOPTION} -stackvar -fsimple=2 -Qoption iropt -O4+unroll
-- Config file: u5_us2i_400.cfg
```