SPEC CFP95 Results

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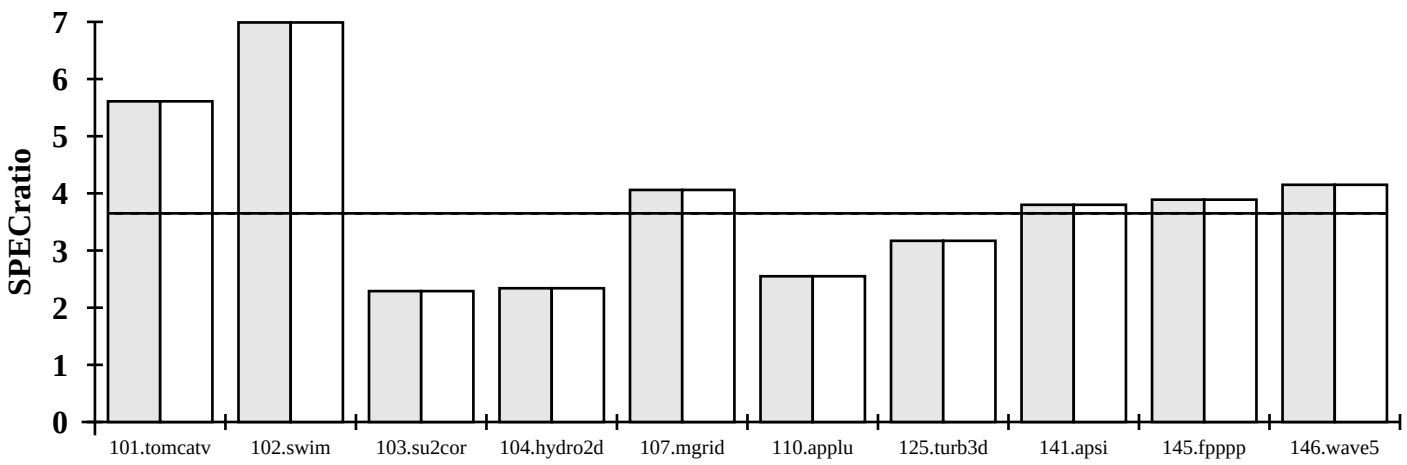
Digital Equipment Corp.
DEC 3000 Model 500

SPECfp95 = 3.65

SPECfp_base95 = 3.65

SPEC license # 2 | Tested By: Digital NH | Test Date: Jul-95 | Hardware Avail: Nov-92 | Software Avail: Aug-95



Hardware/Software Configuration for:
DEC 3000 Model 500

Hardware

Model Name:	DEC 3000 Model 500
CPU:	150 MHz 21064
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KB(I)+8KB(D) on chip
Secondary Cache:	512KB(I+D) off chip
Other Cache:	None
Memory:	128MB
Disk Subsystem:	2 x 2GB
Other Hardware:	Ethernet

Software

Operating System:	Digital UNIX V3.2 (Rev 214)
Compiler:	DEC FORTRAN V3.8
Other Software:	KAPF V2.1(kf77)
File System:	UFS
System State:	Multi User

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	660	5.61	660	5.61
102.swim	8600	1230	6.99	1230	6.99
103.su2cor	1400	612	2.29	612	2.29
104.hydro2d	2400	1027	2.34	1027	2.34
107.mgrid	2500	615	4.06	615	4.06
110.applu	2200	861	2.55	861	2.55
125.turb3d	4100	1294	3.17	1294	3.17
141.apsi	2100	553	3.80	553	3.80
145.fpppp	9600	2466	3.89	2466	3.89
146.wave5	3000	723	4.15	723	4.15
SPECfp_base95 (G. Mean)			3.65		
		SPECfp95 (G. Mean)			3.65

Notes/Tuning Information

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Baseline Optimizations: -fast -O5 -non_shared -om
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