SPEC CFP95 Results

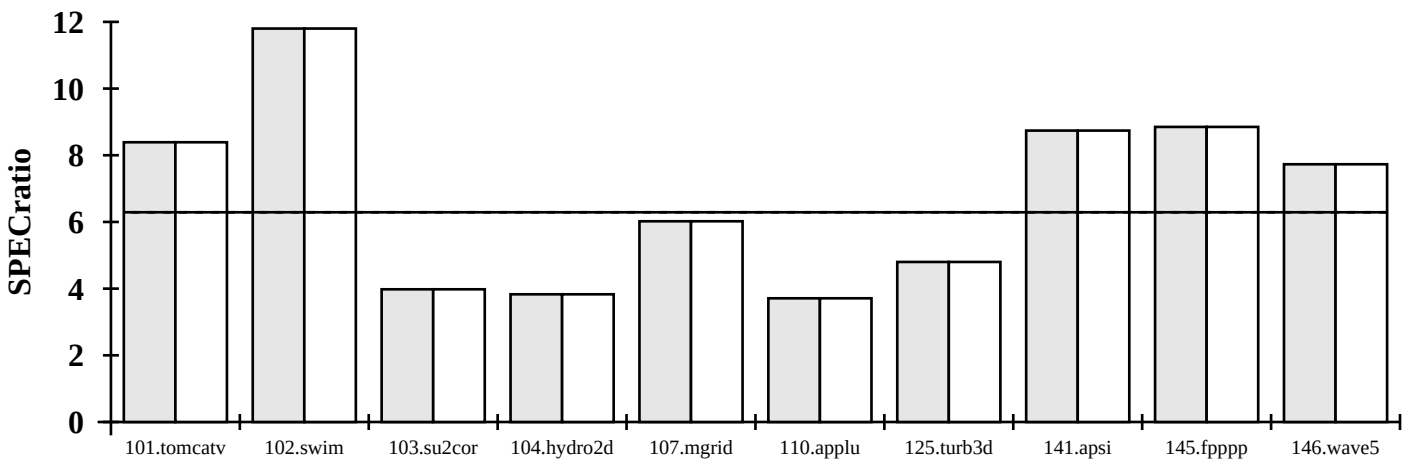
©Copyright 1995, Standard Performance Evaluation Corporation

Digital Equipment Corp.
DEC 3000 Model 900

SPECfp95 = 6.29

SPECfp_base95 = 6.29

SPEC license # 2	Tested By: Digital NH	Test Date: Jul-95	Hardware Avail: Jul-94	Software Avail: Aug-95



Hardware/Software Configuration for:
DEC 3000 Model 900

Hardware

Model Name:	DEC 3000 Model 900
CPU:	275 MHz 21064A
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KB(I)+16KB(D) on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	256MB
Disk Subsystem:	2 x 2GB
Other Hardware:	Ethernet

Software

Operating System:	Digital UNIX X3.2C-6 (Rev 121)
Compiler:	DEC FORTRAN V3.8
Other Software:	KAPF V2.1(kf77)
File System:	UFS
System State:	Multi User

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	441	8.39	441	8.39
102.swim	8600	730	11.8	730	11.8
103.su2cor	1400	352	3.98	352	3.98
104.hydro2d	2400	626	3.83	626	3.83
107.mgrid	2500	415	6.02	415	6.02
110.applu	2200	593	3.71	593	3.71
125.turb3d	4100	855	4.80	855	4.80
141.apsi	2100	240	8.74	240	8.74
145.fpppp	9600	1085	8.85	1085	8.85
146.wave5	3000	388	7.73	388	7.73
SPECfp_base95 (G. Mean)			6.29		
		SPECfp95 (G. Mean)			6.29

Notes/Tuning Information

```
Baseline Optimizations: -fast -O5 -non_shared -om
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 22110
(703) 331-0180
info@specbench.org
http://www.specbench.org