SPEC CINT95rate Results

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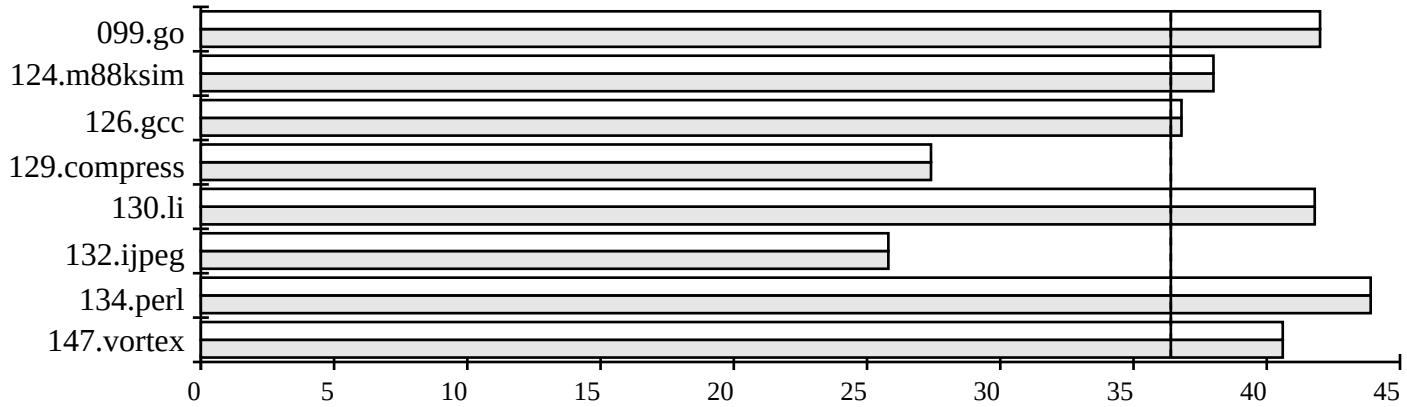
Siemens Nixdorf Informationssysteme PCE-5Smp

SPECint_rate95 =	36.4
SPECint_rate_base95 =	36.4

SPEC license # 22	Tested By: SNI, Augsburg / Germany	Test Date: Mar-96	Hardware Avail: Oct-95	Software Avail: Feb-96



Hardware/Software Configuration for: PCE-5Smp

Hardware

Model Name:	PCE-5Smp
CPU:	133 MHz Pentium
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	3 x 2 GB
Other Hardware:	Mylex DAC960

Software

Operating System:	Unixware 2.1
Compiler:	Intel Reference Compiler 2.2 Beta
File System:	UFS,VXFS
System State:	Single User

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	1	986	42.0	1	986	42.0
124.m88ksim	1	450	38.0	1	450	38.0
126.gcc	1	416	36.8	1	416	36.8
129.compress	1	592	27.4	1	592	27.4
130.li	1	409	41.8	1	409	41.8
132.ijpeg	1	836	25.8	1	836	25.8
134.perl	1	389	43.9	1	389	43.9
147.vortex	1	599	40.6	1	599	40.6
SPECint_rate_base95 (G. Mean)			36.4			
SPECint_rate95 (G. Mean)						36.4

Notes/Tuning Information

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Compiled by Intel
Base and peak flags are the same and use Feedback Directed Optimization
Pass1: -tp p5 -ipo -prof_gen -ircdb_dir /tmp/IRCDB -dn
Pass2: -tp p5 -ipo -prof_use -ircdb_dir /tmp/IRCDB -dn
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
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