SPEC Benchmark CFP95 Summary

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SPEC has determined that this result was not in compliance with the SPEC CPU95 run and reporting rules. Specificly, the result did not adhere to the restriction on the number of flags for baseline results; 5 were used instead of 4 or less.

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| Benchmarks | Base Ref Time | Base Run Time | Base Ratio | Peak Ref Time | Peak Run Time | Peak Ratio |
|---|---|---|---|---|---|---|
| 101.tomcatv | 3700 | NC | NC | 3700 | NC | NC |
| 102.swim | 8600 | NC | NC | 8600 | NC | NC |
| 103.su2cor | 1400 | NC | NC | 1400 | NC | NC |
| 104.hydro2d | 2400 | NC | NC | 2400 | NC | NC |
| 107.mgrid | 2500 | NC | NC | 2500 | NC | NC |
| 110.applu | 2200 | NC | NC | 2200 | NC | NC |
| 125.turb3d | 4100 | NC | NC | 4100 | NC | NC |
| 141.apsi | 2100 | NC | NC | 2100 | NC | NC |
| 145.fpppp | 9600 | NC | NC | 9600 | NC | NC |
| 146.wave5 | 3000 | NC | NC | 3000 | NC | NC |
| SPECfp_base95 (Geom. Mean) | | | NC | | | |
| SPECfp95 (Geom. Mean) | | | | | | NC |

## TESTER INFORMATION

SPEC License #: 6
Tested By: Sun, Palo Alto
Test Date: Jul-98
Hardware Avail: Jul-98
Software Avail: Oct-98

## HARDWARE

Model Name: Ultra 450
CPU: 296 MHz UltraSPARC-II
FPU: Integrated
Number of CPU(s): 4
Primary Cache: 16KBI+16KBD on chip
Secondary Cache: 2MB(I+D) off chip
Other Cache: None
Memory: 512MB
Disk Subsystem: 2 x 4.0GB
Other Hardware: Fast Ethernet
System clock frequency: 99 MHz

## SOFTWARE

Operating System: Solaris 2.6 3/98
Compiler: Sun f77 SF775.0 EFT1 (8/98)
File System: UFS
System State: Single User
Kernel Extensions: consistent_coloring=1

NOTES

P1= -xprofile=collect:`pwd`/../feedback
P2= -xprofile=use:`pwd`/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp; BMR=-Qoption iropt
Baseline:    -fast -xarch=v8plusa -fsimple=2 -xprefetch -xparallel
Peak:    -fast -xO4 -xarch=v8plusa -xtarget=ultra2 -xcache=16/32/1:2048/64/1
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -xparallel -xreduction -
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2 -xparallel
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde -fsimple=2 -xpar
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid:   -fast -xarch=v8plusa -xprefetch -fsimple=2  ${BCOPY} -xparallel
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_di
110 cont:    -fsimple=2  -xparallel
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 ${INV}
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -xchip=ultra3 -fsimple=2 -stackvar  -Qoption iropt
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption irop
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultra450mp_fp.cfg