SPEC CFP95rate Results

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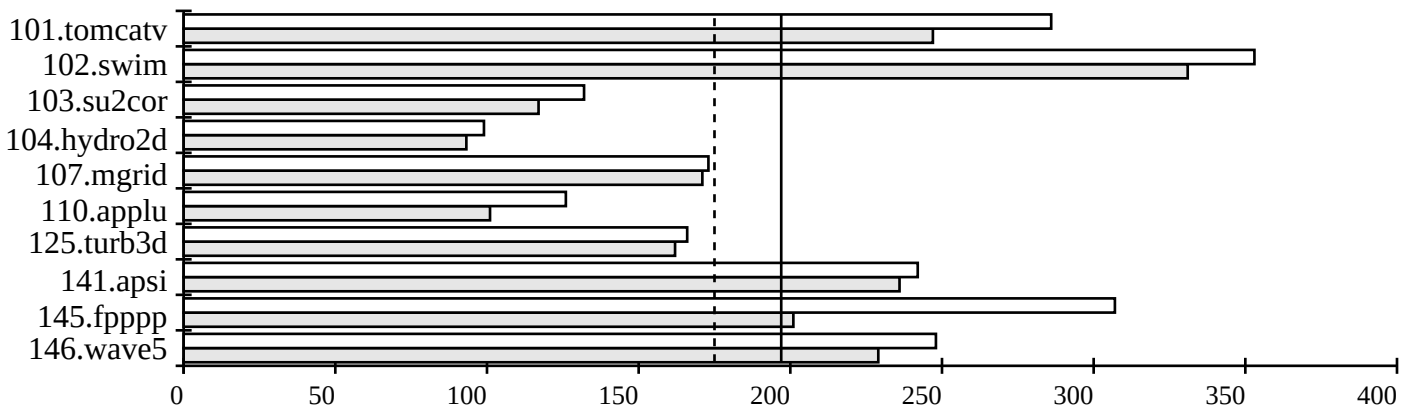
Sun Microsystems
Sun Enterprise 250

SPECfp_rate95	=	197
SPECfp_rate_base95	=	175

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Feb-99	Hardware Avail: Mar-99	Software Avail: Feb-99



SPECrate

Hardware/Software Configuration for:
Sun Enterprise 250

Hardware

Model Name:	Sun Enterprise 250
CPU:	400 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB (I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 4.2GB
Other Hardware:	Fast Ethernet

Software

Operating System:	Solaris 7 (kernel/unix)
Compiler:	Sun f77 5.0 Developer Release (12/22/98)
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
101.tomcatv	1	135	247	1	116	286
102.swim	1	234	331	1	219	353
103.su2cor	1	108	117	1	96	132
104.hydro2d	1	232	93.2	1	218	99.0
107.mgrid	1	132	171	1	130	173
110.applu	1	195	101	1	157	126
125.turb3d	1	227	162	1	222	166
141.apsi	1	79.9	236	1	78	242
145.fpppp	1	429	201	1	282	307
146.wave5	1	118	229	1	109	248
SPECfp_rate_base95 (G. Mean)			175			
SPECfp_rate95 (G. Mean)						197

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2=
‘pwd‘/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp
Baseline:    -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid:   -fast -xarch=v8plusa -xprefetch -fsimple=2  ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
110.applu:   -fast -xarch=v8plusa -Qoption iropt
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 -Qoption
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -xchip=ultra3 -fsimple=2 -stackvar
141.apsi: ${INV} -Qoption iropt -O4+algassoc; PASS1= -xO4 ${P1}; PASS2=
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
-- Config file: E250_DT.pub_sp.int.fp_3.cfg 99/01/27
```