SPEC CFP95 Results

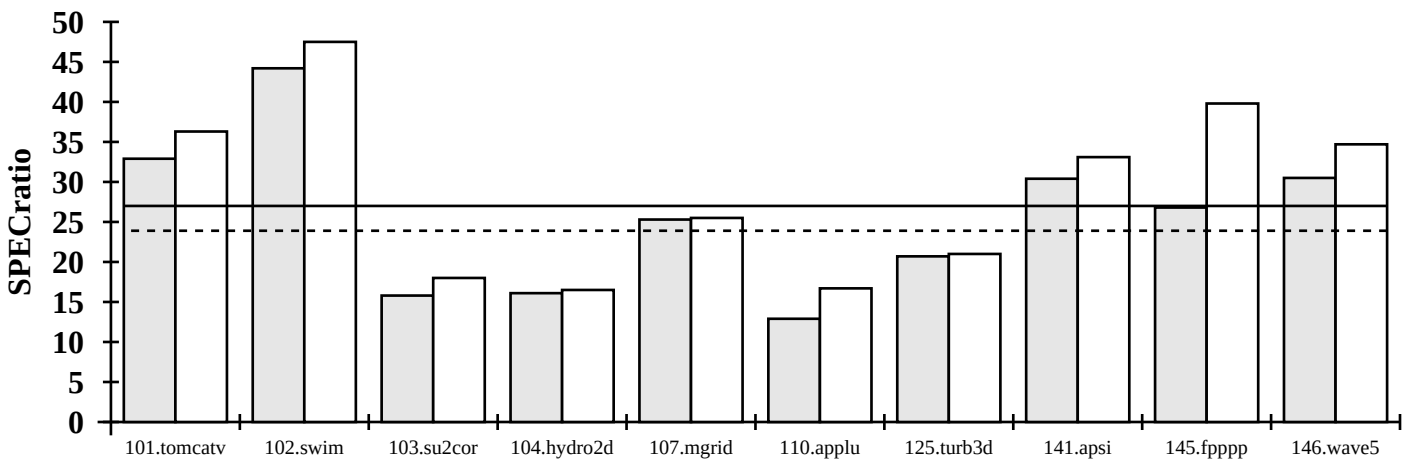
©Copyright 1995, Standard Performance Evaluation Corporation

Sun Microsystems
Ultra 60 Model 1450

SPECfp95 = 27.0

SPECfp_base95 = 23.9

SPEC license # 6 | Tested By: Sun, Palo Alto | Test Date: Apr-99 | Hardware Avail: May-99 | Software Avail: Feb-99



Hardware/Software Configuration for:
Ultra 60 Model 1450

Hardware

Model Name:	Ultra 60 Model 1450
CPU:	450 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 9.1GB (7200 RPM)
Other Hardware:	Fast Ethernet System clock frequency: 112.5 MHz

Software

Operating System:	Solaris 7 (64bit)
Compiler:	Sun WorkShop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	112	32.9	102	36.3
102.swim	8600	195	44.2	181	47.5
103.su2cor	1400	88.7	15.8	78	18.0
104.hydro2d	2400	149	16.1	146	16.5
107.mgrid	2500	98.7	25.3	98	25.5
110.applu	2200	170	12.9	132	16.7
125.turb3d	4100	198	20.7	195	21.0
141.apsi	2100	69.0	30.4	63	33.1
145.fpppp	9600	358	26.8	241	39.8
146.wave5	3000	98.4	30.5	87	34.7
SPECfp_base95 (G. Mean)			23.9		
		SPECfp95 (G. Mean)			27.0

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback; FOPTION= -fast -xarch=v8plusa -s
BCOPY = -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  ${FOPTION} -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2 ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
104.hydro2d: ${FOPTION} -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid:   ${FOPTION} -xprefetch -fsimple=2 ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  ${FOPTION} -stackvar -xpad -fsimple=2 -Qoption iropt -O4+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
141.apsi:    ${FOPTION} -fsimple=2 -stackvar  -Qoption iropt -Si36  ${INV} ${BMR} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   ${FOPTION} -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultrabest.cfg
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@spec.org
http://www.spec.org/

--

Prepared By: gnana