SPEC CINT95rate Results

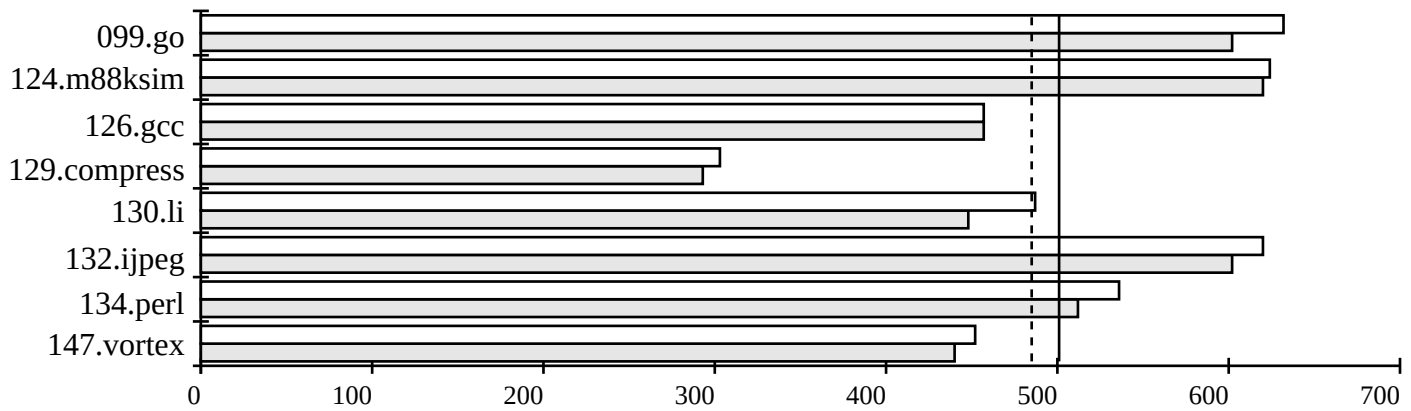
©Copyright 1995, Standard Performance Evaluation Corporation

IBM Corporation
RISC System/6000 H50 (4 CPU)

SPECint_rate95 = 501

SPECint_rate_base95 = 485

SPEC license # 11	Tested By: IBM, Austin, TX	Test Date: Jul-99	Hardware Avail: Feb-98	Software Avail: Dec-98



Hardware/Software Configuration for:
RISC System/6000 H50 (4 CPU)

Hardware

Model Name:	RS/6000 H50 (4 CPU)
CPU:	332 MHz PowerPC 604e
FPU:	Integrated
Number of CPU(s):	4
Primary Cache:	32KBI+32KBD on chip
Secondary Cache:	256KB(I+D) off chip
Other Cache:	None
Memory:	2 GB
Disk Subsystem:	6x2.2GB
Other Hardware:	None

Software

Operating System:	AIX 4.3.2
Compiler:	IBM C/C++ 3.6.4.2 memdbg 4.4.0.1 FDPR
File System:	AIX/JFS
System State:	Multi-user

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	4	275	602	4	262	632
124.m88ksim	4	110	620	4	110	624
126.gcc	4	134	457	4	134	457
129.compress	4	221	293	4	214	303
130.li	4	153	448	4	140	487
132.ijpeg	4	143	602	4	139	620
134.perl	4	134	512	4	128	536
147.vortex	4	221	440	4	215	452
SPECint_rate_base95 (G. Mean)			485			
			SPECint_rate95 (G. Mean)			501

Notes/Tuning Information

```
Compatibility Flags: gcc -ma -qlanglvl=ansi; perl -DI_TIME -DI_SYS_TIME
Base:  -O4 -Q=500 -lhmu -qpdf1/2
Peak:
099:  -O -Q=500 -qdatalocal -qassert=addr -qcompact -bnso -bI:/lib/syscalls.exp
124:  -O4 -Q=100 -qansialias -qpdf1/2; fdpr -R3
126:  -O -qdatalocal -qassert=addr -qinlglue -qpdf1/2 -bnso
-bI:/lib/syscalls.exp; fdpr -R3
129:  -qipa=level=2 -Q=200 -qassert=allp -qunroll=5
130:  -O4 -qlibansi -qpdf1/2; fdpr -R3
132:  -O4 -qunroll=4 -qpdf1/2 -bnso -bI:/lib/syscalls.exp
134:  -O4 -Q=200 -qinlglue -Dsetjmp=_setjmp -Dlongjmp=_longjmp -qansialias
-qunroll=4 -qpdf1/2 -bnso -bI:/lib/syscalls.exp
/usr/ccs/lib/bmalloc.o ; fdpr -R3
147:  -O3 -qarch=ppc -qipa=level=2 -qtune=601 -Q=500 -qpdf1/2
-lhmu ; fdpr -R2
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@spec.org
http://www.spec.org/

-- Prepared By: Cloyce D. Spradling <cloyce@austin.ibm.com>