SPEC CFP95rate Results

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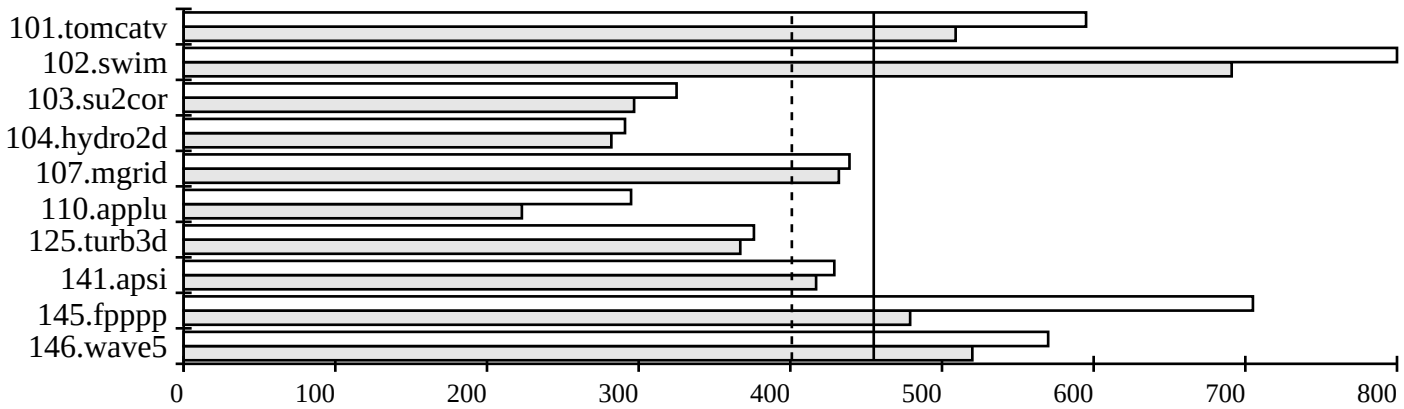
Sun Microsystems
Ultra 80 Model 2450

SPECfp_rate95 = 455

SPECfp_rate_base95 = 401

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Jul-99	Hardware Avail: Nov-99	Software Avail: May-99



Hardware/Software Configuration for:
Ultra 80 Model 2450

Hardware

Model Name:	Ultra 80 Model 2450
CPU:	450 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	2
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 18GB
Other Hardware:	Fast Ethernet System clock frequency: 113 MHz

Software

Operating System:	Solaris 7 SU2 (64-bit)
Compiler:	Sun Workshop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
101.tomcatv	2	131	509	2	112	595
102.swim	2	224	691	2	194	800
103.su2cor	2	84.9	297	2	78	325
104.hydro2d	2	153	282	2	149	291
107.mgrid	2	104	432	2	102	439
110.applu	2	178	223	2	134	295
125.turb3d	2	201	367	2	196	376
141.apsi	2	90.7	417	4	176	429
145.fpppp	2	360	479	2	245	705
146.wave5	2	104	520	2	95	570
SPECfp_rate_base95 (G. Mean)			401			
			SPECfp_rate95 (G. Mean)			455

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback; FOPTION= -fast -xarch=v8plusa -s
BCOPY = -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  ${FOPTION} -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2 ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
104.hydro2d: ${FOPTION} -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid:   ${FOPTION} -xprefetch -fsimple=2 ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  ${FOPTION} -stackvar -xpad -fsimple=2 -Qoption iropt -O4+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
141.apsi:    ${FOPTION} -fsimple=2 -stackvar  -Qoption iropt -Si36  ${INV} ${BMR} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   ${FOPTION} -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultra80fp.cfg
```

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