Agenda - Thursday October 27

13:00  Welcome to the SPEC Symposium’16 on Server Efficiency
       Klaus-Dieter Lange (HPE), SPECpower Chair

13:20  Designing a Good Benchmark – Part 2
       Jeremy Arnold (IBM), SPECpower Architect

14:00  Server Efficiency Rating Tool (SERT) Overview (Design, Metric)
       SPECpower Committee

15:00  Tea Break

15:30  ISO and Global Government Programs
       Henry Wong (ISO)

16:00  Trends in Server Efficiency and Power Usage in Data Centers
       Sanjay Sharma (Intel), SPECpower Vice-Chair

16:30  The Future of Server Efficiency and how it will be measured
       Klaus-Dieter Lange (HPE/SPEC), Henry Wong (ISO),
       David Reiner (AMD/TGG), and Sanjay Sharma (Intel)

17:30  Closing Remarks
## Industry Challenge

<table>
<thead>
<tr>
<th>Server Option</th>
<th>Platform A 18,831,605,760*</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU</strong></td>
<td>44 combinations</td>
</tr>
<tr>
<td></td>
<td>22 types, 1 or 2 populated</td>
</tr>
<tr>
<td><strong>Memory</strong></td>
<td>144 combinations</td>
</tr>
<tr>
<td></td>
<td>6 types, 1 to 24 populated</td>
</tr>
<tr>
<td><strong>Internal Storage</strong></td>
<td>1440 combinations</td>
</tr>
<tr>
<td></td>
<td>80 types, 1 to 18 populated</td>
</tr>
<tr>
<td><strong>Additional Card</strong></td>
<td>172 combinations</td>
</tr>
<tr>
<td></td>
<td>43 types, 4 slots</td>
</tr>
<tr>
<td><strong>PSUs</strong></td>
<td>12 combinations</td>
</tr>
<tr>
<td></td>
<td>6 types, 1 or 2 populated</td>
</tr>
</tbody>
</table>

*Realistic combinations
SPECpower Committee

Working Group was established on January 26, 2006

Create SPEC products and assist other subcommittees, groups and organizations in creating products for fairly and consistently reporting system energy or power usage.

Group was granted committee status on 4/27/2006

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Academic and Corporate Support over the years

ARM, AMD, Dell, Fujitsu, Hewlett Packard Enterprise, Intel, IBM, Microsoft, Oracle, and the University of Würzburg
Thank you!

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