HPC2002 Result

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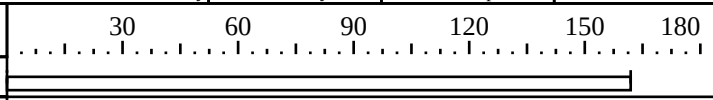
Dell
PowerEdge 1750 cluster

SPECseisM2002 = 162

SPEC license #: HPG0007A	Tested by: Purdue University	Test site: Purdue University	Test date: May-2005	HW Avail: Apr-2004	SW Avail: Mar-2005

Benchmark	Reference Time	Runtime	Ratio
351.seis_m	86400	534	162



Hardware

CPU:	Intel Pentium4 Xeon
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	16 cores, 16 chips, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1 or 2 per node
Primary Cache:	12KB (I) micro-ops (trace) + 8KB (D) on chip
Secondary Cache:	512KB on chip
L3 Cache:	1 MB on chip
Other Cache:	None
Memory:	2 GB DDR PC2100 CL2.5 ECC Registered per node
Disk Subsystem:	1x36 GB SCSI per node
Other Hardware:	see notes

Software

Parallel:	MPI
Processes-Threads:	16
MPI Processes:	16
OpenMP Threads:	-
Operating System:	RedHat Enterprise Linux, Advanced Server version 3 (4)
Compiler:	Intel C++ Compiler- icc, Version 8.1 Build 20050309Z for Linux Intel Fortran Compiler- ifort, Version 8.1 Build 20050309Z for Linux
File System:	NFS shared file system
System State:	Multi-user
Other Software:	mpich-vapi (see notes for configuration)

Notes/Tuning Information

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Tested by Purdue University
Approved Alternate Source: approved alternate source, "purdueintel",
was used to fix Fortran standard violations
Flags (Fortran & C):
CPP Flags: -I. -C -P -traditional -Dmpi -DSPEC_HPG_MPI
```

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Notes/Tuning Information (Continued)