OMPM2001 Result

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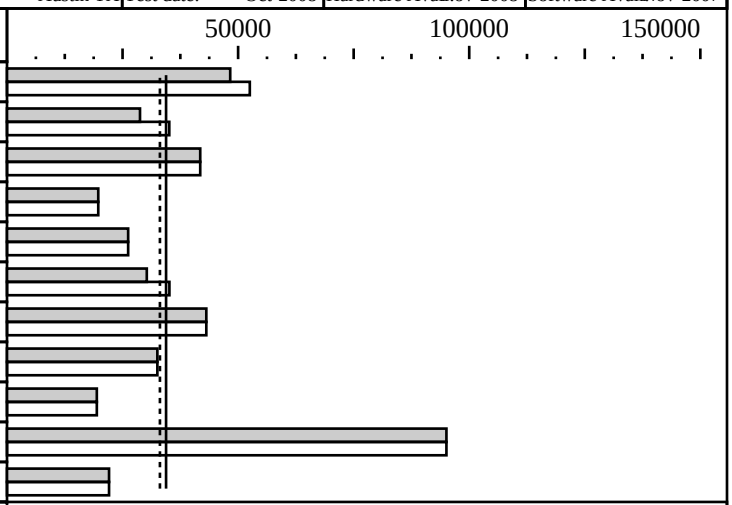
Supermicro Corporation
Supermicro H8QM8

SPECompMpeak2001 = 34415

SPECompMbase2001 = 33086

SPEC license #: HPG0017 | Tested by: Advanced Micro Devices | Test site: Austin TX | Test date: Oct-2008 | Hardware Avail: Nov-2008 | Software Avail: Nov-2007

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
310.wupwise_m	6000	124	48291	114	52530
312.swim_m	6000	209	28774	171	35108
314.mgrid_m	7300	175	41804	175	41804
316.applu_m	4000	203	19744	203	19744
318.galgel_m	5100	195	26212	195	26212
320.equake_m	2600	86.0	30244	74.0	35136
324.apsi_m	3400	78.8	43121	78.8	43121
326.gafort_m	8700	267	32531	267	32531
328.fma3d_m	4600	237	19432	237	19432
330.art_m	6400	67.3	95069	67.3	95069
332.ammp_m	7000	317	22077	317	22077



Hardware

CPU:	Quad-Core AMD Opteron(tm) Processor 8384
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1-4
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	32GB (16x2GB, PC2-6400, CL5 dual-rank)
Disk Subsystem:	160GB 7200 RPM SATA
Other Hardware:	None

Software

OpenMP Threads:	16
Parallel:	OpenMP
Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1 2.6.16.46-0.12-smp
Compiler:	PathScale Compiler Suite ver. 3.1
File System:	Linux/Ext3
System State:	Multi-User

Notes/Tuning Information

```
Extra art Base Flags
  330.art_m (extra): -DINTS_PER_CACHELINE=16 -DDBLS_PER_CACHELINE=8

Alternate Sources:
   314.mgrid_m (base): intel
   310.wupwise_m (peak), 312.swim_m (peak), 320.equake_m (peak): ompl.32
Base tuning:
  C       : pathcc -gnu3 -mp -Ofast -mcpu=barcelona -mcmodel=medium
  Fortran : pathf90 -mp -Ofast -mcpu=barcelona -OPT:early_mp=on -mcmodel=medium
  ONESTEP=yes
Peak Tuning:
   310.wupwise_m: -mp -Ofast -msse3 -LNO:prefetch_ahead=5:prefetch=3
                  -OPT:unroll_times_max=8:unroll_size=128:IEEE_NaN_Inf=off:ro=3
                  -IPA:linear=on:plimit=50000:callee_limit=5000 -INLINE:aggresive=on
   312.swim_m: -mp -O3 -OPT:Ofast -m3dnow -mcmodel=medium -LNO:simd=0
   314.mgrid_m: basepeak=yes
   316.applu_m: basepeak=yes
   318.galgel_m: basepeak=yes
   320.equake_m: -Ofast -mp -OPT:alias=disjoint
   324.apsi_m: basepeak=yes
   326.gafort_m: basepeak=yes
   328.fma3d_m: basepeak=yes
   330.art_m: basepeak=yes
```

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Notes/Tuning Information (Continued)

```
    332.ammp_m: basepeak=yes
    ONESTEP=yes for all peak tests.

Portablility flags:
 318.galgel_m    : -fixedform -col80

Base and Peak User Environment (bash):
  export OMP_NUM_THREADS=16
  export OMP_DYNAMIC=FALSE
  export PSC_OMP_AFFINITY=TRUE
  export PSC_OMP_STATIC_FAIR=TRUE
  export PSC_OMP_THREAD_SPIN=1000000000
  ulimit -s unlimited

For flag descriptions, see file AMD-Jul2008-psc31_flags.txt
Default BIOS settings used, except PowerNow! disabled in Bios
```