SGI
SGI Altix ICE 8200EX
(Intel Xeon X5570, 2.93 GHz)

MiPi2007 license: 4
Test sponsor: SGI
Tested by: SGI

SPECmpiM_peak2007 = Not Run
SPECmpiM_base2007 = 3.80

Test date: Feb-2009
Hardware Availability: Mar-2009
Software Availability: Jan-2009

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Ranks</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
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Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.
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(Intel Xeon X5570, 2.93 GHz)

SPEC MPI2007 Result

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Results Table (Continued)

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Hardware Summary
Type of System: Homogeneous
Compute Node: SGI Altix ICE 8200EX Compute Node
Interconnects: InfiniBand (MPI)
InfiniBand (I/O)
File Server Node: SGI InfiniteStorage Nexis 2000 NAS
Total Compute Nodes: 2
Total Chips: 4
Total Cores: 16
Total Threads: 32
Total Memory: 96 GB
Base Ranks Run: 32
Minimum Peak Ranks: --
Maximum Peak Ranks: --

Node Description: SGI Altix ICE 8200EX Compute Node

Hardware
Number of nodes: 2
Uses of the node: compute
Vendor: SGI
Model: SGI Altix ICE 8200EX (Intel Xeon X5570, 2.93 GHz)
CPU Name: Intel Xeon X5570
CPU(s) orderable: 1-2 chips
Chips enabled: 2
Cores enabled: 8
Cores per chip: 4
Threads per core: 2
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz, 6.4 GT/s QPI, Hyper-Threading enabled
CPU MHz: 2934
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12*4GB DDR3-1066 CL7 RDIMMs)
Disk Subsystem: None
Other Hardware: None
Adapter: Mellanox MT26418 ConnectX IB DDR (PCIe x8 Gen2 5 GT/s)
Number of Adapters: 1

Software
Adapter: Mellanox MT26418 ConnectX IB DDR (PCIe x8 Gen2 5 GT/s)
Adapter Driver: OFED-1.3.1
Adapter Firmware: 2.5.0
Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 kernel 2.6.16.60-0.30-smp
Local File System: NFSv3
Shared File System: NFSv3 IPoIB
System State: Multi-user, run level 3
Other Software: SGI ProPack 6 for Linux Service Pack 2

Continued on next page
## SPEC MPIM2007 Result

**SGI**

### SGI Altix ICE 8200EX

*(Intel Xeon X5570, 2.93 GHz)*

<table>
<thead>
<tr>
<th><strong>SPECmpIm_peak2007</strong></th>
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</tr>
</thead>
<tbody>
<tr>
<td><strong>SPECmpIm_base2007</strong></td>
<td><strong>= 3.80</strong></td>
</tr>
</tbody>
</table>

**MPI2007 license:** 4

**Test sponsor:** SGI

**Tested by:** SGI

---

### Node Description: SGI Altix ICE 8200EX Compute Node

- **Slot Type:** PCIe x8 Gen2
- **Data Rate:** InfiniBand 4x DDR
- **Ports Used:** 2
- **Interconnect Type:** InfiniBand

---

### Node Description: SGI InfiniteStorage Nexis 2000 NAS

#### Hardware

- **Number of nodes:** 1
- **Uses of the node:** fileserver
- **Vendor:** SGI
- **Model:** SGI Altix XE 240 *(Intel Xeon 5140, 2.33 GHz)*
- **CPU Name:** Intel Xeon 5140
- **CPU(s) orderable:** 1-2 chips
- **Chips enabled:** 2
- **Cores enabled:** 4
- **Cores per chip:** 2
- **Threads per core:** 1
- **CPU Characteristics:** 1333 MHz FSB
- **CPU MHz:** 2328
- **Primary Cache:** 32 KB L1 + 32 KB D on chip per core
- **Secondary Cache:** 4 MB L1+D on chip per chip
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 24 GB *(6*4GB DDR2-400 DIMMS)*
- **Disk Subsystem:** 7 TB RAID 5
  - 48 x 147 GB SAS *(Seagate Cheetah 15000 rpm)*
- **Other Hardware:** None
- **Adapter:** Mellanox MT25208 InfiniHost III Ex *(PCIe x8 Gen1 2.5 GT/s)*
- **Adapter Driver:** OFED-1.3
- **Adapter Firmware:** 5.3.0
- **Operating System:** SUSE Linux Enterprise Server 10 *(x86_64)* SP1
  - Kernel 2.6.16.54-0.2.5-smp
- **Local File System:** xfs
- **Shared File System:** --
- **System State:** Multi-user, run level 3
- **Other Software:** SGI ProPack 5 for Linux Service Pack 5

#### Software

---

### Interconnect Description: InfiniBand (MPI)

#### Hardware

- **Vendor:** Mellanox Technologies
- **Model:** MT26418 ConnectX
- **Switch Model:** Mellanox MT47396 InfiniScale III
- **Number of Switches:** 8
- **Number of Ports:** 24
- **Data Rate:** InfiniBand 4x DDR
- **Firmware:** 2020001

#### Software

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*Continued on next page*
### SPEC mpiM2007 Result

**SGI**

SGI Altix ICE 8200EX  
(Intel Xeon X5570, 2.93 GHz)

**SPECmpiM_peak2007 = Not Run**  
**SPECmpiM_base2007 = 3.80**

- **MPI2007 license:** 4  
- **Test date:** Feb-2009
- **Test sponsor:** SGI  
- **Hardware Availability:** Mar-2009
- **Tested by:** SGI  
- **Software Availability:** Jan-2009

### Interconnect Description: InfiniBand (MPI)

<table>
<thead>
<tr>
<th>Topology</th>
<th>Bristle hypercube with express links</th>
</tr>
</thead>
<tbody>
<tr>
<td>Primary Use</td>
<td>MPI traffic</td>
</tr>
</tbody>
</table>

### Interconnect Description: InfiniBand (I/O)

**Hardware**

<table>
<thead>
<tr>
<th>Vendor</th>
<th>Mellanox Technologies</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model</td>
<td>MT26418 ConnectX</td>
</tr>
<tr>
<td>Switch Model</td>
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<td>Number of Switches</td>
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<td>Data Rate</td>
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</tr>
<tr>
<td>Primary Use</td>
<td>I/O traffic</td>
</tr>
</tbody>
</table>

**Software**

### Submit Notes

The config file option 'submit' was used.

### General Notes

Software environment:

```bash
setenv MPI_REQUEST_MAX 65536  
Determine the maximum number of nonblocking sends and 
receives that can simultaneously exist for any single MPI process. MPI generates an error message if this limit 
(or the default, if not set) is exceeded. Default: 16384
```

```bash
setenv MPI_TYPE_MAX 32768  
Determine the maximum number of data types that can 
simultaneously exist for any single MPI process. MPI generates an error message if this limit (or the default, 
if not set) is exceeded. Default: 1024
```

```bash
setenv MPI_BUFS_THRESHOLD 1  
Determines whether MPT uses per-host or per-process message 
bufers for communicating with other hosts. Per-host buffers 
are generally faster but for jobs running across many hosts they 
can consume a prodigious amount of memory. MPT will use per-host 
bufers for jobs running up to and including this many hosts 
and will use per-process buffers for larger host counts. Default: 64
```

```bash
setenv MPI_DSM_DISTRIBUTE  
Activates NUMA job placement mode. This mode ensures that each 
MPI process gets a unique CPU and physical memory on the node 
with that CPU is associated. Currently, the CPUs are 
chosen by simply starting at relative CPU 0 and incrementing
```

Continued on next page
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Software Availability: Jan-2009

General Notes (Continued)

until all MPI processes have been forked.

limit stacksize unlimited
Removes limits on the maximum size of the automatically-
extended stack region of the current process and each
process it creates.
PBS Pro batch scheduler (www.altair.com) is used with
placement sets to ensure each MPI job is assigned to
a topologically compact set of nodes
BIOS settings:
   AMI BIOS version 8.15
   Hyper-Threading Technology enabled (default)
   Intel Turbo Boost Technology enabled (default)
   Intel Turbo Boost Technology activated in the OS via
      /etc/init.d/acpid start
      /etc/init.d/powersaved start
      powersave -f

Base Compiler Invocation

C benchmarks:
   icc

C++ benchmarks:
   126.lammps: icpc

Fortran benchmarks:
   ifort

Benchmarks using both Fortran and C:
   icc ifort

Base Portability Flags

121.pop2: -DSPEC_MPI_CASE_FLAG
127.wrf2: -DSPEC_MPI_CASE_FLAG -DSPEC_MPI_LINUX

Base Optimization Flags

C benchmarks:
   -O3 -ipo -xT -no-prec-div

C++ benchmarks:

Continued on next page
Base Optimization Flags (Continued)

126.lammps: -O3 -ipo -xT -no-prec-div -ansi-alias

Fortran benchmarks:
-03 -ipo -xT -no-prec-div

Benchmarks using both Fortran and C:
-03 -ipo -xT -no-prec-div

Base Other Flags

C benchmarks:
-1mpi

C++ benchmarks:

126.lammps: -1mpi

Fortran benchmarks:
-1mpi

Benchmarks using both Fortran and C:
-1mpi

The flags file that was used to format this result can be browsed at
http://www.spec.org/mpi2007/flags/EM64T_Intel101_flags.20080611.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/mpi2007/flags/EM64T_Intel101_flags.20080611.xml