SPEC® MPIM2007 Result

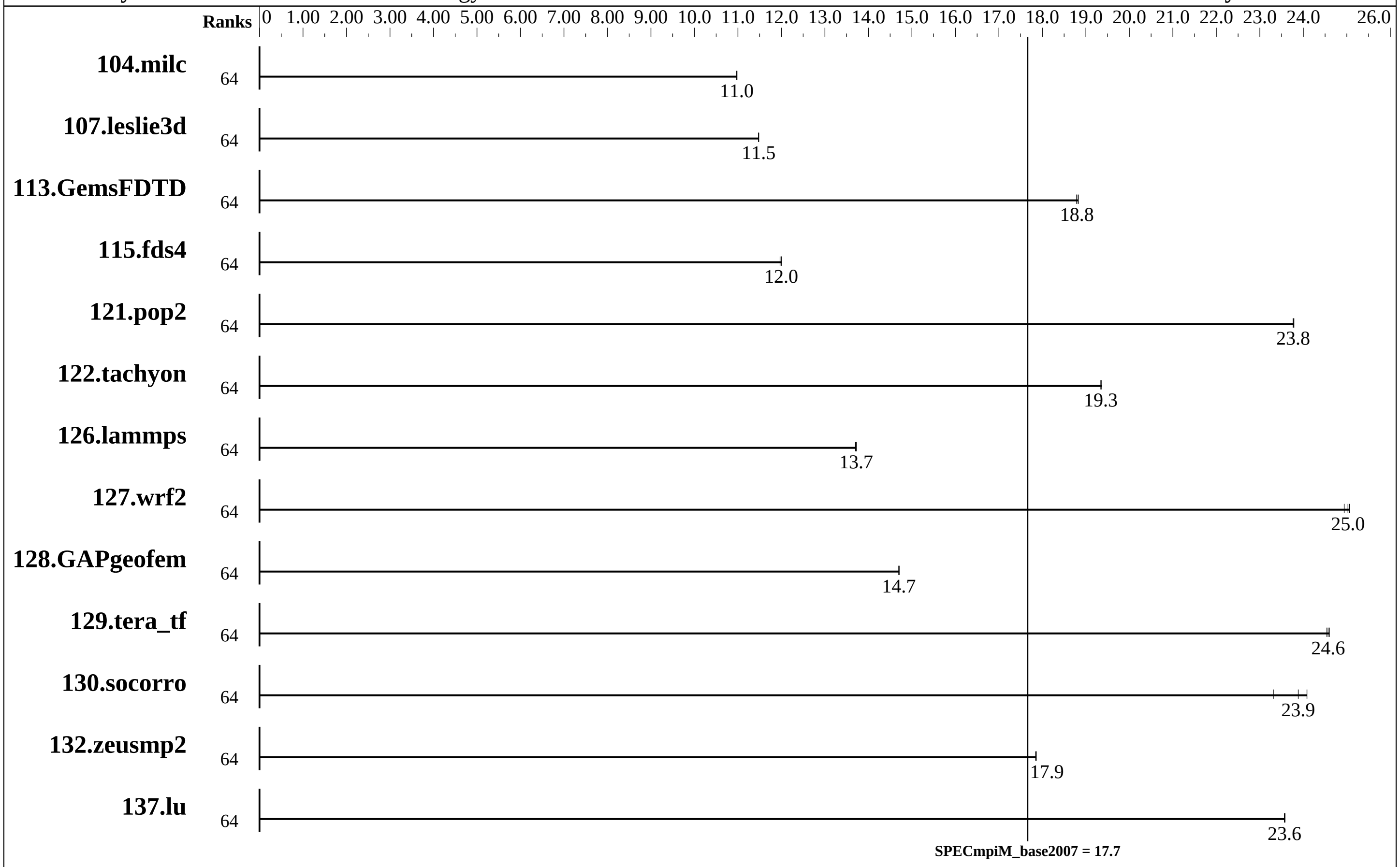
Lenovo Global Technology

ThinkSystem SR655
(AMD EPYC 7H12 CPU, 2.6 GHz)

SPECmpiM_peak2007 = Not Run

SPECmpiM_base2007 = 17.7

MPI2007 license: 28		**Test date:**	Jan-2020
Test sponsor:	Lenovo Global Technology	**Hardware Availability:**	Jun-2020
Tested by:	Lenovo Global Technology	**Software Availability:**	Dec-2018



Results Table

Benchmark	Base							Peak						
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
104.milc	64	143	11.0	143	11.0	**143**	**11.0**							
107.leslie3d	64	**455**	**11.5**	455	11.5	455	11.5							
113.GemsFDTD	64	**336**	**18.8**	336	18.8	335	18.8							
115.fds4	64	163	12.0	162	12.0	**163**	**12.0**							
121.pop2	64	174	23.8	174	23.8	**174**	**23.8**							
122.tachyon	64	**145**	**19.3**	144	19.4	145	19.3							
126.lammps	64	213	13.7	212	13.7	**212**	**13.7**							
127.wrf2	64	**312**	**25.0**	313	24.9	311	25.1							
128.GAPgeofem	64	140	14.7	**140**	**14.7**	141	14.7							
129.tera_tf	64	**113**	**24.6**	113	24.5	113	24.6							

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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Results Table (Continued)

	Base							Peak						
Benchmark	**Ranks**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Ranks**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
130.socorro	64	**160**	**23.9**	158	24.1	164	23.3							
132.zeusmp2	64	**174**	**17.9**	174	17.9	174	17.8							
137.lu	64	**156**	**23.6**	156	23.6	156	23.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Hardware Summary

Type of System:	Homogeneous
Compute Node:	ThinkSystem SR655
Total Compute Nodes:	1
Total Chips:	1
Total Cores:	64
Total Threads:	64
Total Memory:	256 GB
Base Ranks Run:	64
Minimum Peak Ranks:	--
Maximum Peak Ranks:	--

Software Summary

C Compiler:	Intel C++ Compiler 17.0 Update 7 for Linux Version 17.0.7 Build 20180403
C++ Compiler:	Intel C++ Compiler 17.0 Update 7 for Linux Version 17.0.7 Build 20180403
Fortran Compiler:	Intel Fortran Compiler 17.0 Update 7 for Linux Version 17.0.7 Build 20180403
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
MPI Library:	Intel MPI Library for Linux* OS Version 2018 Update 3 Build 20180411
Other MPI Info:	None
Pre-processors:	No
Other Software:	None

Node Description: ThinkSystem SR655

Hardware

Number of nodes:	1
Uses of the node:	compute
Vendor:	Lenovo Global Technology
Model:	SR655
CPU Name:	AMD EPYC 7H12 CPU
CPU(s) orderable:	1 chips
Chips enabled:	1
Cores enabled:	64
Cores per chip:	64
Threads per core:	1
CPU Characteristics:	None
CPU MHz:	2600
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	256 MB I+D on chip per chip 16 MB shared / 4 cores
Other Cache:	None
Memory:	256 GB (8 x 32 GB 2Rx8 PC4-3200AA-R)
Disk Subsystem:	1 x 480 GB SATA 2.5" SSD
Other Hardware:	None
Adapter:	None
Number of Adapters:	0
Slot Type:	None
Data Rate:	None

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Software

Adapter:	None
Adapter Driver:	None
Adapter Firmware:	None
Operating System:	SUSE Linux Enterprise Linux Server 12 SP 4 4.12.14-94.41-default
Local File System:	xfs
Shared File System:	None
System State:	Multi-user, run level 3
Other Software:	None

Node Description: ThinkSystem SR655

Ports Used:	0
Interconnect Type:	None

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
MPI startup command:
  mpiexec command was used to start MPI jobs.

RAM configuration:
  Compute nodes have 1 x 32 GB RDIMM on each memory channel.

Add "idle=poll" into grub

BIOS settings:
  Operating Mode : Maximum Performance Mode
  Hyper-Threading Technology (SMT): Disabled
  NPS4


Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
```

Base Compiler Invocation

C benchmarks:
`mpiicc`

C++ benchmarks:

126.lammps: `mpiicpc`

Fortran benchmarks:
`mpiifort`

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
`mpiicc mpiifort`

Base Portability Flags

121.pop2: `-DSPEC_MPI_CASE_FLAG`
126.lammps: `-DMPICH_IGNORE_CXX_SEEK`
127.wrf2: `-DSPEC_MPI_CASE_FLAG -DSPEC_MPI_LINUX`
130.socorro: `-assume nostd_intent_in`

Base Optimization Flags

C benchmarks:
`-O3 -march=core-avx2 -no-prec-div -ipo`

C++ benchmarks:

126.lammps: `-O3 -march=core-avx2 -no-prec-div -ipo`

Fortran benchmarks:
`-O3 -march=core-avx2 -no-prec-div -ipo`

Benchmarks using both Fortran and C:
`-O3 -march=core-avx2 -no-prec-div -ipo`

The flags files that were used to format this result can be browsed at
http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20190807.html
http://www.spec.org/mpi2007/flags/Lenovo-SPECmpiM_Platform_Flags.20190807.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20190807.xml
http://www.spec.org/mpi2007/flags/Lenovo-SPECmpiM_Platform_Flags.20190807.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC MPI2007 v2.0.1.
Report generated on Mon Mar 16 10:19:12 2020 by SPEC MPI2007 PS/PDF formatter v1463.
Originally published on 12 March 2020.