



SPEC® MPIM2007 Result

Copyright 2006-2010 Standard Performance Evaluation Corporation

Lenovo Global Technology

[SPECmpM_peak2007 = Not Run](#)

ThinkSystem SR665
(AMD EPYC 7H12, 2.6 GHz)

[SPECmpM_base2007 = 51.8](#)

MPI2007 license: 28

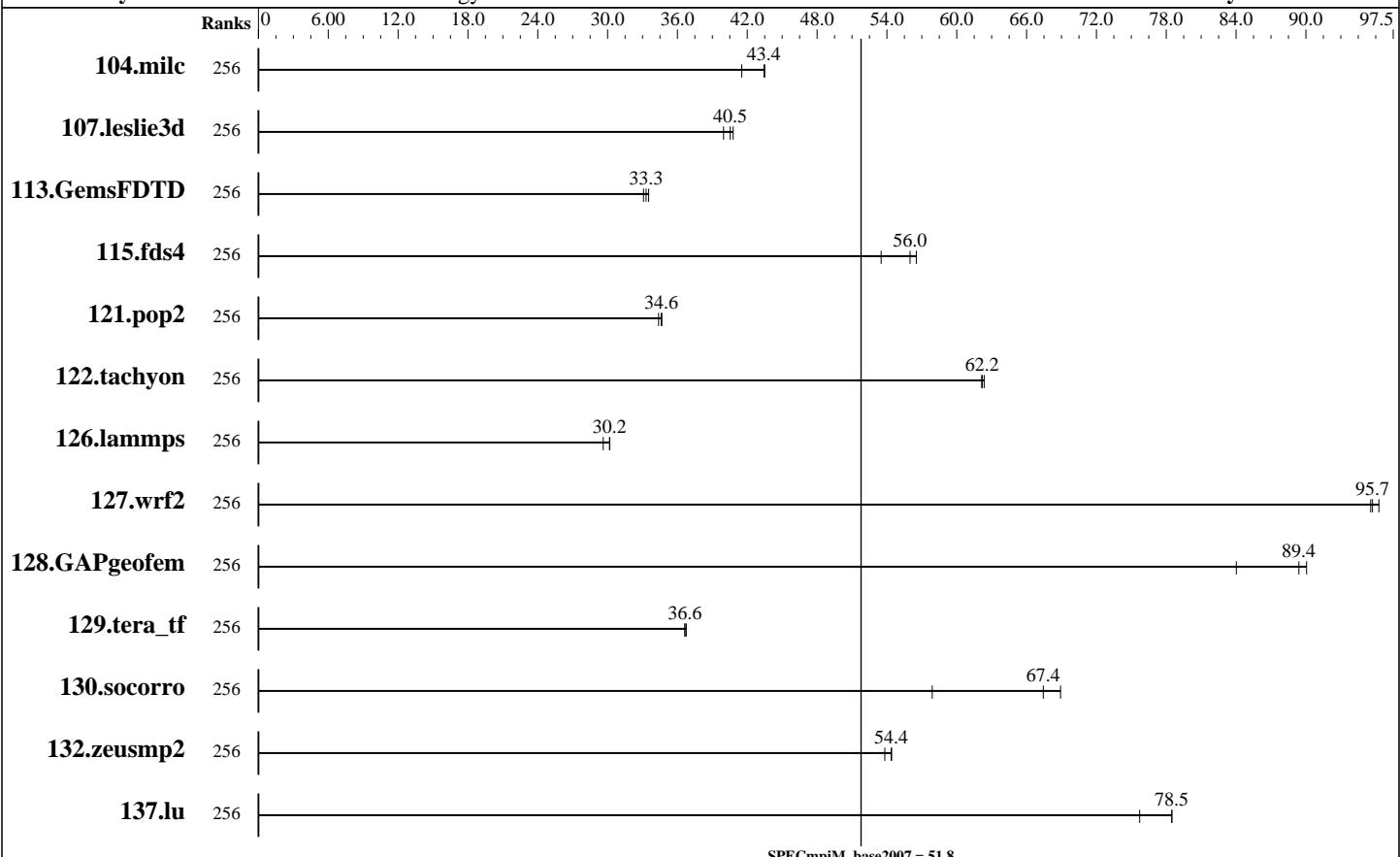
Test date: Jan-2020

Test sponsor: Lenovo Global Technology

Hardware Availability: Jun-2020

Tested by: Lenovo Global Technology

Software Availability: Jun-2020



Results Table

Benchmark	Base								Peak							
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
104.milc	256	36.0	43.5	37.7	41.5	36.0	43.4									
107.leslie3d	256	128	40.8	131	40.0	129	40.5									
113.GemsFDTD	256	188	33.5	189	33.3	191	33.1									
115.fds4	256	34.5	56.5	34.8	56.0	36.5	53.5									
121.pop2	256	119	34.6	120	34.4	119	34.7									
122.tachyon	256	45.0	62.1	45.0	62.2	44.9	62.4									
126.lammps	256	96.6	30.2	98.4	29.6	96.6	30.2									
127.wrf2	256	81.0	96.3	81.6	95.6	81.4	95.7									
128.GAPgeomfem	256	22.9	90.1	24.6	84.0	23.1	89.4									
129.tera_tf	256	75.5	36.6	75.6	36.6	75.3	36.8									

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Results Table (Continued)

Benchmark	Base								Peak							
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
130.socorro	256	56.6	67.4	65.9	57.9	55.4	68.9									
132.zeusmp2	256	57.0	54.4	57.7	53.8	57.1	54.4									
137.lu	256	46.8	78.5	46.8	78.5	48.5	75.7									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Hardware Summary

Type of System: Homogeneous
Compute Node: ThinkSystem SR665
Interconnect: Mellanox ConnectX-6 HDR
File Server Node: NFS
Total Compute Nodes: 2
Total Chips: 4
Total Cores: 256
Total Threads: 256
Total Memory: 2 TB
Base Ranks Run: 256
Minimum Peak Ranks: --
Maximum Peak Ranks: --

Software Summary

C Compiler: AMD Optimizing C Compiler for Linux Version 2.1 Build 1030.2019_11_12
C++ Compiler: AMD Optimizing C++ Compiler for Linux Version 2.1 Build 1030.2019_11_12
Fortran Compiler: AMD Optimizing Fortran Compiler for Linux Version 2.1 Build 1030.2019_11_12
Base Pointers: 64-bit
Peak Pointers: Not Applicable
MPI Library: OpenMPI MPI Library Version 4.0.2
Other MPI Info: None
Pre-processors: No
Other Software: None

Node Description: ThinkSystem SR665

Hardware

Number of nodes: 2
Uses of the node: compute
Vendor: Lenovo Global Technology
Model: SR665
CPU Name: AMD EPYC 7H12
CPU(s) orderable: 1-2 chips
Chips enabled: 2
Cores enabled: 128
Cores per chip: 64
Threads per core: 1
CPU Characteristics: None
CPU MHz: 2600
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 512 KB I+D on chip per core
L3 Cache: 256 MB I+D on chip per chip
16 MB shared / 4 cores
Other Cache: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
Disk Subsystem: 1 x 480 GB SATA 2.5" SSD
Other Hardware: None
Adapter: Mellanox ConnectX-6 HDR Infiniband
Number of Adapters: 1
Slot Type: PCI-Express 4.0 x16

Software

Adapter: Mellanox ConnectX-6 HDR Infiniband
Adapter Driver: 4.7-1.0.0.1.2
Adapter Firmware: 20.25.2006
Operating System: Red Hat Enterprise Linux Server release 8.1, 4.18.0-147.el8.x86_64
Local File System: xfs
Shared File System: None
System State: Multi-user, run level 3
Other Software: None

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Tested by: Lenovo Global Technology

Software Availability: Jun-2020

Node Description: ThinkSystem SR665

Data Rate: 200 Gbs/s
Ports Used: 1
Interconnect Type: Mellanox ConnectX-6 HDR Infiniband Adapter

Node Description: NFS

Hardware	
Number of nodes:	1
Uses of the node:	Fileserver
Vendor:	Lenovo Global Technology
Model:	ThinkSystem SR665
CPU Name:	AMD EPYC 7H12 CPU
CPU(s) orderable:	1-2 chips
Chips enabled:	2
Cores enabled:	128
Cores per chip:	64
Threads per core:	1
CPU Characteristics:	None
CPU MHz:	2600
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	256 MB I+D on chip per chip
Other Cache:	16 MB shared / 4 cores
Memory:	1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
Disk Subsystem:	1 x 480 GB SATA 2.5" SSD
Other Hardware:	None
Adapter:	Mellanox ConnectX-6 HDR Infiniband
Number of Adapters:	1
Slot Type:	PCI-Express 4.0 x16
Data Rate:	200 Gb/s
Ports Used:	1
Interconnect Type:	Mellanox ConnectX-6 HDR Infiniband

Software	
Adapter:	Mellanox ConnectX-6 HDR Infiniband
Adapter Driver:	4.7-1.0.0.1.2
Adapter Firmware:	20.25.2006
Operating System:	Red Hat Enterprise Linux Server release 8.1
Local File System:	None
Shared File System:	NFS
System State:	Multi-User, run level 3
Other Software:	None

Interconnect Description: Mellanox ConnectX-6 HDR

Hardware	
Vendor:	Mellanox
Model:	Infiniband EDR 100Gb/s Switch
Switch Model:	SB7800 Series
Number of Switches:	1
Number of Ports:	36
Data Rate:	100 Gb/s
Firmware:	3.9.0300
Topology:	Mesh

Software

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Interconnect Description: Mellanox ConnectX-6 HDR

Primary Use: MPI Traffic

Submit Notes

The config file option 'submit' was used.

General Notes

MPI startup command:

mpixec command was used to start MPI jobs.

RAM configuration:

Compute nodes have 1 x 32 GB RDIMM on each memory channel.

Add "idle=poll" into grub

BIOS settings:

Operating Mode : Maximum Performance Mode

Hyper-Threading Technology (SMT): Enabled

NPS4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,

that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,

that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Base Compiler Invocation

C benchmarks:

/opt/openmpi/0402_A21_H47_RH81/bin/mpicc

C++ benchmarks:

126.lammps: /opt/openmpi/0402_A21_H47_RH81/bin/mpicxx

Fortran benchmarks:

/opt/openmpi/0402_A21_H47_RH81/bin/mp_ifort

Benchmarks using both Fortran and C:

/opt/openmpi/0402_A21_H47_RH81/bin/mpicc

/opt/openmpi/0402_A21_H47_RH81/bin/mp_ifort

Base Portability Flags

121.pop2: -DSPEC_MPI_CASE_FLAG

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Base Portability Flags (Continued)

126.lammps: -DMPICH_IGNORE_CXX_SEEK

127.wrf2: -DSPEC_MPI_CASE_FLAG -DSPEC_MPI_LINUX -Wno-return-type

Base Optimization Flags

C benchmarks:

-Ofast -festo -ffast-math -march=znver2 -mavx2

C++ benchmarks:

126.lammps: -Ofast -festo -ffast-math -march=znver2 -mavx2

Fortran benchmarks:

-Ofast -festo -ffast-math -march=znver2 -mavx2

Benchmarks using both Fortran and C:

-Ofast -festo -ffast-math -march=znver2 -mavx2

The flags file that was used to format this result can be browsed at

http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20200506.01.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20200506.01.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC MPI2007 v2.0.1.

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