



OMPL2001 Result

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SGI
SGI 3800 128X 400MHz R12K

SPECompLpeak2001 = --
SPECompLbase2001 = 168963

SPEC license #HPG0014 | Tested by: SGI | Test site: SGI | Test date: May-2002 | Hardware Avail: Sep-2000 | Software Avail: Apr-2002

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio	
311.wupwise_1	9200	683	215409			
313.swim_1	12500	1028	194465			
315.mgrid_1	13500	954	226529			
317.applu_1	13500	1288	167723			
321.equake_1	13000	1922	108232			
325.apsi_1	10500	1222	137463			
327.gafort_1	11000	1223	143855			
329.fma3d_1	23500	2291	164106			
331.art_1	25000	1992	200772			

Hardware

CPU: R12000
 CPU MHz: 400
 FPU: Integrated
 CPU(s) enabled: 128
 CPU(s) orderable: 4-512
 Primary Cache: 32KBI + 32KBD on chip
 Secondary Cache: 8MB(I+D) off chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 128 GB
 Disk Subsystem: 1 x 9 GB FC, 24x 36 GB (striped) FC
 Other Hardware: None

Software

OpenMP Threads: 128
 Parallel: OpenMP
 Operating System: IRIX 6.5.16f
 Compiler: MIPSpro 7.3.1.3m C, Fortran90
 File System: xfs
 System State: Multi-user

Notes/Tuning Information

Baseline flags: -64 -mp -Ofast=ip35 -LNO:fusion=2

Baseline user environment:

```
OMP_DYNAMIC=FALSE
MPC_GANG=OFF
_DSM_MUSTRUN=
_DSM_WAIT=SPIN
```