OMPM2001 Result

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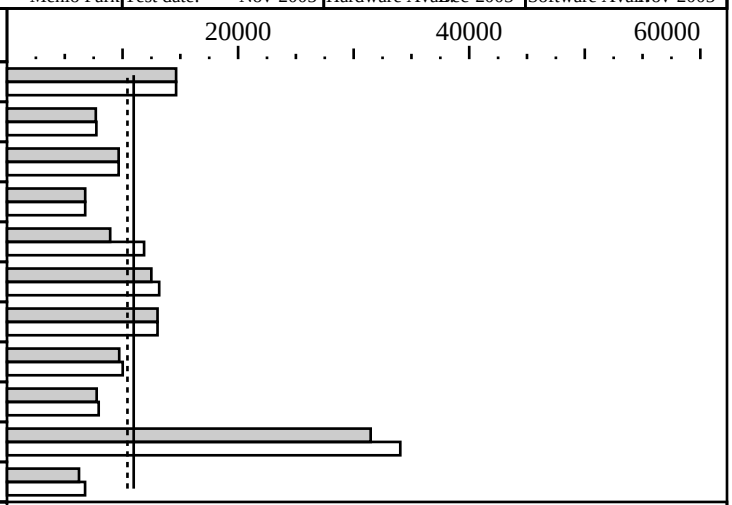
Sun Microsystems
Sun Fire X4200

SPECompMpeak2001 = 10964

SPECompMbase2001 = 10424

SPEC license #:	HPG0010	Tested by:	Sun Microsystems, Santa Clara	Test site:	Menlo Park	Test date:	Nov-2005	Hardware Avail:	Dec-2005	Software Avail:	Nov-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
310.wupwise_m	6000	410	14635	410	14635
312.swim_m	6000	781	7685	776	7734
314.mgrid_m	7300	755	9663	755	9663
316.applu_m	4000	591	6767	591	6767
318.galgel_m	5100	571	8929	430	11863
320.equake_m	2600	208	12491	197	13171
324.apsi_m	3400	261	13027	261	13027
326.gafort_m	8700	896	9711	869	10013
328.fma3d_m	4600	593	7760	580	7934
330.art_m	6400	203	31471	188	34035
332.ammp_m	7000	1123	6231	1036	6757



Hardware

CPU:	AMD Opteron (TM) 285 SE
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 (order by # of chips)
Primary Cache:	64KBI + 64KBD (on chip) per core
Secondary Cache:	1024KB (I+D) (on chip) per core
L3 Cache:	None
Other Cache:	None
Memory:	16GB (8x2GB, PC3200 CL3 DDR SDRAM ECC Registered)
Disk Subsystem:	SAS, 36GB, 10K RPM
Other Hardware:	None

Software

OpenMP Threads:	4
Parallel:	OpenMP
Operating System:	Solaris 10 3/05 HW1
Compiler:	Sun Studio 11
File System:	ufs
System State:	Multi-User

Notes/Tuning Information

```
*****************************************************************
*vvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvvv*
     This config uses the -xalias_level=strong flag for "base"
     and is in violation of run rule 2.2.4.  It is unacceptable
     as a base flag for future submissions.  This submission was
     accepted prior to this rule clarification and it has been
     established that any variation in performance due to this
     flag was within the range of tolerance for reproducibility.
*^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^^*
*****************************************************************
 Compiler Invocation:
   C   : cc
   F90 : f90
   F77 : f90

 Base tuning:
   Fortran : -fast -xipo=2 -xarch=amd64 -xprefetch_level=3 -xvector=simd -xopenmp
   C       : -fast -xipo=2 -xalias_level=strong -xarch=amd64 -xopenmp
   ONESTEP=yes

 +FDO:  PASS1= -xprofile=collect:./feedback PASS2= -xprofile=use:./feedback
```

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SPEC license #1 HPG0010 | Tested by: Sun Microsystems, Santa Clara | Test site: Menlo Park | Test date: Nov-2005 | Hardware Avail: Dec-2005 | Software Avail: Nov-2005

Notes/Tuning Information (Continued)

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Peak tuning:
 ONESTEP=yes for all peak tests.

 310.wupwise_m   : basepeak=yes
 312.swim_m      : -fast -xipo=2 -xarch=amd64 -xprefetch_level=3 -xvector=simd -xopenmp
 314.mgrid_m     : basepeak=yes
 316.applu_m     : basepeak=yes
 318.galgel_m    : -fast -xipo=2 -xprefetch_level=3 -xvector=simd
                   -xarch=amd64 -xopenmp -xpagesize=2m -xlic_lib=sunperf +FDO
                    RM_SOURCES=lapak.f90
 320.equake_m    : -fast -xipo=2 -xalias_level=strong  -xarch=amd64 -xopenmp
 324.apsi_m      : basepeak=yes
 326.gafort_m    : -fast -xipo=2 -xarch=amd64 -xprefetch_level=3 -xvector=simd
                    -xopenmp -xpagesize=2m
 328.fma3d_m     : -fast -xipo=2 -xprefetch_level=3 -xarch=amd64 -xopenmp -xpagesize=2m +FDO
 330.art_m       : -fast -xcrossfile -xarch=amd64 -xalias_level=std -xopenmp
                   -xpagesize=2m -lmtmalloc -lm
 332.ammp_m      : -fast -xipo=2 -xarch=amd64 -xalias_level=std -xopenmp -xpagesize=2m -lmopt -lm

Portablility flags:
      318.galgel_m      : -e -fixed
      330.art_m         : Extra Flag: -DINTS_PER_CACHELINE=16 -DDBLS_PER_CACHELINE=8

Alternate Source:
      330.art_m         : Required srcalt purdue1 used for base and peak
      312.swim_m        : Approved srcalt ompl.32 used for peak
      320.equake_m      : Approved srcalt ompl.32 used for peak

Base and Peak User Environment:
  export OMP_NUM_THREADS=4
  export SUNW_MP_PROCBIND=TRUE
  export SUNW_MP_THR_IDLE=SPIN
  export OMP_NESTED=FALSE
  export STACKSIZE=16384
  export OMP_DYNAMIC=TRUE
  ulimit -s unlimited

For a description of Sun Studio 11 Compiler flags, portability flags
and system parameters used to generate this result, please refer to
SUN-20051207-Studio-Solaris-opteron.txt file in the flags directory.

Default BIOS settings used.

This result was measured on Sun Fire X4200;
Sun Fire X4100 with AMD Opteron (TM) 285 SE and Sun Fire X4200 with
AMD Opteron (TM) 285 SE are electronically equivalent.
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