OMPL2001 Result

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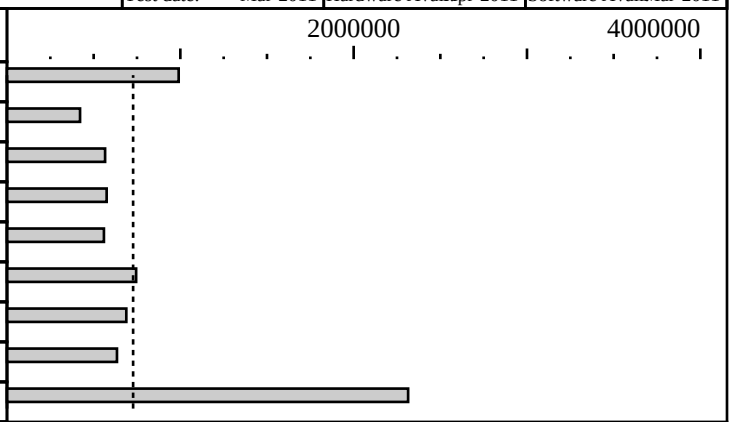
Cisco
C460 M2

SPECompLpeak2001 = --

SPECompLbase2001 = 727635

SPEC license #: HPG9019 | Tested by: Cisco | Test site: -- | Test date: Mar-2011 | Hardware Avail: Apr-2011 | Software Avail: Mar-2011

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
311.wupwise_l	9200	149	990418		
313.swim_l	12500	475	421416		
315.mgrid_l	13500	382	565944		
317.applu_l	13500	376	574931		
321.equake_l	13000	372	559186		
325.apsi_l	10500	226	744997		
327.gafort_l	11000	256	688328		
329.fma3d_l	23500	593	634455		
331.art_l	25000	173	2314185		



Hardware

CPU:	Intel Xeon processor E7-4870
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip, 2 thread/core
CPU(s) orderable:	1,2
Primary Cache:	32KB(I)+32KB(D)/core on chip
Secondary Cache:	256KB/core (I+D) on chip
L3 Cache:	30720KB (I+D) on chip
Other Cache:	N/A
Memory:	1024 GB (RDIMM 64x16-GB 1066 MHz)
Disk Subsystem:	LSI MRSASRoMB-8i (scsi)
Other Hardware:	

Software

OpenMP Threads:	80
Parallel:	OpenMP
Operating System:	Red Hat Enterprise Linux Server release 5.5, kernel 2.6.18-194.el5 x86_64
Compiler:	Intel C/C++ Compiler 12.0.2.137 for Linux Intel FORTRAN Compiler 12.0.2.137 for Linux GNU C Compiler 4.1.2
File System:	Linux ext3
System State:	Default

Notes/Tuning Information

```
================================================================================
BIOS settings notes:
  Intel Hyper-Threading Technology (SMT): Enabled
  Intel Turbo Boost Technology (Turbo)  : Enabled (Max 2.8 GHz)

================================================================================
Extra Flags:
  331.art_l: -DINTS_PER_CACHELINE=16 -DDBLS_PER_CACHELINE=8
  all: -gcc-name=/usr/bin/gcc

================================================================================
General Notes and Enviroment variables
  export KMP_LIBRARY=turnaround
  export KMP_STACKSIZE=31M
  export KMP_BLOCKTIME=infinite
  export OMP_DYNAMIC=FALSE
  ONESTEP=yes
  ulimit -s 64000

For compiler/openmp flags description please refer:
Intel-ic12.0-linux64-revB.html

Base optimization flags and enviroment variables:
================================================================================
Large:
  OPTIMIZE  = -O3 -xSSE4.2 -ipo -openmp -mcmodel=medium -shared-intel
```

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Notes/Tuning Information (Continued)

```
COPTIMIZE  = -ansi-alias
export KMP_AFFINITY=compact,1
```