Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8180, 2.50 GHz)

SPECompG_peak2012 = Not Run
SPECompG_base2012 = 39.0

<table>
<thead>
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</tbody>
</table>

Base Threads Run: 224
Minimum Peak Threads: 224

Hardware
CPU Name: Intel Xeon Platinum 8180
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 2500
CPU MHz Maximum: 3800
FPU: Integrated
CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 Chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 38.5 MB I+D on chip per chip
Other Cache: None
Memory: 768 GB (48 x 16 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 X 400 GB SSD SAS
Other Hardware: None
Base Threads Run: 224

Software
Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64)
Compiler: C/C++/Fortran: Version 18.0.0.082 of Intel Composer
Auto Parallel: No
File System: xfs
System State: Run Level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None
## SPEC OMPG2012 Result

### Cisco Systems

Cisco UCS C480 M5 (Intel Xeon Platinum 8180, 2.50 GHz)

<table>
<thead>
<tr>
<th>OMP2012 license:9019</th>
<th>Test date:</th>
<th>Jun-2017</th>
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<td>Hardware Availability:</td>
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<td>Maximum Peak Threads: --</td>
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### SPECompG_peak2012 = Not Run

### SPECompG_base2012 = 39.0

## Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Sysinfo program /opt/omp2012/Docs/sysinfo
$Rev: 395 $ $Date:: 2012-07-25 #$ 8f8c0fe9e19c658963a1e67685e50647
running on linux-wjnw Tue Jun 20 22:49:25 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/omp2012/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
  - 4 "physical id"s (chips)
  - 224 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

- cpu cores : 28
- siblings : 56
- physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
- physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
- physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30

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**SPEC OMPG2012 Result**

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**Platform Notes (Continued)**

```
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
```

**General Notes**

- **BIOS settings notes:**
  - Transparent Huge Pages enabled with:
    - `echo always > /sys/kernel/mm/transparent_hugepage/enabled`
  - **BIOS settings notes:**

  (Continued on next page)
Cisco Systems
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**General Notes (Continued)**

- Intel Turbo Boost Technology (Turbo) : Enabled
- CPU performance set to Enterprise
- Power Performance Tuning set to OS
- SNC set to Disabled
- IMC Interleaving set to Auto

**General OMP Library Settings**
- ENV_KMP_LIBRARY=turnaround
- ENV_OMP_SCHEDULE=static
- ENV_KMP_BLOCKTIME=200
- ENV_KMP_STACKSIZE=16G
- ENV_OMP_DYNAMIC=FALSE
- ENV_OMP_NESTED=FALSE

**General base OMP Library Settings**
- ENV_KMP_AFFINITY=compact,1

**Base Compiler Invocation**

- C benchmarks: icc
- C++ benchmarks: icpc
- Fortran benchmarks: ifort

**Base Portability Flags**

- 350.md: -FR
- 357.bt331: -mcmode=medium
- 363.swim: -mcmode=medium
- 367.imagick: -std=c99

**Base Optimization Flags**

- C benchmarks: -O3 -qopenmp -ipo -xHOST -ansi-alias
- C++ benchmarks: -O3 -qopenmp -ipo -xHOST -ansi-alias
- Fortran benchmarks: -O3 -qopenmp -ipo -xHOST -align array64byte
## SPEC OMPG2012 Result

### Cisco Systems

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The flags file that was used to format this result can be browsed at:

http://www.spec.org/omp2012/flags/Intel-ic13.0-linux64.20170711.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/omp2012/flags/Intel-ic13.0-linux64.20170711.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC OMP2012 v1.0.