CINT2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

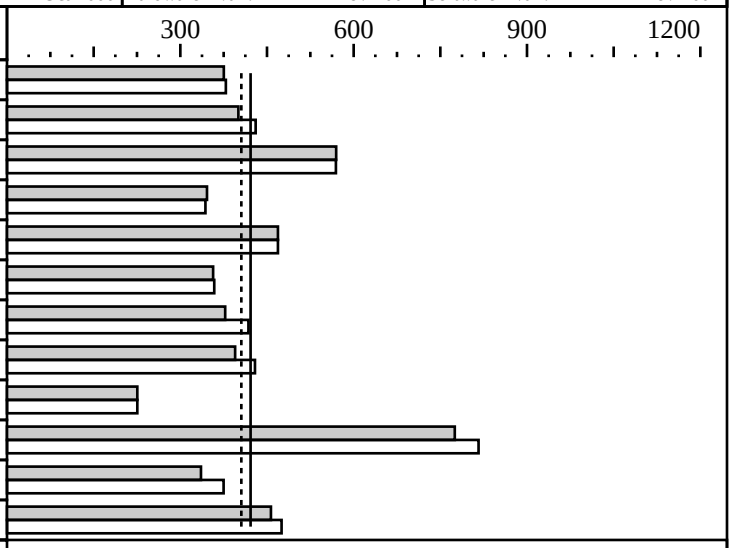
Hewlett-Packard Company
HP 9000 Model A500

SPECint2000 = 422

SPECint_base2000 = 406

SPEC license #: 3 | Tested by: HP Richardson, TX | Test date: Oct-2000 | Hardware Avail: Nov-2001 | Software Avail: Nov-2001

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	373	375	370	379
175.vpr	1400	350	400	325	430
176.gcc	1100	193	570	193	569
181.mcf	1800	520	346	524	343
186.crafty	1000	213	469	213	469
197.parser	1800	505	357	502	359
252.eon	1300	344	378	311	418
253.perlbmk	1800	456	395	419	429
254.gap	1100	488	226	488	226
255.vortex	1900	245	775	233	816
256.bzip2	1500	447	336	400	375
300.twolf	3000	657	457	631	475



Hardware

CPU:	550 MHz PA-8600
CPU MHz:	550
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	--
Primary Cache:	512KBI+1MBD (on chip)
Secondary Cache:	none
L3 Cache:	none
Other Cache:	None
Memory:	8GB
Disk Subsystem:	2x18 GB 10000RPM
Other Hardware:	--

Software

Operating System:	HP-UX B.11.0
Compiler:	HP92453-01 A.11.01.20 HP C Compiler B3913DB C.03.25 HP aC++ Compiler (S800)
File System:	HP-UX VxFS
System State:	Multi-user

Notes/Tuning Information

```
Portability flags:
175.vpr: -DSPEC_CPU2000 ; 176.gcc: -DHOST_WORDS_BIG_ENDIAN
181.mcf: -DWANT_STDC_PROTO ; 186.crafty: -DHP -DSPEC_CPU2000
197.parser: -DSPEC_CPU2000 ; 252.eon: -DFMAX_IS_DOUBLE -DNDEBUG
253.perlbmk: -DSPEC_CPU2000_HP ; 254.gap: -DSPEC_CPU2000_HP
-DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO
300.twolf: -DSPEC_CPU2000
Base flags:
 cc: +Oall +Ostatic_prediction -Wl,-aarchive +I/+P
c++: +O3 +Ostatic_prediction -Wl,-aarchive +I/+P

Peak flags:
Peak flags = base flags for 186.crafty and 254.gap
        all: +Ostaticprediction -Wl,-aarchive  +I/+P
   164.gzip: +Oall +Optrs_strongly_typed +Oinline_budget=800
             -Wl,/opt/langtools/lib/fastmem.o
    175.vpr: +Oall  +Optrs_strongly_typed +Owhole_program_mode
    176.gcc: +Oall +ESlit +ESfic
    181.mcf: +Oall +Opromote_indirect_calls
 186.crafty: basepeak
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
 197.parser: +Oall +Odataprefetch +ESfic +Oinline_budget=400
    252.eon: +noeh +O4 +Optrs_strongly_typed +Olibcalls
253.perlbmk: +Oall +ESlit +ESfic -Wl,/opt/langtools/lib/fastmem.o
    254.gap: basepeak
 255.vortex: +Oall +Oinline_budget=800 +Optrs_strongly_typed
             -Wl,/opt/langtools/lib/fastmem.o
  256.bzip2: +Oall +Odataprefetch +Optrs_strongly_typed
  300.twolf: +Oall +Optrs_strongly_typed
Kernel tunables:
vps_pagesize=4096, vps_ceiling=16384, maxdsiz=0X040000000, maxssiz=0X10000000
2nd processor disabled at boot - co cpu 1 off
Event Monitoring System disabled.
```