CINT2000 Result

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Advanced Micro Devices

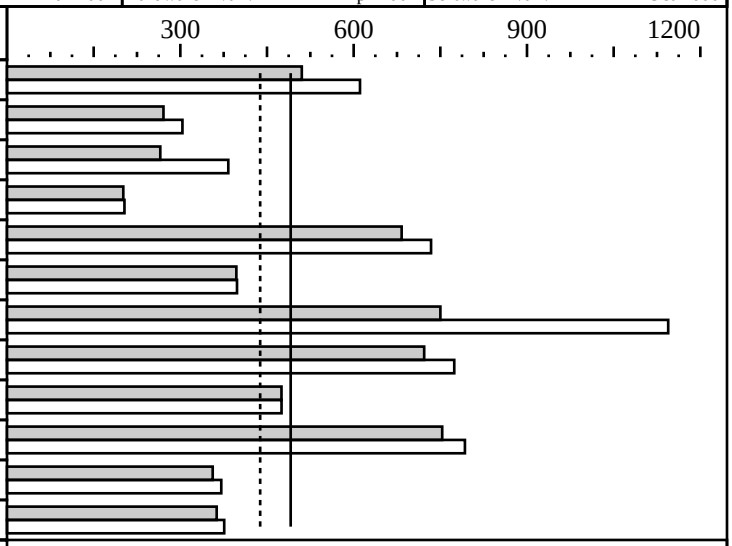
ASUS A7V Motherboard, 1.3GHz Athlon Processor

SPECint2000 = 491

SPECint_base2000 = 438

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Mar-2001 | Hardware Avail: Apr-2001 | Software Avail: Oct-2000

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	274	510	229	611
175.vpr	1400	517	271	461	304
176.gcc	1100	415	265	287	383
181.mcf	1800	894	201	885	203
186.crafty	1000	146	683	136	734
197.parser	1800	453	397	452	398
252.eon	1300	173	750	114	1144
253.perlbmk	1800	249	722	233	774
254.gap	1100	232	475	232	475
255.vortex	1900	252	753	240	793
256.bzip2	1500	421	356	404	371
300.twolf	3000	827	363	798	376



Hardware

CPU:	1.3GHz AMD Athlon processor A1300AMS3B
CPU MHz:	1300
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	256KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x128MB PC133 SDRAM CL2 2 banks per DIMM
Disk Subsystem:	WDC WD153BA
Other Hardware:	None

Software

Operating System:	Windows 2000
Compiler:	Intel C 5.0 Microsoft Visual Studio 6.0 (libraries) MicroQuill Smartheap Library 5.0
File System:	FAT32
System State:	Default

Notes/Tuning Information

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shlW32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
176.gcc:   -Dalloca=_alloca /F10000000
186.crafty:   -DNT_i386
253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap:    -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:   -QaxK -Qipo +FDO shlW32M.lib
Baseline C++: -QaxK -Qipo -GX -GR
Peak tuning:
164.gzip:     -Qxi -Qipo -Oi- +FDO
175.vpr:      -Qxi -QaxK -Qwp_ipo +FDO
176.gcc:      -QxiM -QaxK -Qipo -Oi- +FDO shlW32M.lib
181.mcf:      -Qxi -QaxK -Qwp_ipo +FDO
186.crafty:   -Qxi -Qwp_ipo +FDO
197.parser:   -Qxi -QaxK -Qwp_ipo +FDO
252.eon:      -QxiM -Qwp_ipo +FDO
253.perlbmk:  -QxiM -Qwp_ipo +FDO shlW32M.lib
254.gap:      -QaxK -Qwp_ipo +FDO shlW32M.lib
255.vortex:   -QxiM -Qwp_ipo -Oa -Oi- +FDO shlW32M.lib
```

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Notes/Tuning Information (Continued)

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 256.bzip2:    -Qxi -QaxK -Oa -Qwp_ipo
 300.twolf:    -Qxi -QaxK -Qwp_ipo -Oa +FDO shlW32M.lib
 ONESTEP is used for all base and peak runs
The tested system can be assembled using an ATX case such as the Antec KS-282,
a 300W power supply such as the Sparkle FSP300-60GT, and a PCI or AGP video card.
The System bus runs at 200MHz
```