



CFP2000 Result

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Fujitsu Siemens Computers
PRIMEPOWER600 (600MHz)

SPECfp_rate2000 = 17.4
SPECfp_rate_base2000 = 12.7

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Sep-2001 | Hardware Avail: Oct-2001 | Software Avail: Dec-2001

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	588	12.6	4	409	18.1
171.swim	4	1307	11.0	4	1214	11.8
172.mgrid	4	925	9.03	4	765	10.9
173.applu	4	1410	6.91	4	616	15.8
177.mesa	4	444	14.6	4	420	15.5
178.galgel	4	293	45.9	4	251	53.5
179.art	4	613	19.7	4	116	104
183.quake	4	998	6.05	4	542	11.1
187.facerec	4	455	19.4	4	374	23.6
188.amp	4	608	16.8	4	577	17.7
189.lucas	4	977	9.50	4	961	9.65
191.fma3d	4	1228	7.94	4	1128	8.64
200.sixtrack	4	447	11.4	4	395	12.9
301.apsi	4	833	14.5	4	820	14.7

Hardware

CPU: SPARC64 GP
CPU MHz: 600
FPU: Integrated
CPU(s) enabled: 4 cores, 4 chips, 1 core/chip
CPU(s) orderable: 2 to 8
Parallel: None
Primary Cache: 128KBI+128KBD on chip
Secondary Cache: 8MB(I+D) off chip, per CPU
L3 Cache: None
Other Cache: None
Memory: 8192MB
Disk Subsystem: 1 x 36.4GB SCSI (10000rpm)
Other Hardware: None

Software

Operating System: Solaris 8 7/01
Compiler: Fujitsu Parallelnavi 1.0.2
with patch 911403-01
Sun Forte Developer 6 update 2
File System: ufs
System State: single user

Notes/Tuning Information

```
FDO: (Parallelnavi 1.0.2)
fdo_pre0=rm -rf `pwd`/*.*.d
PASS1=-Kpg PASS2=-Kpu
FDO: (Forte Developer 6 update 2)
fdo_pre0=rm -rf `pwd`/../feedback.profile
PASS1=-xprofile=collect:`pwd`/../feedback
PASS2=-xprofile=use:`pwd`/../feedback
Baseline :
(using Fortran compiler of Parallelnavi 1.0.2)
-Kfast_GP=2,largepage -O4 -fs FDO

(using C compiler of Parallelnavi 1.0.2)
-Kfast_GP=2,largepage FDO

Peak:
(using Fortran compiler of Parallelnavi 1.0.2)
168.wupwise: -Kfast_GP=2,prefetch=4,nounroll,largepage -x dir=`pwd`/../../src -fs
```



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Notes/Tuning Information (Continued)

FDO ONESTEP=yes

171.swim: -Kfast_GP=2,GREG,preex,ilfunc,prefetch=3,largepage,commonpad=152,prefetch_iteration=3,unroll=2,nogs,frecipro
-O4 -fs

172.mgrid: -Kfast_GP=2,preex,GREG,commonpad=144,unroll=3,largepage,prefetch=3
-O4 -fs

178.galgel: -Kfast_GP=2,GREG,largepage,preex,unroll=2,prefetch_iteration=2,commonpad=24
-O4 -lssl2mtfma -fs FDO

RM_SOURCES=lapak.f90

189.lucas: -Kfast_GP=2,GREG,preex,largepage,nounroll -O4 -fs FDO

191.fma3d: -Kfast_GP=2,preex,GREG,nounroll,V9FMADD,prefetch=4 -O4 -fs FDO

200.sixtrack: -Kfast_GP=2,GREG,noprefetch,unroll=4,largepage,frecipro -fs

301.apsi: -Kfast_GP=2,GREG,preex,largepage,unroll=2 -O4 -fs FDO

(using C compiler of Parallelnavi 1.0.2)

188.ammp: -Kfast_GP=2,GREG,popt,prefetch=4,preex,preload,largepage,fuse,unroll=3 -x-

(using FORTRAN77 compiler of Forte Developer 6 update 2)

173.applu: -fast -Qoption iropt -whole,-Adata_access,-Mt6000,-Mm12000,-Mr40000,-Ma400 -xarch=v8plus -dn
ONESTEP=yes

(using FORTRAN90 compiler of Forte Developer 6 update 2)

187.facerec: -fast -xarch=v9 FDO ONESTEP=yes

(using C compiler of Forte Developer 6 update 2)

177.mesa: -fast -xcrossfile -xrestrict -xalias_level=std -xregs=syst -Wc,-Qgsched-trace_late=1,-Qgsched-trace_spec_load=1
-xarch=v8plus -W2,-Amemopt -dn

FDO ONESTEP=yes

179.art: -fast -xalias_level=strong -xdepend -xregs=syst -W2,-whole,-Amemopt
-xarch=v8plus -lmopt -lm -dn FDO ONESTEP=yes

183.quake: -fast -xalias_level=strong -xdepend -W2,-whole,-Amemopt
-xarch=v8plus -lmopt -lm FDO ONESTEP=yes

Portability:

(for Parallelnavi 1.0.2)

178.galgel: -Am -Fixed

187.facerec: -Am

191.fma3d: -Am

Note:

System Tunables: (for /etc/system)

consistent_coloring=1, tune_t_fsflushr=86400, autoup=86400,

shmsys:shminfo_shmmax=8589934592, shmsys:shminfo_shmmni=1024, shmsys:shminfo_shmseg=1024

(for /etc/opt/FJSPvnm/lpg.conf)

TSS=512M, SHMSEGSIZE=256M

Feedback directed optimization was used for all baseline benchmarks and peak benchmarks
except following peak benchmarks: 171.swim, 172.mgrid, 173.applu, 188.ammp, 200.sixtrack.