CINT2000 Result

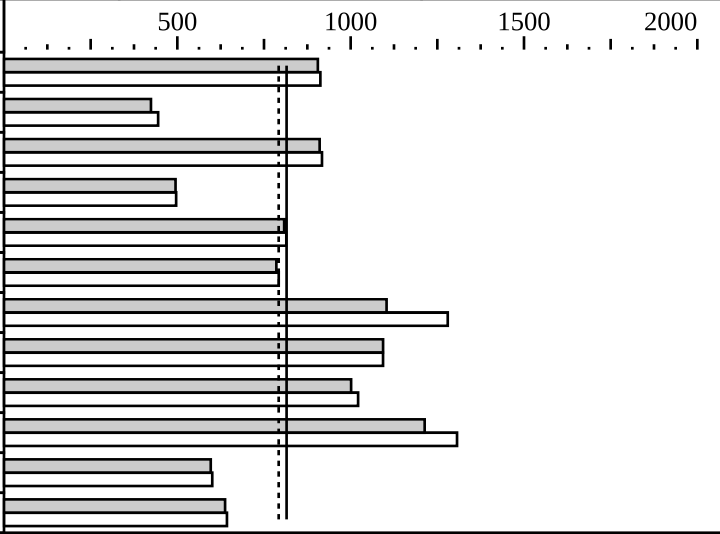
Dell
PowerEdge 2650 (2.4 GHz Xeon)

SPECint2000 = 815

SPECint_base2000 = 792

SPEC license #: 55	Tested by: Dell, Round Rock, TX	Test date: Mar-2002	Hardware Avail: May-2002	Software Avail: Jun-2001

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	155	905	153	913
175.vpr	1400	330	424	315	445
176.gcc	1100	121	911	120	917
181.mcf	1800	364	495	362	497
186.crafty	1000	124	808	123	814
197.parser	1800	229	786	227	792
252.eon	1300	118	1104	102	1280
253.perlbmk	1800	165	1094	165	1093
254.gap	1100	110	1001	108	1021
255.vortex	1900	157	1213	145	1307
256.bzip2	1500	252	596	250	601
300.twolf	3000	470	638	466	643



Hardware

CPU:	Intel Xeon
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1, 2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4GB PC1600 (DDR 200)
Disk Subsystem:	1 x 9.2GB Quantum Atlas 10K II U160
Other Hardware:	

Software

Operating System:	Windows 2000 Professional (SP2)
Compiler:	Intel C++ Compiler 5.0.1 (010922Z) Microsoft Visual C++ 6.0 (SP5) MicroQuill SmartHeap Library 5.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK-DIRECTED OPTIMIZATION
  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use
BASE TUNING
  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR
PEAK TUNING
  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
```

Notes/Tuning Information (Continued)

```
      255.vortex:  -Qwp_ipo +FDO -QxW -Oa
      256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
      300.twolf:   -Qipo +FDO -QxW shlW32M.lib
    EXTRA LIBRARIES
      shlW32M.lib: MicroQuill SmartHeap Library 5.0
                   www.microquill.com
   1 CPU configuration achieved by editing boot.ini
```