CINT2000 Result

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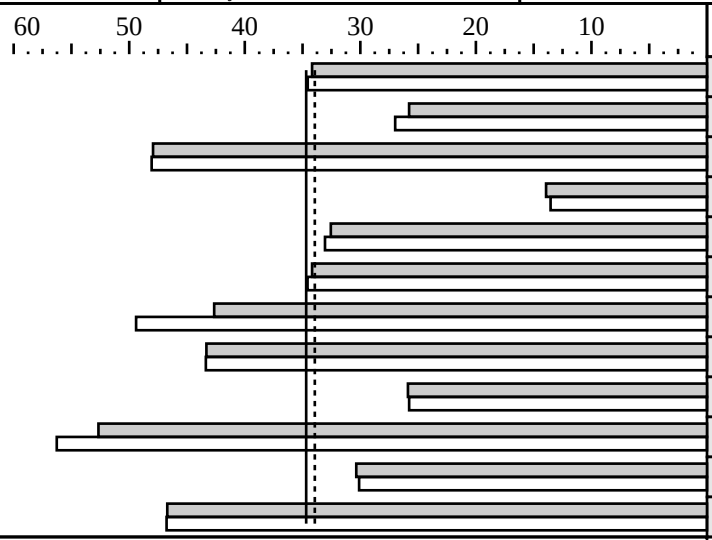
Dell
PowerEdge 6650 (2.0 GHz Xeon MP)

SPECint_rate2000 = 34.7

SPECint_rate_base2000 = 33.9

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Sep-2002 | Hardware Avail: Nov-2002 | Software Avail: Jun-2001



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	190	34.2	4	188	34.6
175.vpr	4	252	25.8	4	241	27.0
176.gcc	4	106	47.9	4	106	48.1
181.mcf	4	600	13.9	4	617	13.5
186.crafty	4	143	32.6	4	140	33.1
197.parser	4	244	34.2	4	242	34.6
252.eon	4	141	42.6	4	122	49.4
253.perlbmk	4	193	43.3	4	193	43.4
254.gap	4	197	25.9	4	198	25.8
255.vortex	4	167	52.7	4	157	56.3
256.bzip2	4	229	30.4	4	231	30.1
300.twolf	4	298	46.7	4	298	46.8

Hardware

CPU:	Intel Xeon (400 MHz system bus)
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 4 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1, 2, 4
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	2048KB(I+D) on chip
Other Cache:	None
Memory:	8 x 512MB DDR200
Disk Subsystem:	1 x 36GB SEAGATE ST336706LC
Other Hardware:	None

Software

Operating System:	Windows 2000 Advanced Server (SP2)
Compiler:	Intel C++ Compiler 5.0.1 (020125Z) Microsoft Visual C++ 6.0 (SP5) MicroQuill SmartHeap Library 5.0
File System:	NTFS
System State:	Hyper-Threading Technology disabled in BIOS

Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK-DIRECTED OPTIMIZATION
  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use
BASE TUNING
  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR
PEAK TUNING
  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
```

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Notes/Tuning Information (Continued)

```
    255.vortex:  -Qwp_ipo +FDO -QxW -Oa
    256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
    300.twolf:   -Qipo +FDO -QxW shlW32M.lib
  EXTRA LIBRARIES
    shlW32M.lib: MicroQuill SmartHeap Library 5.0
                 www.microquill.com
```