CINT2000 Result

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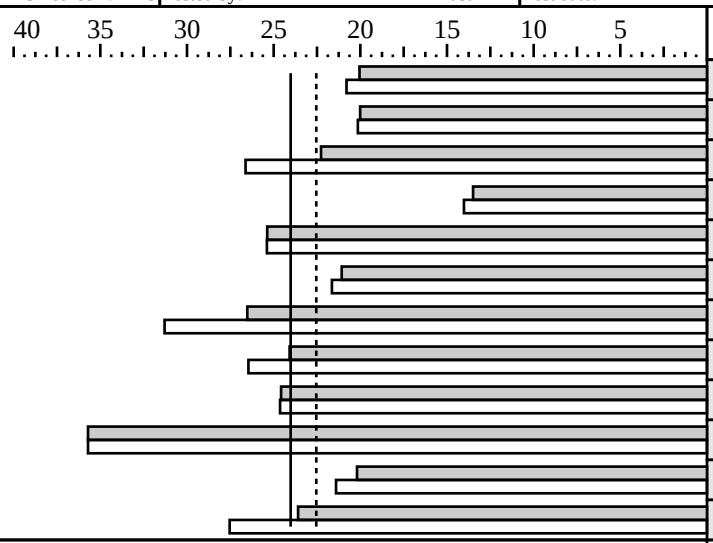
Einux
A4800

SPECint_rate2000 = 24.0

SPECint_rate_base2000 = 22.5

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Apr-2003 | Hardware Avail: Jul-2003 | Software Avail: May-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	162	20.0	2	156	20.8
175.vpr	2	162	20.0	2	161	20.1
176.gcc	2	115	22.3	2	95.9	26.6
181.mcf	2	309	13.5	2	298	14.0
186.crafty	2	91.4	25.4	2	91.4	25.4
197.parser	2	198	21.1	2	193	21.6
252.eon	2	114	26.5	2	96.4	31.3
253.perlbmk	2	173	24.1	2	158	26.5
254.gap	2	104	24.6	2	104	24.6
255.vortex	2	123	35.7	2	123	35.7
256.bzip2	2	172	20.2	2	163	21.4
300.twolf	2	295	23.6	2	253	27.5

Hardware

CPU:	AMD Opteron 242, 1.6 GHz
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip
CPU(s) orderable:	1,2,4
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x512MB PC2700 DDR ECC Registered SDRAM CL2.5
Disk Subsystem:	IDE 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition
Compiler:	Intel C/C++ 7.0 build 20021212Z Microsoft Visual Studio .NET (libraries)7.0.9466 MicroQuill Smartheap Library 6.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
shlW32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
  176.gcc:      -Dalloca=_alloca /F10000000
  186.crafty:   -DNT_i386
  253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:           +FD0 -O3 -QxW  -Qipo
Baseline C++:         +FD0           -Qipo -GX  -GR
Peak tuning:
  164.gzip:           +FD0 -O3 -QaxK -Qipo -Oi-
  175.vpr:            +FD0 -O3 -QxW  -Qipo
  176.gcc:            +FD0 -O3 -QxK  -Qipo -Oi-
  181.mcf:            +FD0           -Qipo -Oa
  186.crafty:         +FD0 -O3 -QxW  -Qipo
  197.parser:         +FD0 -O3 -QxW  -Qipo -Oa
  252.eon:            +FD0 -O3 -QaxW -Qipo -Zp4
  253.perlbmk:        +FD0 -O3       -Qipo -Oa  shlW32M6.lib
  254.gap:            +FD0 -O3 -QxW  -Qipo
  255.vortex:         basepeak=1
```

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Notes/Tuning Information (Continued)

```
  256.bzip2:          +FD0           -Qipo -Oa  -Qunroll1
  300.twolf:          +FD0     -Qxi  -Qipo      shlW32M6.lib
ONESTEP is used for all base and peak runs
```