CINT2000 Result

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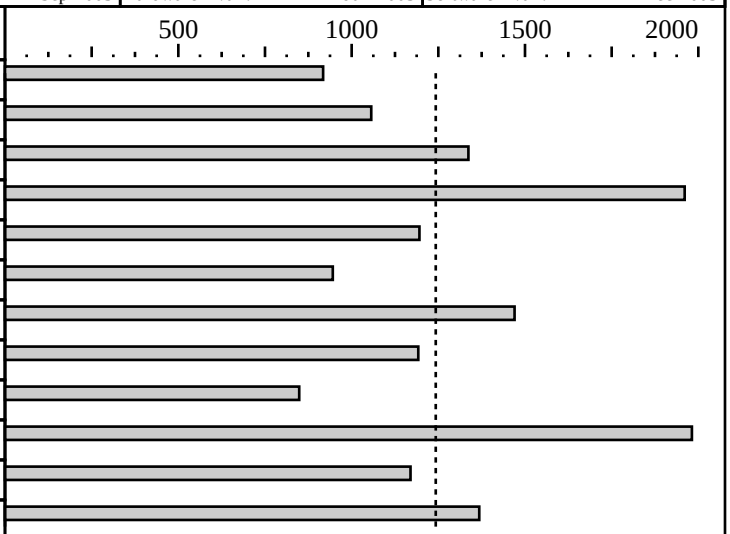
SGI
SGI Altix 3000 (1500MHz, Itanium 2)

SPECint2000 = --

SPECint_base2000 = 1243

SPEC license #: 4 | Tested by: SGI | Test date: Sep-2003 | Hardware Avail: Jun-2003 | Software Avail: Dec-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	153	918		
175.vpr	1400	133	1056		
176.gcc	1100	82.3	1337		
181.mcf	1800	91.8	1961		
186.crafty	1000	83.6	1195		
197.parser	1800	190	946		
252.eon	1300	88.4	1470		
253.perlbmk	1800	151	1192		
254.gap	1100	130	849		
255.vortex	1900	95.9	1981		
256.bzip2	1500	128	1170		
300.twolf	3000	219	1368		



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1500
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	6.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	4 GB
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.2
Compiler:	Intel(R) C++ Compiler for Linux 8.0 (Build 20030823) MicroQuill SmartHeap Library 7.01 (www.microquill.com)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

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+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -fast -auto_ilp32 +FDO
  C++ programs:     -fast -ansi_alias +FDO
  Extra Libraries:  libsmartheap64.a

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Process was bound to CPU using runon.
Extra CPUs were disabled at firmware.
```