



CINT2000 Result

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Advanced Micro Devices
ASUS SK8N Motherboard, AMD Athlon (TM) FX-51

SPECint2000 = 1447
SPECint_base2000 = 1376

SPEC license #: 49 Tested by: AMD, Austin, TX Test date: Sep-2003 Hardware Avail: Sep-2003 Software Avail: Feb-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	116	1203	113	1236	
175.vpr	1400	120	1170	120	1170	
176.gcc	1100	77.1	1427	69.2	1591	
181.mcf	1800	191	944	191	940	
186.crafty	1000	66.4	1505	66.5	1504	
197.parser	1800	134	1345	134	1347	
252.eon	1300	81.9	1588	69.3	1875	
253.perlbnk	1800	124	1457	114	1579	
254.gap	1100	71.3	1542	71.4	1542	
255.vortex	1900	88.5	2147	88.5	2147	
256.bzip2	1500	131	1144	123	1215	
300.twolf	3000	220	1366	186	1616	

Hardware

CPU: AMD Athlon (TM) FX-51 ADAFX51CE5AK
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 2x512MB PC3200 DDR SDRAM CL2.5, ECC Registered
Disk Subsystem: Western Digital WD360GD, SATA, 10k rpm
Other Hardware: None

Software

Operating System: Microsoft Windows XP Professional Service Pack 1.0a
Compiler: Intel C/C++ 7.0 build 20021212Z
Microsoft Visual Studio .NET 7.0.9466(For libraries)
MicroQuill Smartheap Library 6.0
File System: NTFS
System State: Default

Notes/Tuning Information

shlw32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FD0: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C:

+FD0 -O3 -QxW -Qipo

Baseline C++:

+FD0 -Qipo -GX -GR

Peak tuning:

164.gzip: +FD0 -O3 -QaxK -Qipo -Oi-
175.vpr: +FD0 -O3 -QxW -Qipo
176.gcc: +FD0 -O3 -QxK -Qipo -Oi-
181.mcf: +FD0 -Qipo -Oa
186.crafty: +FD0 -O3 -QxW -Qipo
197.parser: +FD0 -O3 -QxW -Qipo -Oa
252.eon: +FD0 -O3 -QaxW -Qipo -Zp4
253.perlbnk: +FD0 -O3 -Qipo -Oa shlw32M6.lib
254.gap: +FD0 -O3 -QxW -Qipo
255.vortex: basepeak=1



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Notes/Tuning Information (Continued)

256.bzip2: +FD0 -Qipo -Oa -Qunroll1
300.twolf: +FD0 -Qxi -Qipo shlw32M6.lib

ONESTEP is used for all base and peak runs

The tested system can be assembled using an ATX case such as the Antec KS-282,
a 460W power supply such as the Antec True 460
ECC disabled