CINT2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Dell

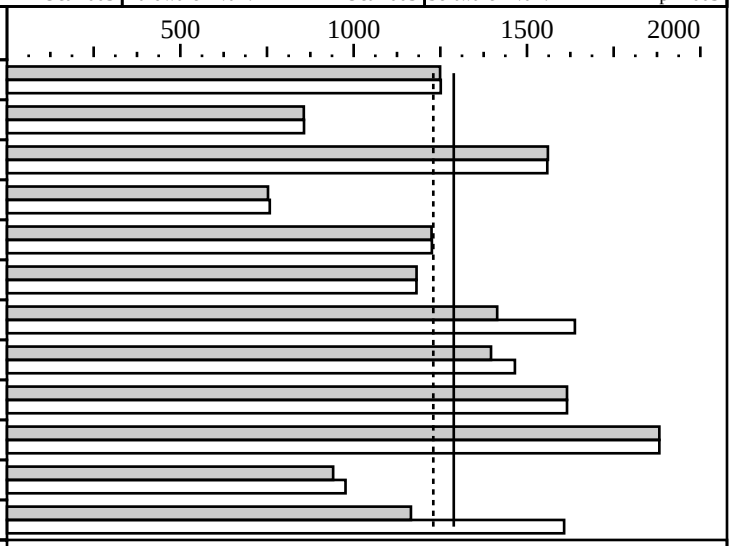
PowerEdge 1750 (3.2 GHz Xeon, 1MB L3)

SPECint2000 = 1289

SPECint_base2000 = 1230

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Oct-2003 | Hardware Avail: Oct-2003 | Software Avail: Apr-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	112	1249	112	1251
175.vpr	1400	164	856	163	857
176.gcc	1100	70.5	1560	70.6	1559
181.mcf	1800	239	753	237	758
186.crafty	1000	81.7	1225	81.6	1226
197.parser	1800	152	1181	152	1181
252.eon	1300	91.9	1414	79.4	1638
253.perlbmk	1800	129	1396	123	1465
254.gap	1100	68.1	1615	68.1	1615
255.vortex	1900	101	1882	101	1882
256.bzip2	1500	159	941	154	977
300.twolf	3000	257	1165	187	1607



Hardware

CPU:	Intel Pentium Xeon (533 MHz system bus)
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	1024KB(I+D) on chip
Other Cache:	N/A
Memory:	4 X 512MB DDR266
Disk Subsystem:	1 x 17GB Ultra3 Seagate SCSI
Other Hardware:	

Software

Operating System:	Windows 2000 Server (SP2)
Compiler:	Intel C++ Compiler 7.1 (20030402Z) Microsoft Visual Studio .NET (7.0.9466) MicroQuill SmartHeap Library 6.01
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafy:   -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK DIRECTED OPTIMIZATION
  FDO:          PASS1=  -Qprof_gen  PASS2=-Qprof_use
BASE TUNING
  C:           -QxW -Qipo -O3 +FDO
  C++:         -QxW -Qipo -GX -GR
PEAK TUNING
  164.gzip:    -QxW -Qipo -Oa -O3 +FDO
  175.vpr:     -QxW -Qipo     -O3 +FDO
  176.gcc:     -QxW -Qipo     -O3 +FDO
  181.mcf:     -QxW -Qipo     -O3 +FDO shlW32M.lib
  186.crafty:  PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
  197.parser:  -QxW -Qipo     -O3 +FDO
  252.eon:     PASS1= -Qprof_gen PASS2= -Qipo -Qprof_use -QxW -O3
  253.perlbmk: -QxW -Qipo     -O3 +FDO shlW32M.lib
  254.gap:     basepeak=yes
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
  255.vortex   basepeak=yes
  256.bzip2:   -Qipo -Oa -Qunroll1 +FDO
  300.twolf:   -Qipo -QxW -O3 -Oa      shlW32M.lib
EXTRA LIBRARIES
  shlW32M.lib: MicroQuill SmartHeap Library 6.01
               www.microquill.com
ONESTEP=YES is set for all benchmarks
Hyper-Threading Technology disabled in BIOS
```