CFP2000 Result

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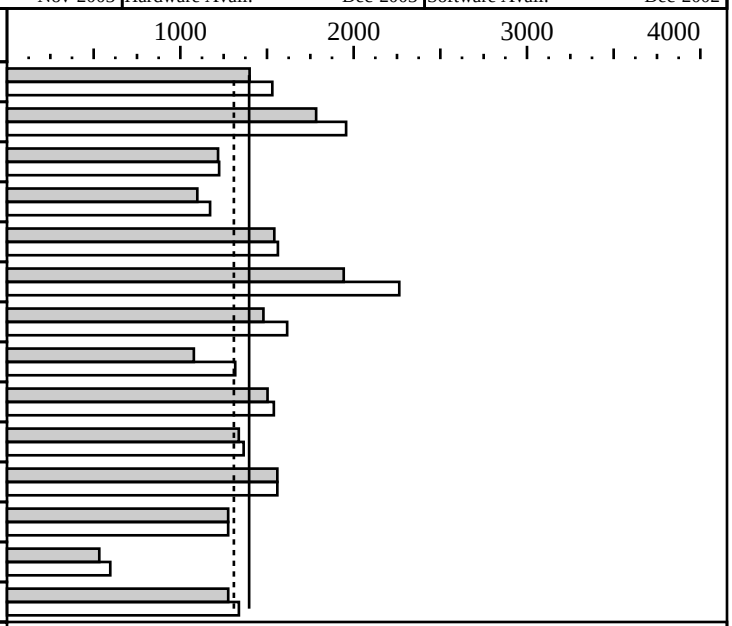
Fujitsu Siemens Computers
CELSIUS V810

SPECfp2000 = 1397

SPECfp_base2000 = 1309

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2003 | Hardware Avail: Dec-2003 | Software Avail: Dec-2002

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	114	1400	105	1531
171.swim	3100	174	1784	158	1956
172.mgrid	1800	148	1218	147	1224
173.applu	2100	191	1098	179	1172
177.mesa	1400	90.8	1542	89.6	1563
178.galgel	2900	149	1943	128	2263
179.art	2600	176	1479	161	1616
183.equake	1300	121	1078	98.7	1317
187.facerec	1900	126	1503	123	1539
188.ammp	2200	165	1337	161	1365
189.lucas	2000	128	1560	128	1560
191.fma3d	2100	165	1276	165	1276
200.sixtrack	1100	207	532	185	596
301.apsi	2600	204	1276	194	1338



Hardware

CPU:	AMD Opteron (TM) 248 (2.2 GHz)
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x1GB PC2700 DDR SDRAM CL2.5, ECC Registered
Disk Subsystem:	ATA-100
Other Hardware:	None

Software

Operating System:	Windows XP Pro SP1
Compiler:	Intel C/C++ 7.0 build 20021212Z and Intel Fortran 7.0 build 20021212Z Compaq Visual Fortran Compiler Version 6.6 (Update B) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 6.0
File System:	FAT32
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
icl and ifl are the Intel C/C++ and Fortran compilers
f90 is the Compaq Fortran compiler
shlW32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
Portability:
  178.galgel: -FI -Fe$@ -link -stack:32000000
Baseline: C       icl +FDO -O3 -QxW  -Qipo
Peak tuning:
Baseline: Fortran ifl +FDO -O3 -QxW  -Qipo
  168.wupwise:    ifl +FDO     -QxK  -Qipo -Ow
  171.swim:       f90 -Optimize:5 -alignment:dcommons -alignment:records
                      -alignment:sequence -architecture:k7
                      -assume:noaccuracy_sensitive -math_library:fast -tune:k7
  172.mgrid:      ifl +FDO -O3 -QaxW -Qipo -Oa -Qprefetch-
  173.applu:      ifl +FDO -O3 -QxK  -Qipo     -Qscalar_rep-           -Zp8
  177.mesa:       icl +FDO -O3 -QxW  -Qipo -Oa -Qscalar_rep-
  178.galgel:     f90 -Optimize:5 -fast
```

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Notes/Tuning Information (Continued)

```
  179.art:        icl                -Qipo -Oa                -Qunroll4 -Zp4
  183.equake:     icl      -O3 -QxK  -Qipo -Oa shlW32M6.lib             -Zp4
  187.facerec:    ifl +FDO -O3 -QaxW -Qipo     -Qscalar_rep- -Qunroll1
  188.ammp:       icl           -QxW        -Oa
  189.lucas:      ifl +FDO -O3 -QxW  -Qipo     -Qprefetch-
  191.fma3d:      ifl basepeak=1
  200.sixtrack:   ifl                -Qipo -Oa                          -Zp4
  301.apsi:       f90 -Optimize:5 -fast
```

System tested with only one CPU present.
BIOS default settings have been used. These include:
ECC memory checking disabled,
node interleave disabled, SRAT enabled.

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