CINT2000 Result

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Hewlett-Packard Company

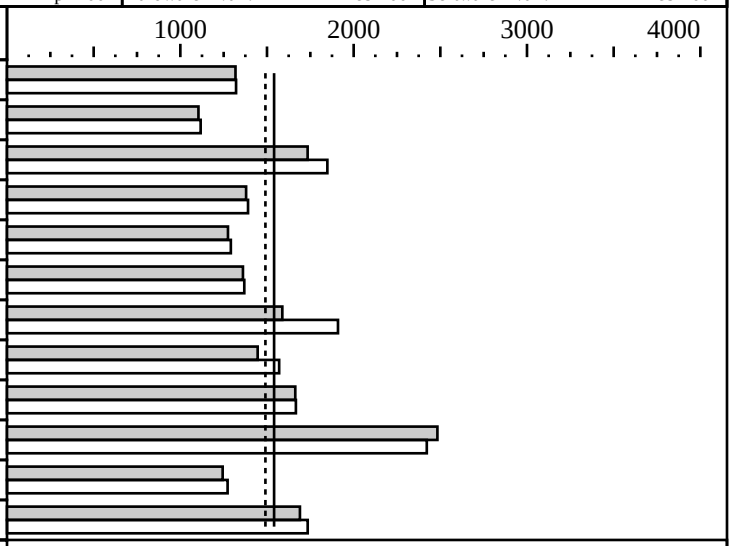
ProLiant DL360 G3 (3.2GHz/2MB L3 cache, Intel Xeon)

SPECint2000 = 1541

SPECint_base2000 = 1491

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Apr-2004 | Hardware Avail: Feb-2004 | Software Avail: Feb-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	106	1318	106	1322
175.vpr	1400	127	1105	125	1117
176.gcc	1100	63.4	1734	59.5	1848
181.mcf	1800	130	1379	129	1391
186.crafty	1000	78.4	1275	77.4	1291
197.parser	1800	132	1361	131	1369
252.eon	1300	81.8	1588	68.1	1910
253.perlbmk	1800	124	1446	115	1570
254.gap	1100	66.1	1663	66.0	1667
255.vortex	1900	76.5	2483	78.5	2422
256.bzip2	1500	121	1244	118	1273
300.twolf	3000	178	1690	173	1735



Hardware

CPU:	Intel Xeon (3.2GHz, 533MHz bus)
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K micro-ops I + 8KBD on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	2048KB(I+D) on chip
Other Cache:	N/A
Memory:	4x512MB PC2100
Disk Subsystem:	1x36.4GB 15K Ultra320 SCSI
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -QxN -Qipo -O3 +FDO ONESTEP=yes
Base tuning for C++ programs: -Qipo -GX -GR +FDO ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafy: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:       -QxN -Qipo -Oa -O3 +FDO ONESTEP=yes
  175.vpr:       -QxN -Qipo -O3 +FDO ONESTEP=yes
  176.gcc:       -QxN -Qipo -Oi- -O3 +FDO ONESTEP=yes
  181.mcf:       -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
  186.crafty:     -QxN -Qipo -Oa -O3 +FDO shlw32M.lib ONESTEP=yes
  197.parser:     -QxN -Qipo -O3 +FDO ONESTEP=yes
  252.eon:      -QxN -Qipo -O3 +FDO ONESTEP=yes
  253.perlbmk:    -Qipo -Oa -O3 +FDO shlw32M.lib ONESTEP=yes
  254.gap:      -QxN -Qipo -O3 +FDO ONESTEP=yes
  255.vortex:     -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
  256.bzip2:      -Qunroll1 -Oa -Qipo +FDO ONESTEP=yes
```

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Notes/Tuning Information (Continued)

```
  300.twolf:      -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
BIOS Configuration Notes
  HT Technology disabled
```