CINT2000 Result

Advanced Micro Devices

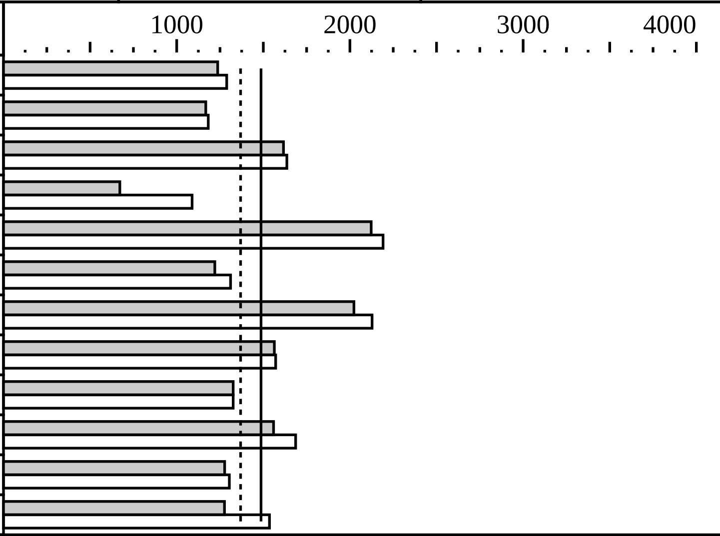
ASUS SK8V Motherboard, AMD Opteron (TM) 150

SPECint2000 = 1487

SPECint_base2000 = 1369

SPEC license #: 49	Tested by: AMD, Austin, TX	Test date: Apr-2004	Hardware Avail: Jun-2004	Software Avail: Apr-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	113	1237	109	1289
175.vpr	1400	120	1168	118	1182
176.gcc	1100	68.0	1616	67.2	1636
181.mcf	1800	268	672	165	1089
186.crafty	1000	47.1	2123	45.6	2191
197.parser	1800	148	1220	137	1311
252.eon	1300	64.3	2023	61.1	2128
253.perlbmk	1800	115	1564	115	1571
254.gap	1100	83.0	1326	83.0	1326
255.vortex	1900	122	1559	113	1687
256.bzip2	1500	117	1277	115	1304
300.twolf	3000	235	1276	195	1535



Hardware

CPU:	AMD Opteron (TM) 150
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x512 MB PC3200 DDR SDRAM ECC Reg CL2
Disk Subsystem:	IDE, Western Digital WD2000
Other Hardware:	None

Software

Operating System:	SuSE Linux 8.0 SLES 64 bit (SP3)
Compiler:	SuSE optional gcc 3.3 compiler (20030312) (from SLES8 SP3)
File System:	Linux/ext3
System State:	Multi-user SuSE Run level 3

Notes/Tuning Information

```
+FDO: PASS1=-fprofile-arcs PASS2=-fbranch-probabilities
Portability:
              PORTABILITY=-DSPEC_CPU2000_LP64 is applied to all benchmarks,
              except for peak runs of 181.mcf, 197.parser and 300.twolf, which
              are compiled for 32-bit in peak.
 186.crafty:  CPORTABILITY=-DLINUX_i386
 176.gcc:     Approved src.alt is used
 252.eon:     CXXPORTABILITY=-DHAS_ERRLIST -DFMAX_IS_DOUBLE
              Approved src.alt is used.
 253.perlbmk: CPORTABILITY=-DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL
                           -DSPEC_CPU2000_GLIBC22
 254.gap:     CPORTABILITY=-DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO
                         -DSYS_HAS_SIGNAL_PROTO -DSYS_HAS_ANSI -DSYS_HAS_CALLOC_PROTO
Baseline C:   gcc -O3 -funroll-all-loops +FDO
Baseline C++: g++ -O3 -funroll-all-loops +FDO
Peak tuning:
 164.gzip:    -O3 -funroll-all-loops -finline-limit=900 -freduce-all-givs +FDO
 175.vpr:     -O3 -funroll-loops -finline-limit=1000 +FDO
 176.gcc:     -O3 -funroll-all-loops -finline-limit=900 +FDO
 181.mcf:     -O3 -funroll-all-loops -m32 +FDO
```

CINT2000 Result

Notes/Tuning Information (Continued)

```
 186.crafty:  -O3 -funroll-loops -fprefetch-loop-arrays
 197.parser:  -O3 -funroll-all-loops -m32 +FDO
 252.eon:     -O3 -funroll-all-loops -ffast-math -finline-limit=3000 +FDO
 253.perlbmk: -O3 -funroll-all-loops -finline-limit=1800 +FDO
 254.gap:     basepeak=true
 255.vortex:  -O3 -funroll-all-loops -finline-limit=1000 +FDO
 256.bzip2:   -O3 -funroll-all-loops -freduce-all-givs -finline-limit=2700 +FDO
 300.twolf:   -O3 -funroll-all-loops -finline-limit=2000 -m32 +FDO
ONESTEP is used for all base and peak runs
BIOS rev.: 1001
Memory used is Corsair CMX512R-3200C2
Cas Latency manually set to 2.0 in BIOS
```