CINT2000 Result

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Hewlett-Packard Company

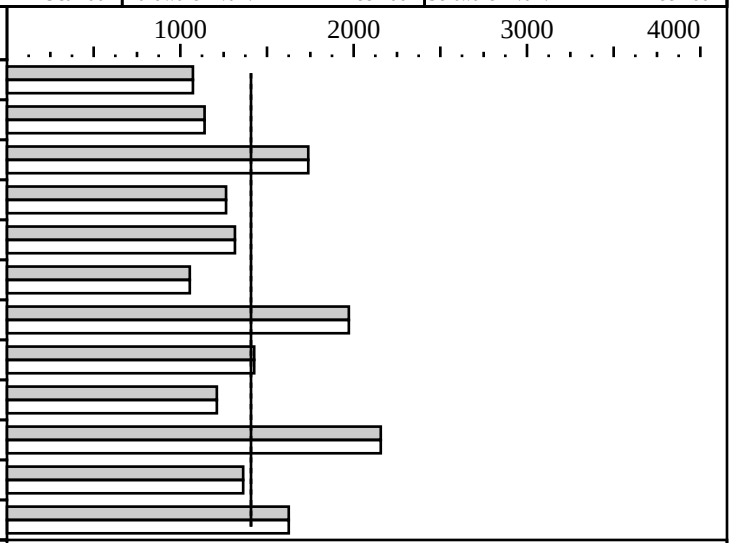
HP Integrity rx2620-2 (1.6GHz/3MB Itanium 2)

SPECint2000 = 1408

SPECint_base2000 = 1408

SPEC license #: 3 | Tested by: HP Richardson | Test date: Oct-2004 | Hardware Avail: Dec-2004 | Software Avail: Dec-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	131	1073	131	1073
175.vpr	1400	123	1140	123	1140
176.gcc	1100	63.3	1738	63.3	1738
181.mcf	1800	142	1264	142	1264
186.crafty	1000	76.0	1315	76.0	1315
197.parser	1800	171	1055	171	1055
252.eon	1300	65.9	1972	65.9	1972
253.perlbmk	1800	126	1426	126	1426
254.gap	1100	90.9	1211	90.9	1211
255.vortex	1900	88.1	2156	88.1	2156
256.bzip2	1500	110	1363	110	1363
300.twolf	3000	185	1625	185	1625



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	no
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB (I+D) on chip, per core
L3 Cache:	3.0MB (I+D) on chip, per core
Other Cache:	N/A
Memory:	24GB (12x2GB DIMMs)
Disk Subsystem:	1x146GB 10K SCSI
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23.0409
Compiler:	HP C/ANSI C Compiler C.06.00 HP aC++ Compiler C.06.00
File System:	vxfs
System State:	Multi-user

Notes/Tuning Information

```
Portability Flags
176.gcc     : -DHOST_WORDS_BIG_ENDIAN
186.crafty  : -DHP
252.eon     : -DFMAX_IS_DOUBLE
253.perlbmk : -DSPEC_CPU2000_HP
254.gap     : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
  all       : +Oprofile=collect:all/+Oprofile=use
  C         : +Ofaster -exec +Otype_safety=ansi
  C++       : +Ofaster +Otype_safety=ansi

Peak Flags
  same as base (basepeak=true set globally)

Kernel Tunables:
  dbc_max_pct=20
  dbc_min_pct=20
  maxdsiz=3221225472
```

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Notes/Tuning Information (Continued)

```
  maxssiz=401604608
  maxdsiz_64bit=4396972761584
  maxssiz_64bit=1073741824
  vps_pagesize=4096
  vps_ceiling=16384

Notes:

The system under test had the HP-UX 11i v2 (version 11.23.0409,
  September, 2004) Technical Computing Operating Environment and
  AR1204 compilers installed, along with the following patches:

  PHSS_31849 linker + fdp cumulative patch
  PHSS_31850 assembler patch
  PHSS_31851 Integrity Unwind Library
  PHSS_31852 aC++ Runtime (IA: A.05.60, PA A.03.60)
  PHSS_31853 Math Library Cumulative Patch
  PHSS_31854 milli cumulative patch

EFI command "cpuconfig" used prior to booting to
  deconfigure other processors if necessary
```