# CINT2000 Result

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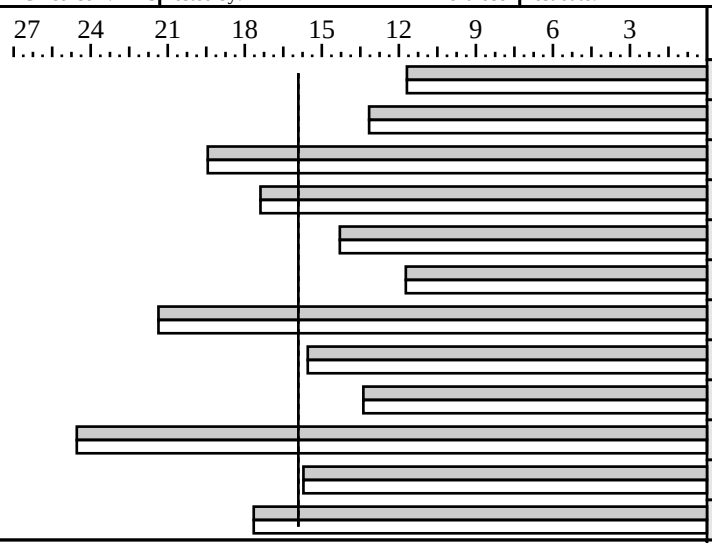
## Hewlett-Packard Company

HP Integrity rx4640-8 (1.5GHz/4MB Itanium 2)

SPECint_rate2000 = 15.9

SPECint_rate_base2000 = 15.9

SPEC license #: 3 | Tested by: HP Richardson | Test date: Nov-2004 | Hardware Avail: Dec-2004 | Software Avail: Dec-2004




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 1 | 139 | 11.7 | 1 | 139 | 11.7 |
| 175.vpr | 1 | 123 | 13.2 | 1 | 123 | 13.2 |
| 176.gcc | 1 | 65.6 | 19.4 | 1 | 65.6 | 19.4 |
| 181.mcf | 1 | 120 | 17.4 | 1 | 120 | 17.4 |
| 186.crafty | 1 | 81.1 | 14.3 | 1 | 81.1 | 14.3 |
| 197.parser | 1 | 178 | 11.7 | 1 | 178 | 11.7 |
| 252.eon | 1 | 70.6 | 21.4 | 1 | 70.6 | 21.4 |
| 253.perlbmk | 1 | 134 | 15.5 | 1 | 134 | 15.5 |
| 254.gap | 1 | 95.4 | 13.4 | 1 | 95.4 | 13.4 |
| 255.vortex | 1 | 89.8 | 24.5 | 1 | 89.8 | 24.5 |
| 256.bzip2 | 1 | 111 | 15.7 | 1 | 111 | 15.7 |
| 300.twolf | 1 | 197 | 17.7 | 1 | 197 | 17.7 |

### Hardware

| | |
|---|---|
| CPU: | Intel Itanium 2 |
| CPU MHz: | 1500 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip |
| CPU(s) orderable: | 1-4 |
| Parallel: | no |
| Primary Cache: | 16KBI + 16KBD on chip, per core |
| Secondary Cache: | 256KB (I+D) on chip, per core |
| L3 Cache: | 4.0MB (I+D) on chip, per core |
| Other Cache: | N/A |
| Memory: | 32GB (16x2GB DIMMs) |
| Disk Subsystem: | 1x36GB 15K SCSI |
| Other Hardware: | N/A |

### Software

| | |
|---|---|
| Operating System: | HPUX11i-TCOE B.11.23.0409 |
| Compiler: | HP C/ANSI C Compiler C.06.00<br>HP aC++ Compiler C.06.00 |
| File System: | vxfs |
| System State: | Multi-user |

## Notes/Tuning Information

```
Portability Flags
176.gcc     : -DHOST_WORDS_BIG_ENDIAN
186.crafty  : -DHP
252.eon     : -DFMAX_IS_DOUBLE
253.perlbmk : -DSPEC_CPU2000_HP
254.gap     : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
  all       : +Oprofile=collect:all/+Oprofile=use
  C         : +Ofaster -exec +Otype_safety=ansi
  C++       : +Ofaster +Otype_safety=ansi

Peak Flags
  same as base (basepeak=true set globally)

Kernel Tunables:
  dbc_max_pct=20
  dbc_min_pct=20
  maxdsiz=3221225472
```

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## Notes/Tuning Information (Continued)

```
    maxssiz=401604608
    maxdsiz_64bit=4396972761584
    maxssiz_64bit=1073741824
    vps_pagesize=4096
    vps_ceiling=16384

  Notes:

  The system under test had the HP-UX 11i v2 (version 11.23.0409,
    September, 2004) Technical Computing Operating Environment and
    AR1204 compilers installed, along with the following patches:

    PHSS_31849 linker + fdp cumulative patch
    PHSS_31850 assembler patch
    PHSS_31851 Integrity Unwind Library
    PHSS_31852 aC++ Runtime (IA: A.05.60, PA A.03.60)
    PHSS_31853 Math Library Cumulative Patch
    PHSS_31854 milli cumulative patch

  EFI command "cpuconfig" used prior to booting to
    deconfigure other processors if necessary
```