CINT2000 Result

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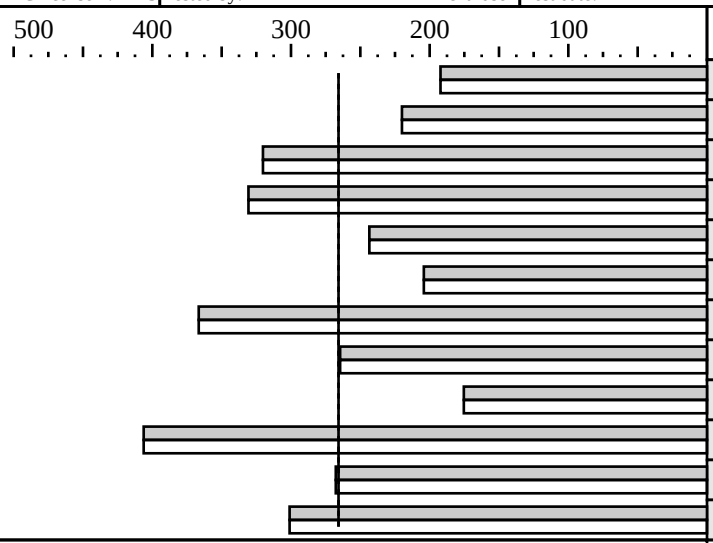
Hewlett-Packard Company

HP Integrity rx8620-32 (1.6GHz/6MB Itanium 2, 4 cells)

SPECint_rate2000 = 266

SPECint_rate_base2000 = 266

SPEC license #: 3 | Tested by: HP Richardson | Test date: Jan-2005 | Hardware Avail: Jan-2005 | Software Avail: Jan-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	16	135	192	16	135	192
175.vpr	16	118	220	16	118	220
176.gcc	16	63.8	320	16	63.8	320
181.mcf	16	101	331	16	101	331
186.crafty	16	76.2	243	16	76.2	243
197.parser	16	164	204	16	164	204
252.eon	16	65.8	367	16	65.8	367
253.perlbmk	16	126	265	16	126	265
254.gap	16	116	175	16	116	175
255.vortex	16	86.8	406	16	86.8	406
256.bzip2	16	104	268	16	104	268
300.twolf	16	185	301	16	185	301

Hardware

CPU:	Intel Itanium 2 (1.6GHz/6MB, 400MHz FSB)
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	16 cores, 16 chips, 1 core/chip
CPU(s) orderable:	2 to 16 by 2
Parallel:	no
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB (I+D) on chip, per core
L3 Cache:	6.0MB (I+D) on chip, per core
Other Cache:	N/A
Memory:	32GB (64x512MB DIMMs)
Disk Subsystem:	4x36GB 15K SCSI (striped)
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23.0409
Compiler:	HP C/ANSI C Compiler C.06.00 HP aC++ Compiler C.06.00
File System:	vxfs
System State:	Multi-user

Notes/Tuning Information

```
Portability Flags
176.gcc     : -DHOST_WORDS_BIG_ENDIAN
186.crafty  : -DHP
252.eon     : -DFMAX_IS_DOUBLE
253.perlbmk : -DSPEC_CPU2000_HP
254.gap     : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
  all       : +Oprofile=collect:all/+Oprofile=use
  C         : +Ofaster -exec +Otype_safety=ansi
  C++       : +Ofaster +Otype_safety=ansi

Peak Flags
  same as base (basepeak=true set globally)

Kernel Tunables:
  dbc_max_pct=20
  dbc_min_pct=20
  maxdsiz=3221225472
```

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Notes/Tuning Information (Continued)

```
    maxssiz=401604608
    maxdsiz_64bit=4396972761584
    maxssiz_64bit=1073741824
    vps_pagesize=4096
    vps_ceiling=16384

  Notes:

  The system under test had the HP-UX 11i v2 (version 11.23.0409,
    September, 2004) Technical Computing Operating Environment and
    AR1204 compilers installed, along with the following patches:

    PHSS_31849 linker + fdp cumulative patch
    PHSS_31850 assembler patch
    PHSS_31851 Integrity Unwind Library
    PHSS_31852 aC++ Runtime (IA: A.05.60, PA A.03.60)
    PHSS_31853 Math Library Cumulative Patch
    PHSS_31854 milli cumulative patch

  System was configured with 1/2 of memory interleaved and
    1/2 of memory local to each cell

  System configured as a single partition with 4 cells and
    4 processors per cell

  Filesystem used for SPEC runs mounted "tmplog"

  Processes were assigned to localities using the
    HP-UX mpsched utility
```