CINT2000 Result

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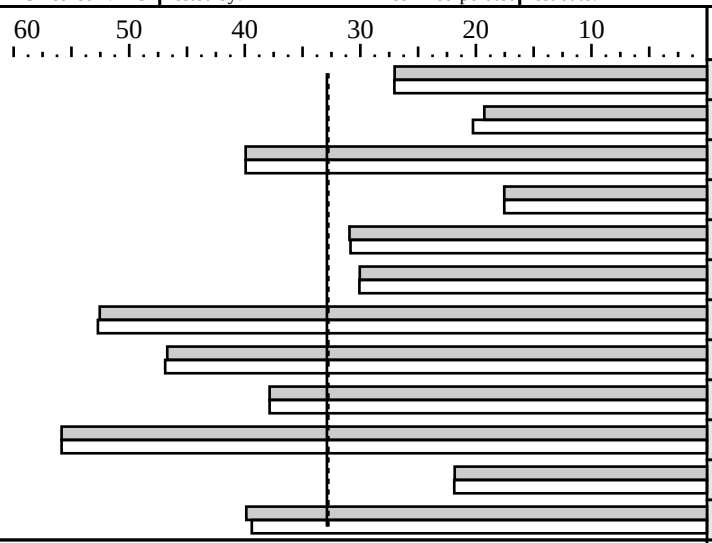
Acer Incorporated
Altos R510 (3.6 GHz Intel Xeon)

SPECint_rate2000 = 32.9

SPECint_rate_base2000 = 32.8

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Mar-2005 | Hardware Avail: Mar-2005 | Software Avail: Mar-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	120	27.0	2	120	27.1
175.vpr	2	169	19.3	2	160	20.3
176.gcc	2	63.9	39.9	2	63.9	39.9
181.mcf	2	238	17.5	2	238	17.5
186.crafty	2	75.0	30.9	2	75.2	30.8
197.parser	2	139	30.1	2	139	30.1
252.eon	2	57.4	52.6	2	57.2	52.7
253.perlbmk	2	89.4	46.7	2	89.1	46.9
254.gap	2	67.4	37.9	2	67.4	37.9
255.vortex	2	78.9	55.9	2	78.9	55.9
256.bzip2	2	159	21.8	2	159	21.9
300.twolf	2	175	39.9	2	177	39.4

Hardware

CPU:	Intel Xeon (3.6 GHz, 1MB L2, 800MHz system bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	6x512MB DDR-333 ECC SDRAM
Disk Subsystem:	1 x 80GB 7200RPM SATA HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler:	Intel C++ Compiler 8.1 Build 20041019Z Microsoft Visual Studio .Net 2003(7.1.3091, for libraries) SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     -fast -Qansi_alias -Oa  +FDO
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   -fast -Qansi_alias -Oa +FDO
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast -O3 +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located as of Apr-2005:
http://www.acer.com
```